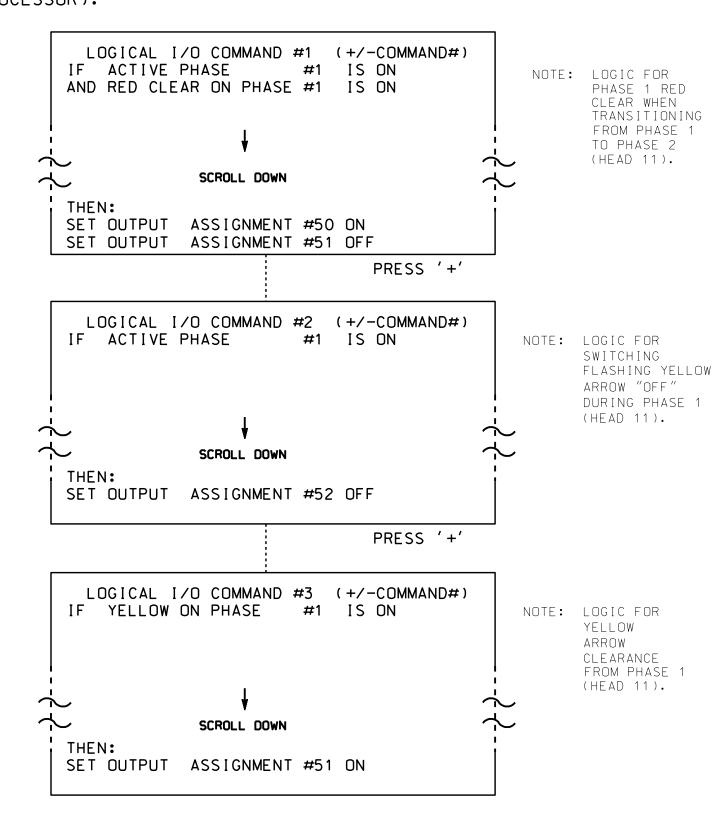
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

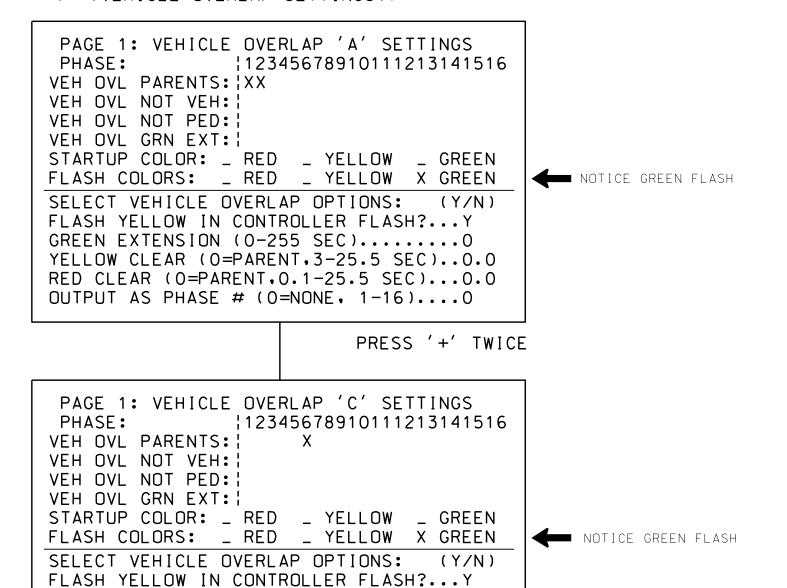
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

GREEN EXTENSION (0-255 SEC).....0

YELLOW CLEAR (O=PARENT.3-25.5 SEC)...O.O RED CLEAR (O=PARENT.0.1-25.5 SEC)...O.O OUTPUT AS PHASE # (O=NONE. 1-16)...O

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0984
DESIGNED: April 2016
SEALED: 8/9/2016
REVISED: N/A

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

Peachtree Road (Carolina Cinemas)/BB&T Entrance

Division 13 Buncombe County Asheville

PLAN DATE: June 2016 REVIEWED BY:

REVISIONS INIT. DATE

DocuMent NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

036880

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuMent NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

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9/8/2016

SIG. INVENTORY NO. 13-0984

S:*IIS&SU*IIS SIGNAIS*WORKGroups*SIG MAN*AFMSTFONG*ISU984_SM_616_XX Sarmstrong -