RAILROAD PREEMPTION PROGRAMMING DETAIL

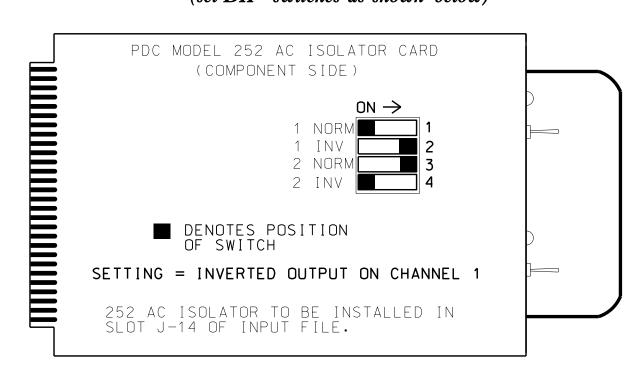
(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions).

PREEMPTION #1 SETTINGS (NEXT:1-10) INTERVAL/TIMING CLEAR/DWELL PHASES GRN YEL RED 12345678910111213141516 1 10 3.2 2.7 X 2 255 0.0 0.0 X X 3 0 0.0 0.0 4 0 0.0 0.0 5 1 0.0 0.0 X X EXIT CALLS
OPTIONS
PRIORITY (Y/N TO SELECT)HIGH
DELAY TIMER (0-255 SEC)
MIN GREEN BEFORE PRE (O= DEFAULT)1
PED CLEAR BEFORE PRE (O= DEFAULT)O
YELLOW CLEAR BEFORE PRE (O= DEFAULT).5.2
RED CLEAR BEFORE PRE (O= DEFAULT)1.1
DWELL MIN TIMER (0-255 SEC)12
DWELL MAX TIMER (0=OFF,1-255MIN)0
DWELL HOLD-OVER TIMER (0-255)
LATCH CALL?
LINK TO NEXT PREEMPT?
HOLD CLEAR 1 PHASES DURING DELAY?N
FAST GREEN FLASH DWELL PHASES?N
PED CLEARANCE THROUGH YELLOW?N
INHIBIT OVERLAP GREEN EXTENSION?N
SERVICE DURING SOFTWARE FLASH?N
REST IN RED DURING DWELL INTERVAL?N
FLASH DWELL INTERVAL?
ALLOW PEDS IN DWELL INTERVAL?
RE-TIME DWELL INTERVAL?
OVERLAPS: ABCDEFGHIJKLMNOP
DWELL INT FLASH YELLOW:
OMIT OVERLAPS: X X
I

PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)

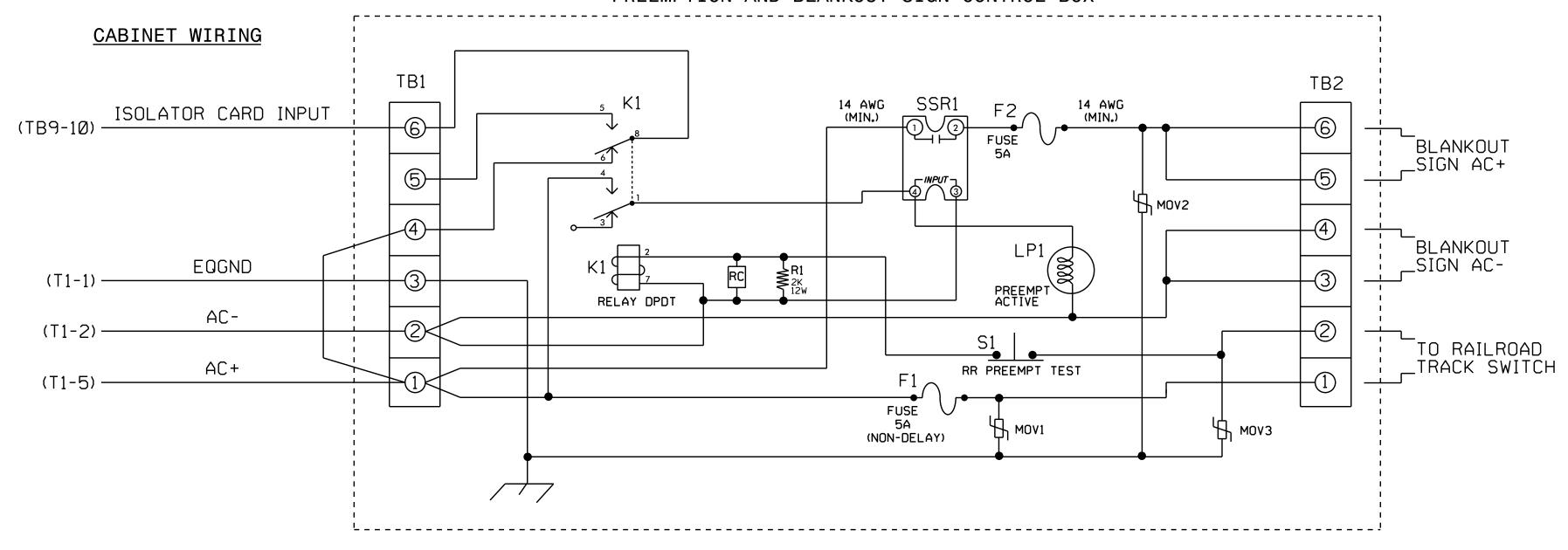


NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

RAILROAD PREEMPTION WIRING DETAIL

(wire as shown below)

PREEMPTION AND BLANKOUT SIGN CONTROL BOX



NOTES

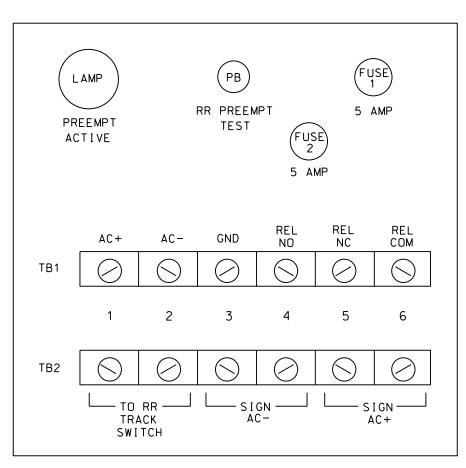
- Relay K1 is shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay K1 is a DPDT with 120VAC coil with octal base.
- 3. Relay SSR1 is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card.
- 5. IMPORTANT!! A jumper must be added between input file terminals J14-E and J14-K if not already present. Also, terminal TB9-12 (on input panel) shall be connected to AC neutral (jumper may have to be added).

FRONT VIEW

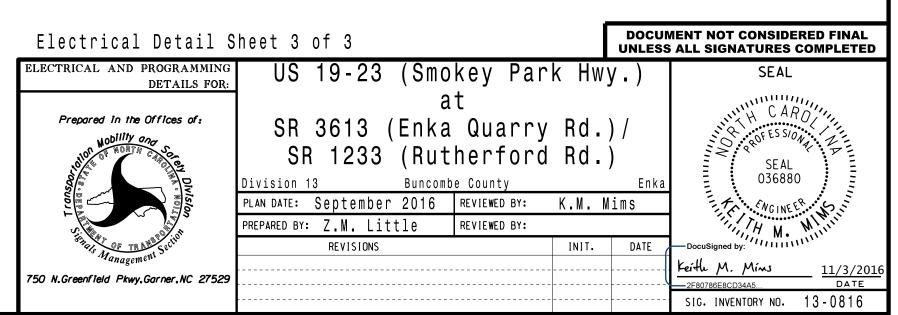
PROJECT REFERENCE NO.

U-4715 B

Sig.155.3



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0816
DESIGNED: March 2016
SEALED: 11/2/2016
REVISED:



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