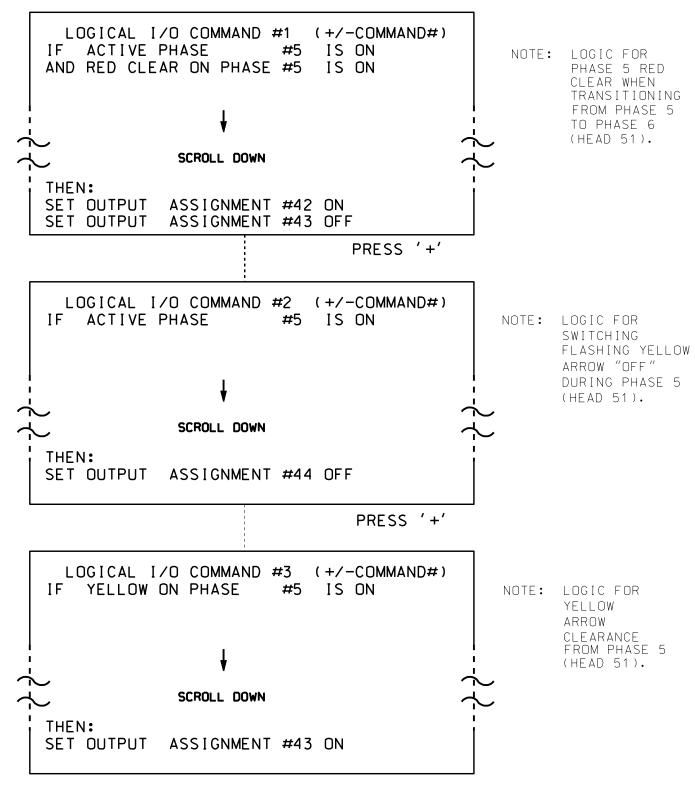
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

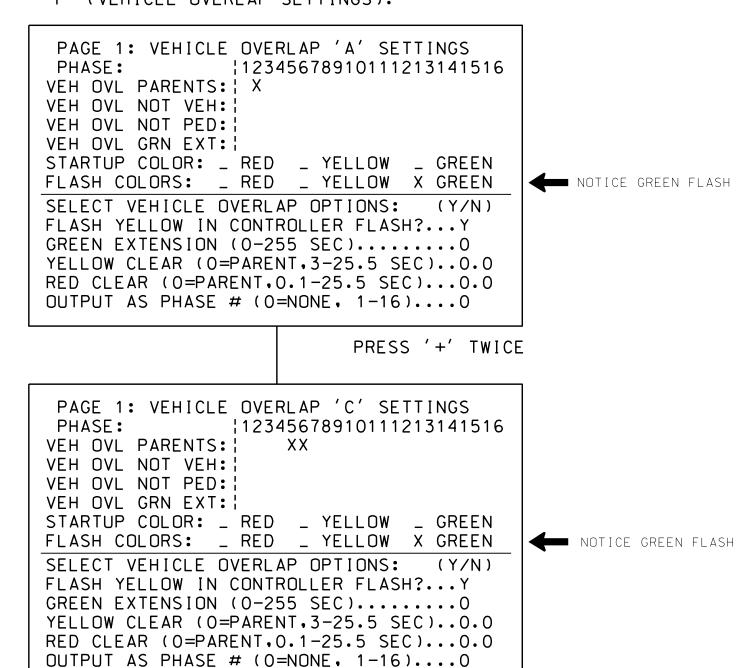
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

U-4715 B Sig.146.2

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0729
DESIGNED: May 2016
SEALED: 9/19/2016
REVISED:

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:

SR 3501 (Clayton Road)

Division 13

PLAN DATE: Spetember 2016 REVIEWED BY:

REVISIONS

REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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Division 13

PLAN DATE: Spetember 2016 REVIEWED BY:

REVISIONS

INIT. DATE

Docusigned by: William

Document not considered Final Unless All SIGNATURES COMPLETED

SEAL

O36880

PREPARED BY: Z.M. Little REVIEWED BY:

REVISIONS

TO N. Greenfield Pkwy. Garner. NC 27529

SIG. INVENTORY NO. 13-0729

S:*ITS&SU*ITS Signals*Workgroups*Sig Man*Little*13-0729*130729_sm_ele_20 _zmlittle