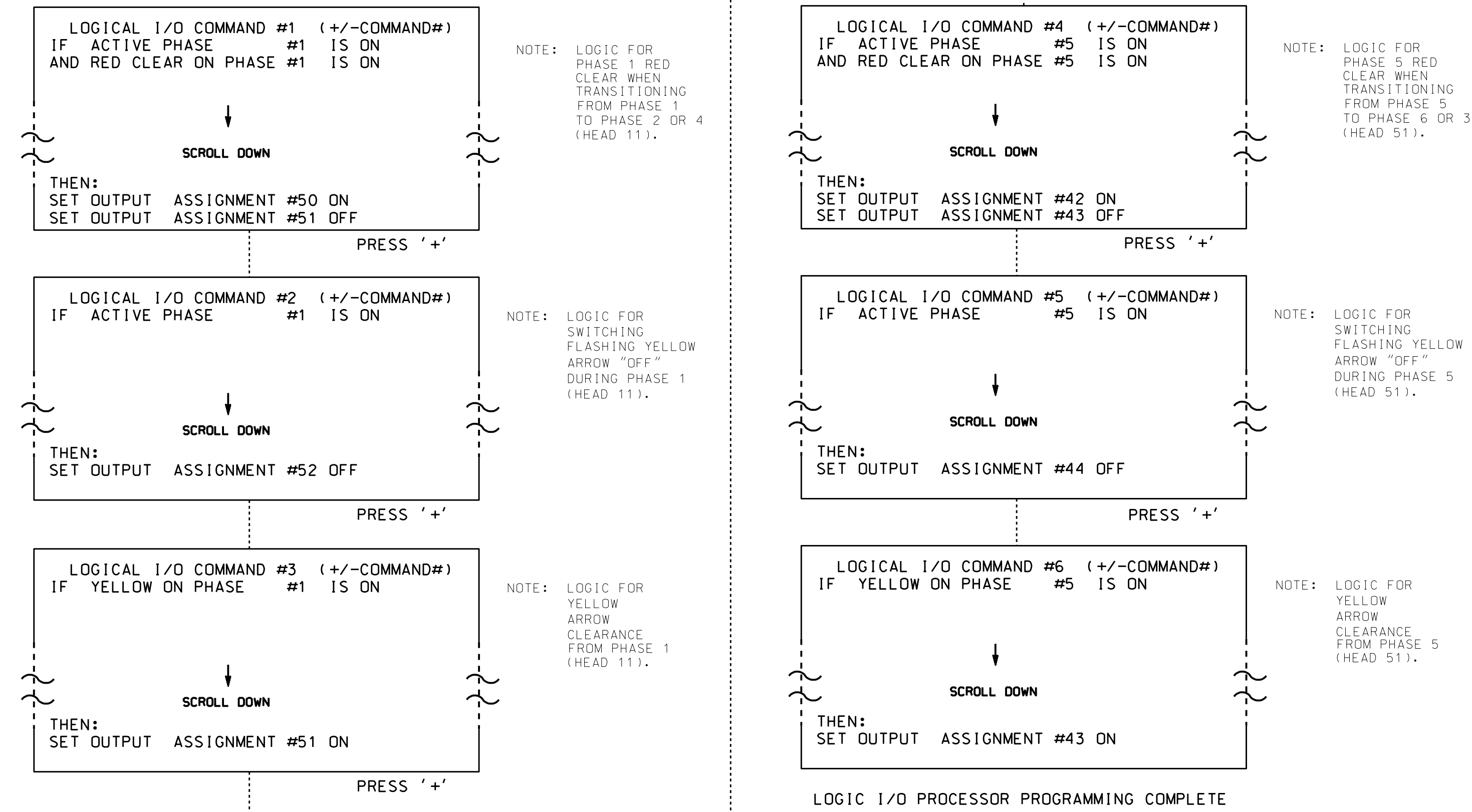


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

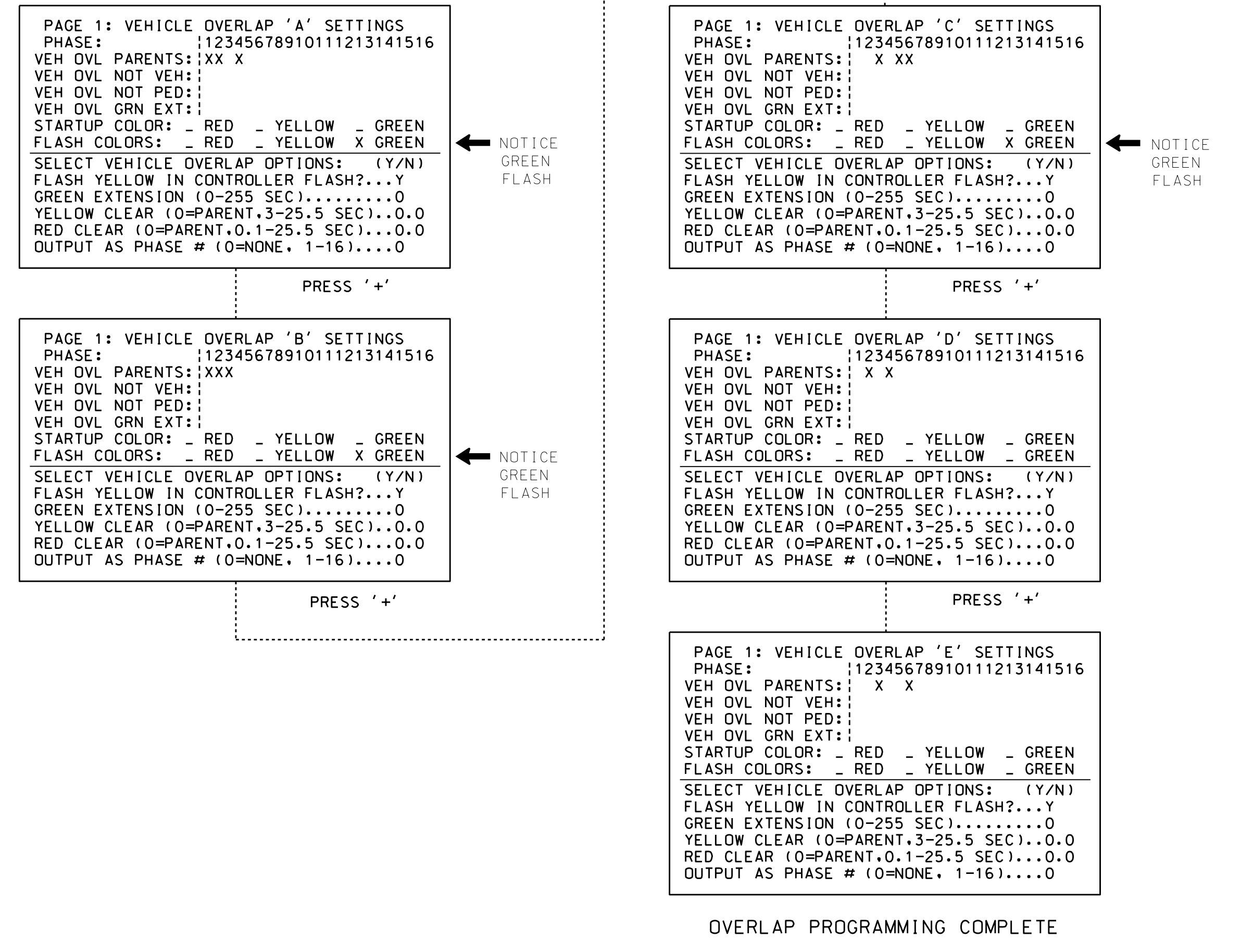


OUTPUT REFERENCE SCHEDULE	
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0707
DESIGNED: July 2016
SEALED: 10/25/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 3

<p>Prepared In the Offices of:</p> <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>US 70 (Tunnel Road) at Pleasant Ridge Drive and SR 2001 (Arnold Road)</p> <p>Division 13 Buncombe County Asheville</p> <p>PLAN DATE: October 2016 REVIEWED BY: BAS</p> <p>PREPARED BY: S. Armstrong REVIEWED BY:</p>	<p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <p>SEAL</p> <p>DocuSigned by: Keith M. Mims 11/3/2016</p> <p>SIG. INVENTORY NO. 13-0707</p>
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