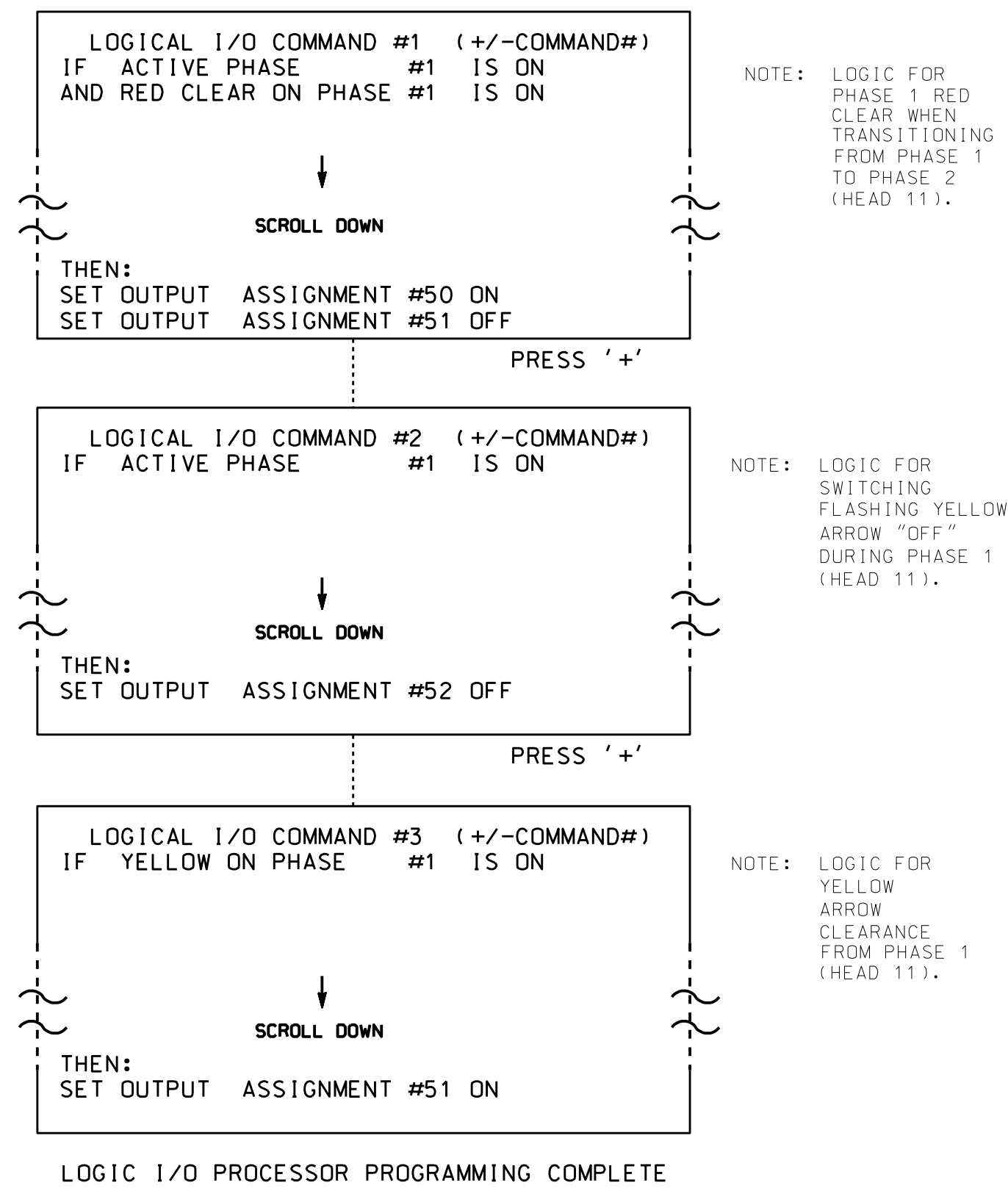


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

*(program controller as shown below)*

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



<b>OUTPUT REFERENCE SCHEDULE</b>
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

**OVERLAP PROGRAMMING DETAIL**

*(program controller as shown below)*

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 13-0554  
DESIGNED: June 2016  
SEALED: 8-10-16  
REVISED: N/A

20-105-2016\_08-58  
 S:\1\25310\13-SIGNALS\WORK\output51.g\_MarkPeterson\30554\_sml.e...xxx.dgn  
 J.peterson

ELECTRICAL AND PROGRAMMING DETAILS FOR:		US 74-A (Charlotte Highway) at SR 3071 (Avondale Road)		SEAL	
Prepared In the Offices of:		Division 13 Buncombe County Asheville			
		PLAN DATE: August 2016 REVIEWED BY: BAS			
750 N. Grantfield Pkwy, Garner, NC 27529		PREPARED BY: James Peterson REVIEWED BY:		DocuSigned by: Keith M. Mins 8/30/2016	
		REVISIONS		INIT. DATE	
				SIGNATURE DATE	
				SIG. INVENTORY NO. 13-0554	

DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL DETAIL SHEET 2 OF 2