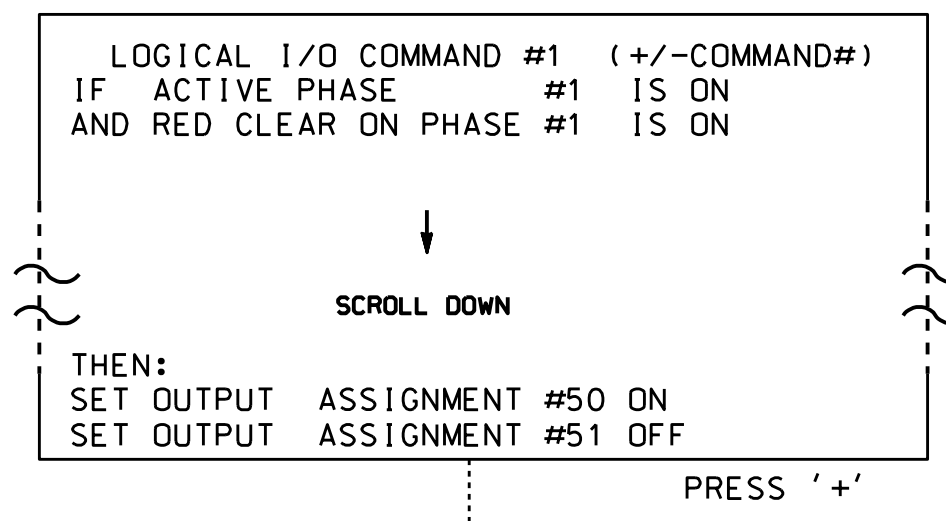


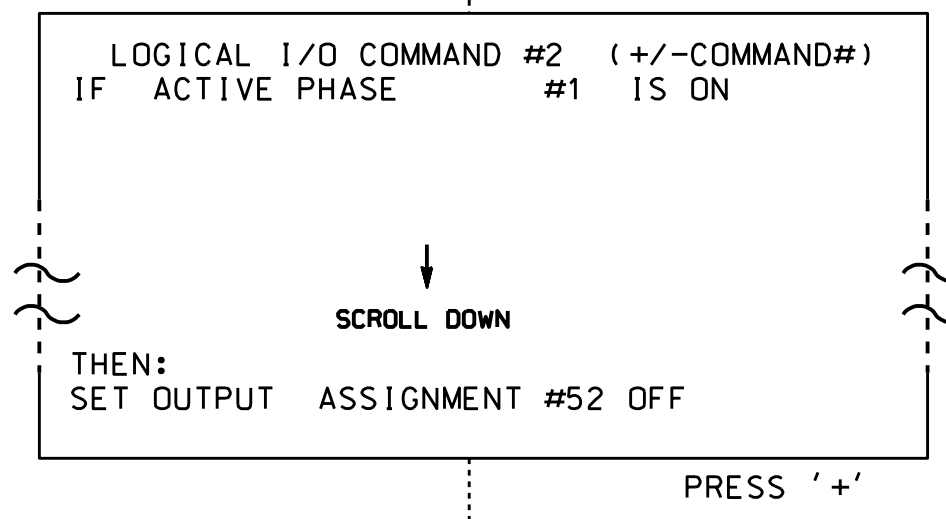
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

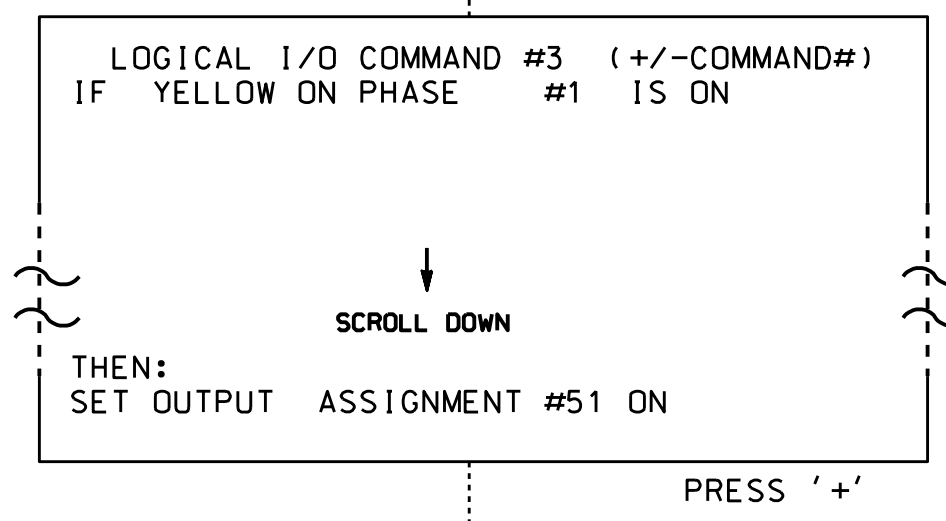
1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



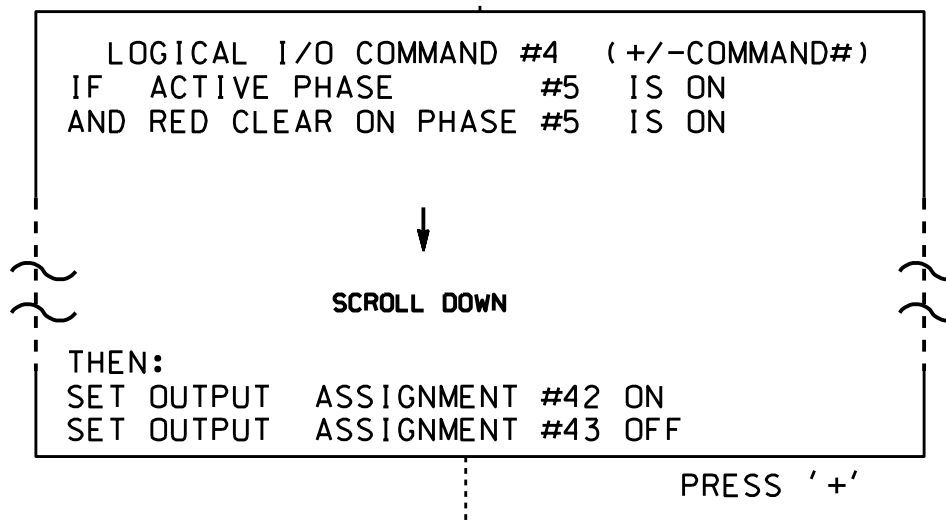
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



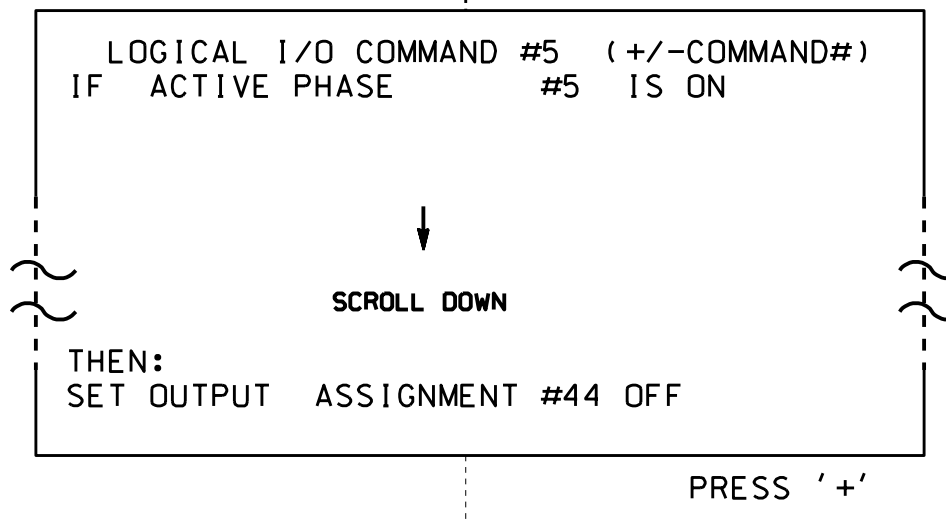
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).



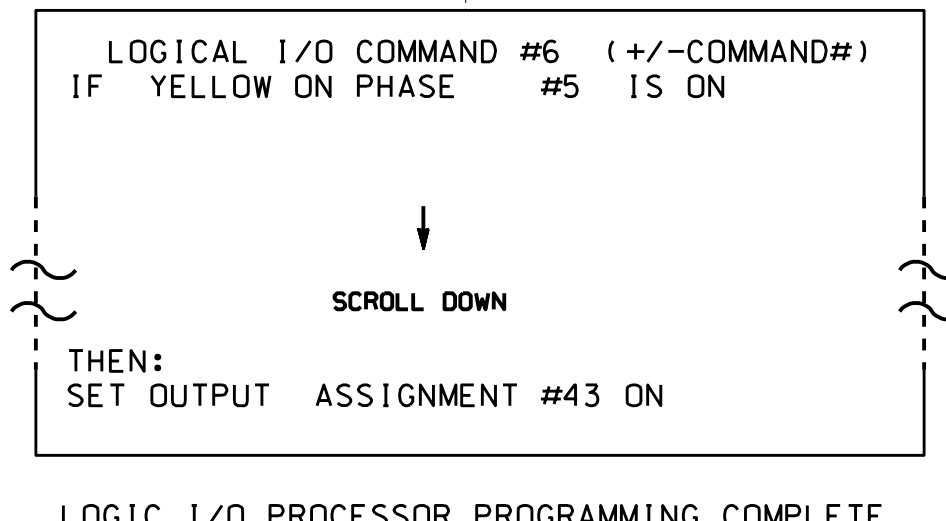
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0


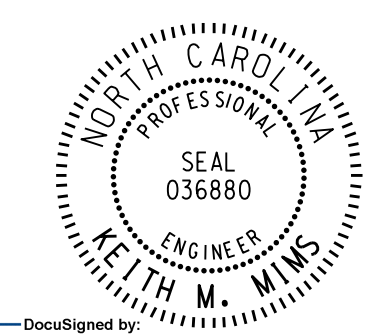
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0465
DESIGNED: June 2016
SEALED: 8/15/2016
REVISED:

Electrical Detail Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

<p>Prepared In the Offices of:</p>  <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>US 74A (Fairview Road) at I-240 Eastbound Ramp / Business Entrance</p> <p>Division 13 Buncombe County Asheville</p> <p>PLAN DATE: July 2016 REVIEWED BY: K.M. Mims PREPARED BY: Z.M. Little REVIEWED BY:</p>	<p style="text-align: center;">SEAL</p>  <p>DocuSigned by: Keith M. Mims 8/17/2016</p>						
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>		REVISIONS	INIT.	DATE				<p>SIG. INVENTORY NO. 13-0465</p>
REVISIONS	INIT.	DATE						

16-AUG-2016 1:00 PM S:\TSS\2016\SIG\Map\1111e13-0465\130465_sic.sm.2016mdd.dgn zmlittle