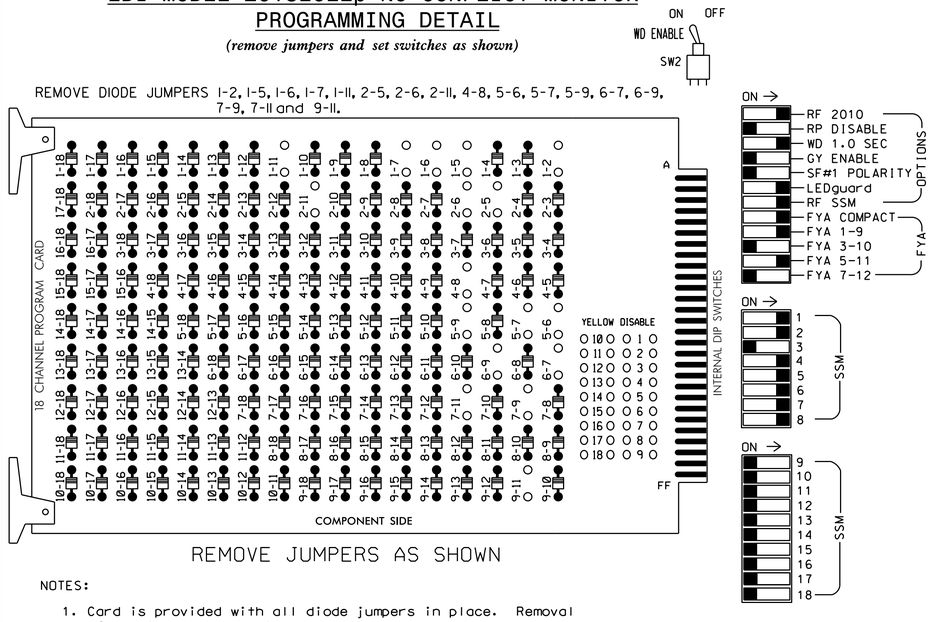
PROJECT REFERENCE NO.	SHEET NO.
U-4715B	Sig. 93.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.
5. Special cabinet wiring is required to utilize FYA COMPACT mode. See Ped Yellow Conflict Monitor Wiring Detail on this sheet.

INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" U	Ø 1	Ø2/SYS	SLOT	Ø 4	Ø 5	Ø6/SYS	SLOT	Ø 8	SLOT	SLOT	SLOT	SLOT	SLOT	FS
	1A	2A/S1		4A	5A	6A/S3		8A						DC ISOLATOR
L	NOT USED	Ø2/SYS	EMPTY	Ø 4	NOT USED	Ø6/SYS	EMPTY	Ø 8	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	ST
		2B/S2		4B		6B/S4		8B						DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB21-1,2	I1U	56	18	1	1	Y	Y			
	-	-	59	21	15	6	Y	Y			
2A/S1	TB21-3,4	I2U	39	1	2	2/SYS	Y	Y			
2B/S2	TB23-3,4	I2L	43	5	12	2/SYS	Y	Y			
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			3
4B	TB23-7,8	I4L	45	7	14	4	Y	Y			10
5A[2]	TB21-9,10	I5U	55	17	5	5	Y	Y			
	-	-	63	25	32	2	Y	Y			
6A/S3	TB21-11,12	I6U	40	2	6	6/SYS	Y	Y			
6B/S4	TB23-11,12	I6L	44	6	16	6/SYS	Y	Y			
8A	TB22-1,2	I8U	42	4	8	8	Y	Y			3
8B	TB24-1,2	I8L	46	8	18	8	Y	Y			10

[1] Add jumper from I1-F to I1-SP, on rear of input file.

[2] Add jumper from I5-F to I5-SP, on rear of input file.

INPUT FILE POSITION LEGEND: I2L
- FILE I
- SLOT 2
- LOWER

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Start Up In Green.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
7. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....336
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....POLE
OUTPUT FILE POSITIONS...12
LOAD SWITCHES USED.....S1,S2,S3,S5,S7,S8,S9,S10,S11
PHASES USED.....1,2,4,5,6,8
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED
OVERLAP "E".....1

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode on the 2018ECLip-NC Monitor, the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to chan. 9 green (monitor pin 13), and from 6 PY (field term. 120) to chan. 10 green (monitor pin R).

Follow the instructions below to make the appropriate connections:

STEP 1: Fold down rear panel of output file.

STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).

STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

CMU-13 ——— 2PY (term. 114)
CMU-R ——— 6PY (term. 120)

NOTE: Some cabinet manufacturers use a key plug to accomplish this wiring configuration. If connectors are used, simply plug the two connectors together that are labeled with the pin-out as shown above.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0430
DESIGNED: January 2016
SEALED: 8-17-16
REVISED: N/A

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3		S4	S5	S6	S7	S8	S9		S10	S11	S12
CMU CHANNEL NO.	1	2	9	13	3	4	14	5	6	11	15	7	8	16
PHASE	OLA	2	1 GRN	2 PED	3	4	4 PED	OLC	6	5 GRN	6 PED	OLE	8	8 PED
SIGNAL HEAD NO.	11★	21,22	11★	NU	NU	41,42	NU	51★	61,62	51★	NU	82	81,82	NU
RED		128				101			134			*	107	
YELLOW		129				102			135				108	
GREEN		130				103			136				109	
RED ARROW	125							131						
YELLOW ARROW	126							132				123		
FLASHING YELLOW ARROW	127							133						
✋														
GREEN ARROW			114							120		124		
🚶				*							*			

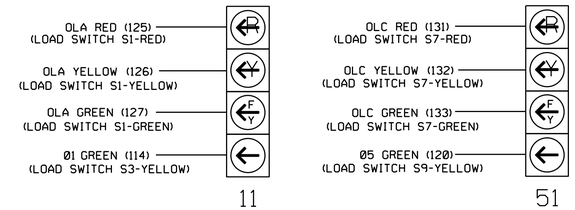
* Denotes install load resistor. See load resistor installation detail below.

★ See pictorial of head wiring in detail below.

NOTE: Load Switches S1, S3, S7, S9 require output remapping.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



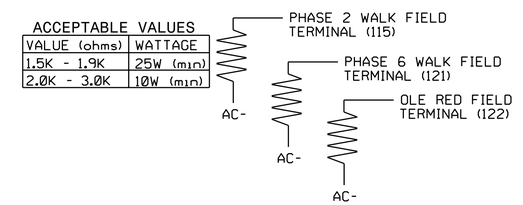
NOTE

1. The sequence display for these signals requires special logic and output remapping. See sheets 2, 3 and 4 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



ELECTRICAL DETAIL SHEET 1 OF 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signal Management Section
750 N.Greenfield Pkwy, Garner, NC 27529

US 25 (Hendersonville Road) at Golds Gym Entrance/ Publix Entrance

Division 13 | Buncombe County | Asheville

PLAN DATE: July 2016	REVIEWED BY: DTJ
PREPARED BY: James Peterson	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 030530 ENGINEER ZACHARY M. LITTLE

DocuSigned by: Zachary M. Little 10/10/2016
DATE
SIG. INVENTORY NO. 13-0430

07-OCT-2016 11:40
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jrpeterson