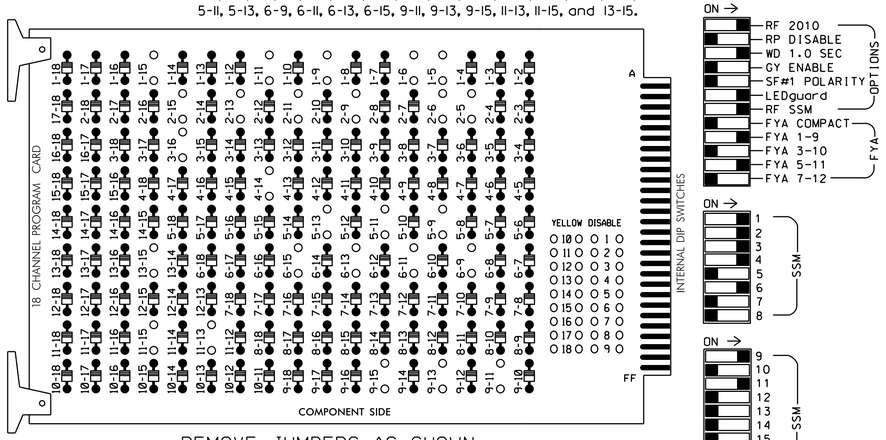
PROJECT REFERENCE NO.	SHEET NO.
U-4715B	Sig. 78.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS I-5, I-6, I-9, I-11, I-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-16, 4-14, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 9-11, 9-13, 9-15, 11-13, 11-15, and 13-15.



REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Start Up In Green.
4. Program phases 2, 3, 4 and 6 for 'STARTUP PED CALL'.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8, S9,S12,AUX S1,AUX S4
PHASES USED.....1,2,2PED,3,3PED,4,4PED, 5,6,6PED
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4		S5		S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3		4		14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3		4		4 PED	5	6	6 PED	7	8	3 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	42	21,22	P21, P22	31	32	41	42	P41, P42	51★	61,62	P61, P62	NU	NU	P31, P32	11★	NU	NU	51★	NU	NU
RED		✱	128		116	116	101	101			134									
YELLOW			129		117	117	102	102		✱	135									
GREEN			130		118	118	103	103			136									
RED ARROW															A121			A114		
YELLOW ARROW		126													A122			A115		
FLASHING YELLOW ARROW															A123			A116		
GREEN ARROW	127	127		118		103			133											
✋			113					104			119			110						
🚶			115					106			121			112						

NU = Not Used

✱ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø1 1B	Ø2/SYS 2A/S1	WIRED INPUT ⊗	SLOT EMPTY	Ø3 3A	Ø4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	Ø6 PED DC ISOLATOR	FS DC ISOLATOR
	L	NOT USED	NOT USED	Ø2/SYS 2B/S2			Ø3 3B	Ø4 4B					Ø4 PED DC ISOLATOR	Ø3 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6/SYS 6A/S3	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø6/SYS 6B/S4												

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A/S1	TB2-9,10	I3U	63	25	32	2/SYS	Y	Y			
2B/S2	TB2-11,12	I3L	76	38	42	2/SYS	Y	Y			
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			10
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			
5A[2]	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A/S3	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S4	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P31,P32	TB8-8,9	I13L	70	32	PED 8	3 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

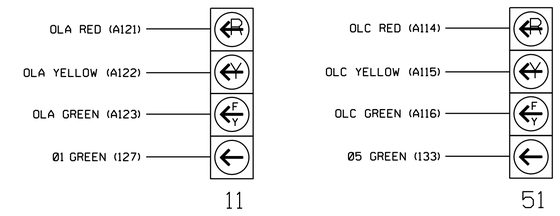
[1] Add jumper from I1-W to J4-W, on rear of input file.

[2] Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



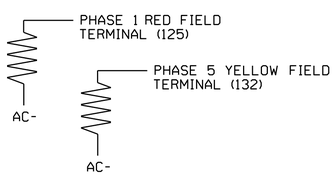
NOTE

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0381
DESIGNED: May 2016
SEALED: 8/8/2016
REVISED: N/A

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

SR 3214 (Biltmore Ave.) at St. Joseph's Hospital Entrance/ Hospital Drive

Division 13 | Buncombe County | Asheville

PLAN DATE: July 2016 | REVIEWED BY: BAS

PREPARED BY: S. Armstrong | REVIEWED BY:

REVISIONS	INIT.	DATE

Prepared in the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 036880 ENGINEER KEITH M. MIMS

DocuSigned by: Keith M. Mims 8/30/2016
2F80786E8CD34A5... DATE

SIG. INVENTORY NO. 13-0381

09-AUG-2016 11:28
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