(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 VEH OVL PARENTS: X VEH OVL NOT VEH: : VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

PRESS '+' PAGE 1: VEHICLE OVERLAP 'B' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | VEH OVL NOT VEH: VEH OVL NOT PED: : VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN | — NOTICE GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N FLASH GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (0=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

PRESS '+' TWICE

USING + OR - KEY POSITION ON OVERLAP 'P' PAGE 1: VEHICLE OVERLAP 'P' SETTINGS 12345678910111213141516 VEH OVL PARENTS: XXX X XXX VEH OVL NOT VEH: | VEH OVL NOT PED: : VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

The utilization of overlap P ensures consistent clearance timing during transition to preemption

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

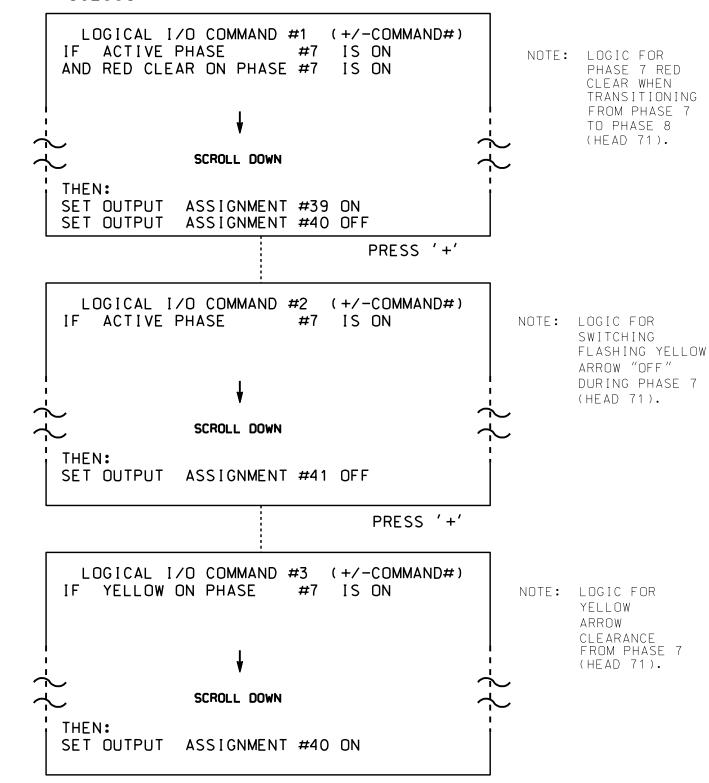
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

PROJECT REFERENCE NO. U-4715 B Sig. 48.

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

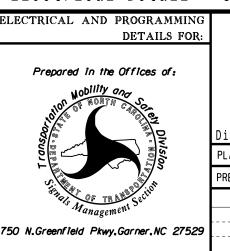
- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 39 = Overlap D Red OUTPUT 40 = Overlap D Yellow OUTPUT 41 = Overlap D Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0242 DESIGNED: January 2016 SEALED: 11/7/2016 REVISED:



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Electrical Detail - Sheet 2 of 3 NC 81 (Hendersonville Rd.

US 25A (Lodge St.)

REVISIONS

ivision 13 Buncombe County Asheville PLAN DATE: August 2016 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:

030530 · CACINEEN INIT. DATE

Zachary M. Little 11/8/2016 SIG. INVENTORY NO. 13-0242

CARO

ROFES SION