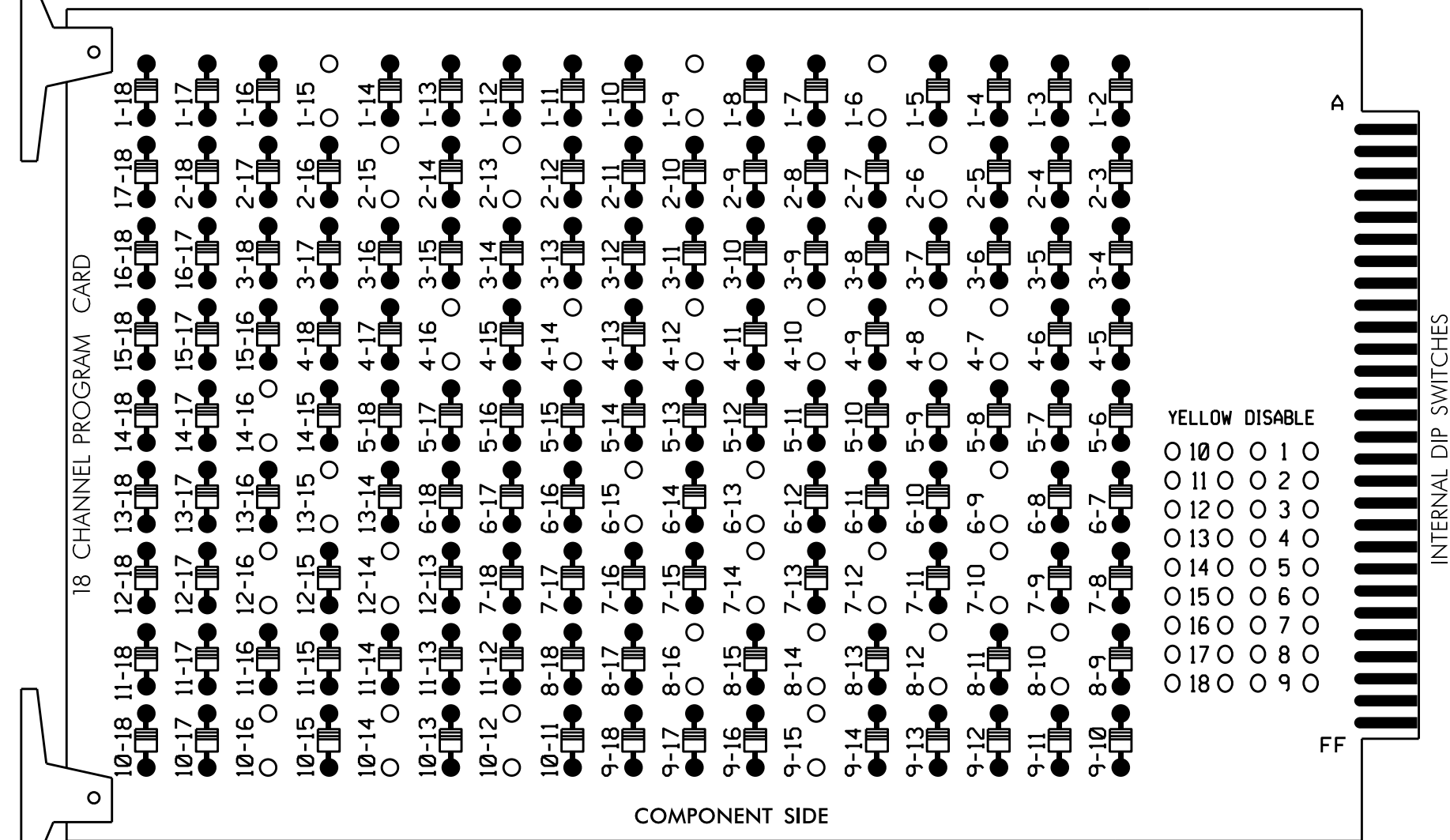


**EDI MODEL 2018EClip-NC CONFLICT MONITOR**

**PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-15, 2-6, 2-13, 2-15, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 6-9, 6-13, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 9-15, 10-12, 10-14, 10-16, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash and overlap 2 as Wag Overlaps.
7. The cabinet and controller are part of the Asheville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S8,S9,S10,S11,  
 S12,AUX S1,AUX S2,AUX S5  
 PHASES USED.....1,2,4,6,7,8  
 OVERLAP "A".....1  
 OVERLAP "B".....4  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....7+8  
 OVERLAP "P".....1+2+4+6+7+8

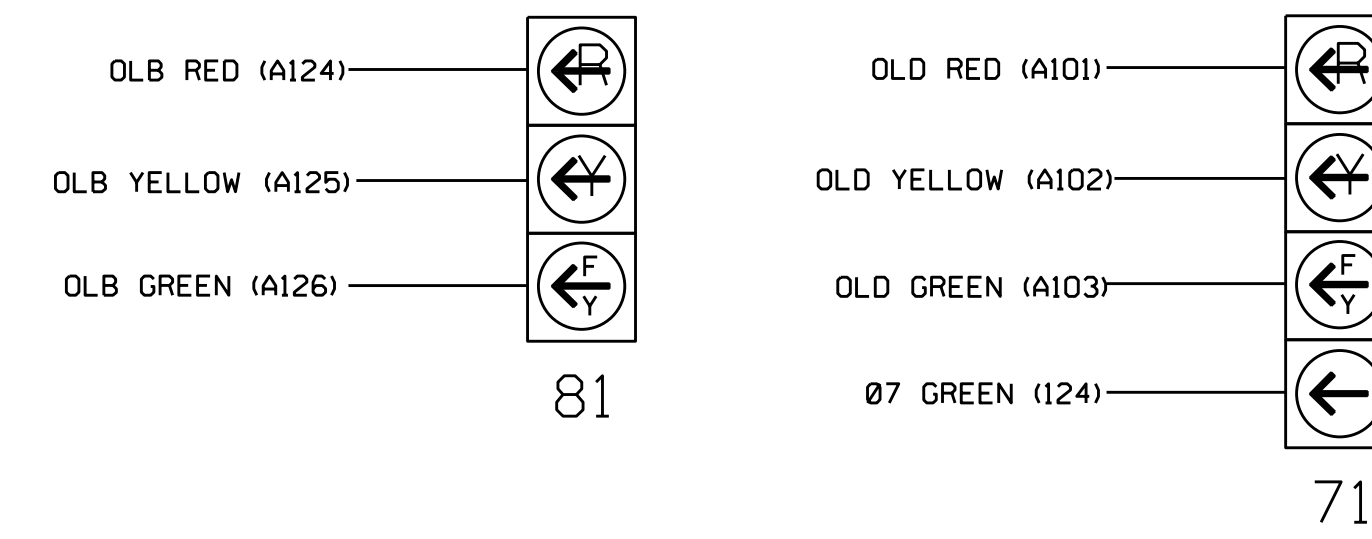
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	61	21,22	P21, P22	NU	41,42	P41, P42	NU	61,62	P61, P62	71	82,83	P81, P82	83	81	NU	NU	71	NU
RED	*	128			101			134			107		*					
YELLOW		129			102			135		*	108							
GREEN		130			103			136			109							
RED ARROW														A124				A101
YELLOW ARROW	126													A122	A125			A102
FLASHING YELLOW ARROW															A126			A103
GREEN ARROW	127									124				A123				
Hand				113		104			119			110						
Walking				115		106			121			112						

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**  
 1. The sequence display for signal head 71 requires special logic programming. See sheet 2 for programming instructions.

**INPUT FILE POSITION LAYOUT**

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13
L	1A	1B	2A	2B	3A	3B	4A	4B	5A	5B	6A	6B	7A	7B
U	NOT USED	NOT USED	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13
L	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	PRE1
U	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	AC ISOLATOR
L	∅ 1	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	NOT USED

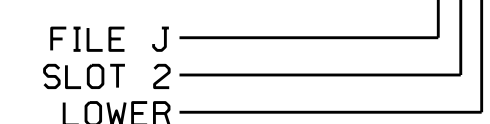
EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME  
 PRE = PREEMPT  
 ⊗ Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			5
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
7A <sup>1</sup>	TB5-5,6	J5U	57	19	7	7	Y	Y			15
		I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29							2 PED
P41,P42	TB8-5,6	I12L	69	31							4 PED
P61,P62	TB8-7,9	I13U	68	30							6 PED
P81,P82	TB8-8,9	I13L	70	32							8 PED

<sup>1</sup>Add jumper from J5-W to I8-W, on rear of input file.

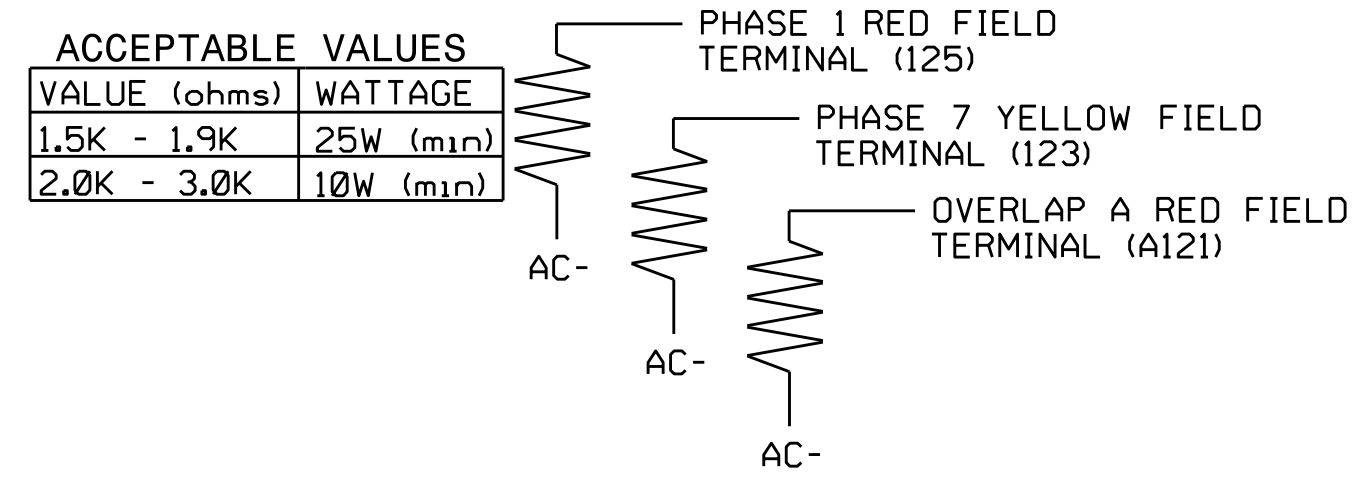
INPUT FILE POSITION LEGEND: J2L



**NOTE:**  
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistor as shown below)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0242  
 DESIGNED: January 2016  
 SEALED: 11/7/2016  
 REVISED:

**COUNTDOWN PEDESTRIAN SIGNAL OPERATION**

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

**BACKUP PROTECTION NOTE**

(program controller as shown below)  
 From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 6 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
 Transportation Mobility and Safety Solutions  
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 81 (Hendersonville Rd.) at US 25A (Lodge St.)

Division 13 Buncombe County Asheville  
 PLAN DATE: August 2016 REVIEWED BY: T. Joyce  
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS: INIT. DATE

Sealed by: *Carlynn M. Little* 11/8/2016  
 SEAL 030530  
 ENGINEER JACOBARY M. LITTLE

SIG. INVENTORY NO. 13-0242

07-1004-2016 11-15-17  
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