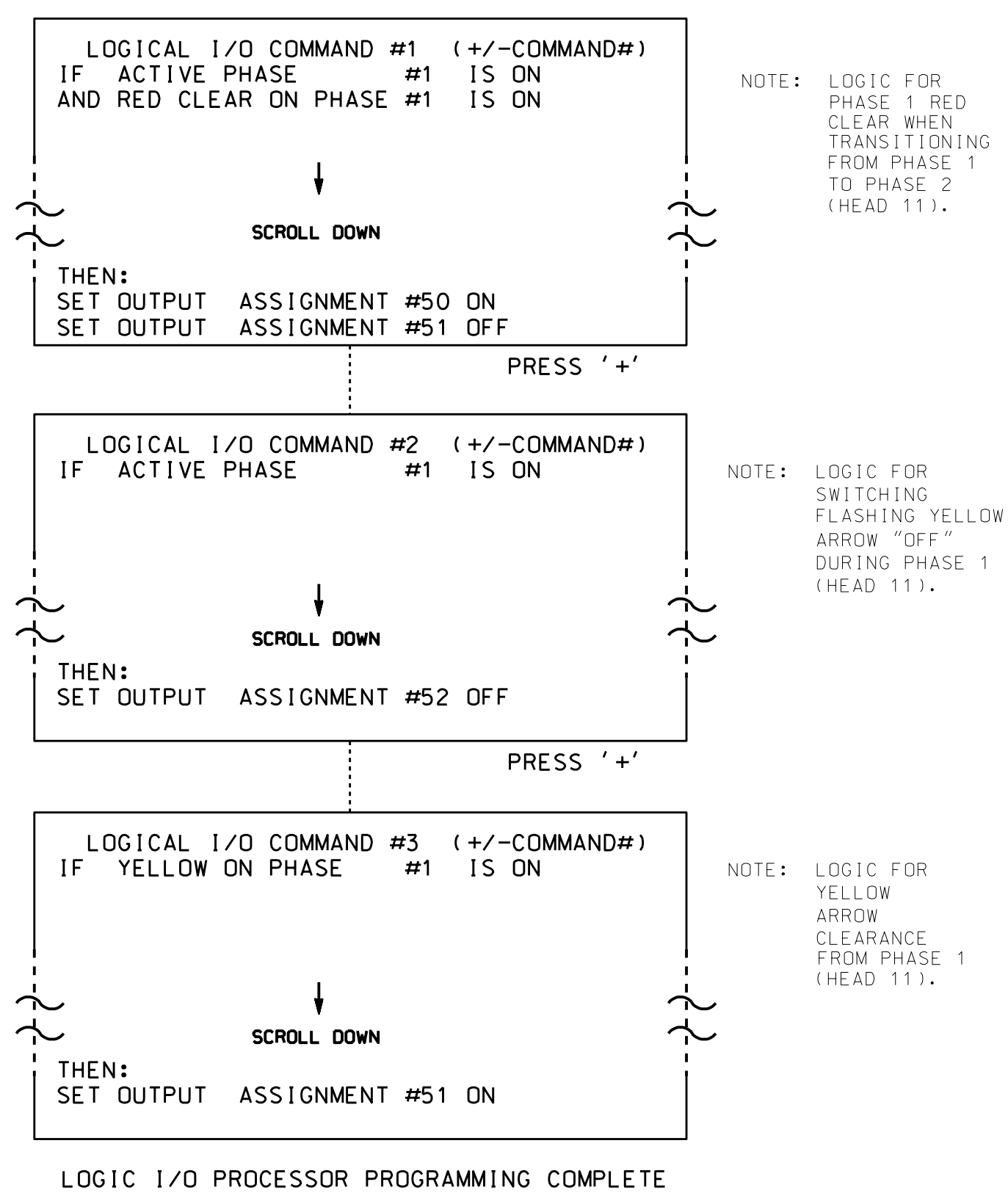


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

OVERLAP PROGRAMMING DETAIL (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

    PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
    PHASE:      12345678910111213141516
    VEH OVL PARENTS: XX
    VEH OVL NOT VEH:
    VEH OVL NOT PED:
    VEH OVL GRN EXT:
    STARTUP COLOR: _ RED _ YELLOW _ GREEN
    FLASH COLORS:  _ RED _ YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```

    PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
    PHASE:      12345678910111213141516
    VEH OVL PARENTS: X
    VEH OVL NOT VEH:
    VEH OVL NOT PED:
    VEH OVL GRN EXT:
    STARTUP COLOR: _ RED _ YELLOW _ GREEN
    FLASH COLORS:  _ RED _ YELLOW X GREEN
    SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
    FLASH YELLOW IN CONTROLLER FLASH?...Y
    GREEN EXTENSION (0-255 SEC)...0
    YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
    RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
    OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0230
DESIGNED: December 2015
SEALED: 8/4/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

	DETAILS FOR: US 25 (Merrimon Avenue) at SR 2053 (Beaverdam Road)/ Osborne Road		
	Division 13 PLAN DATE: June 2016 PREPARED BY: S. Armstrong	Buncombe County Asheville REVIEWED BY: BAS REVIEWED BY:	

04-AUG-2016 15:05
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