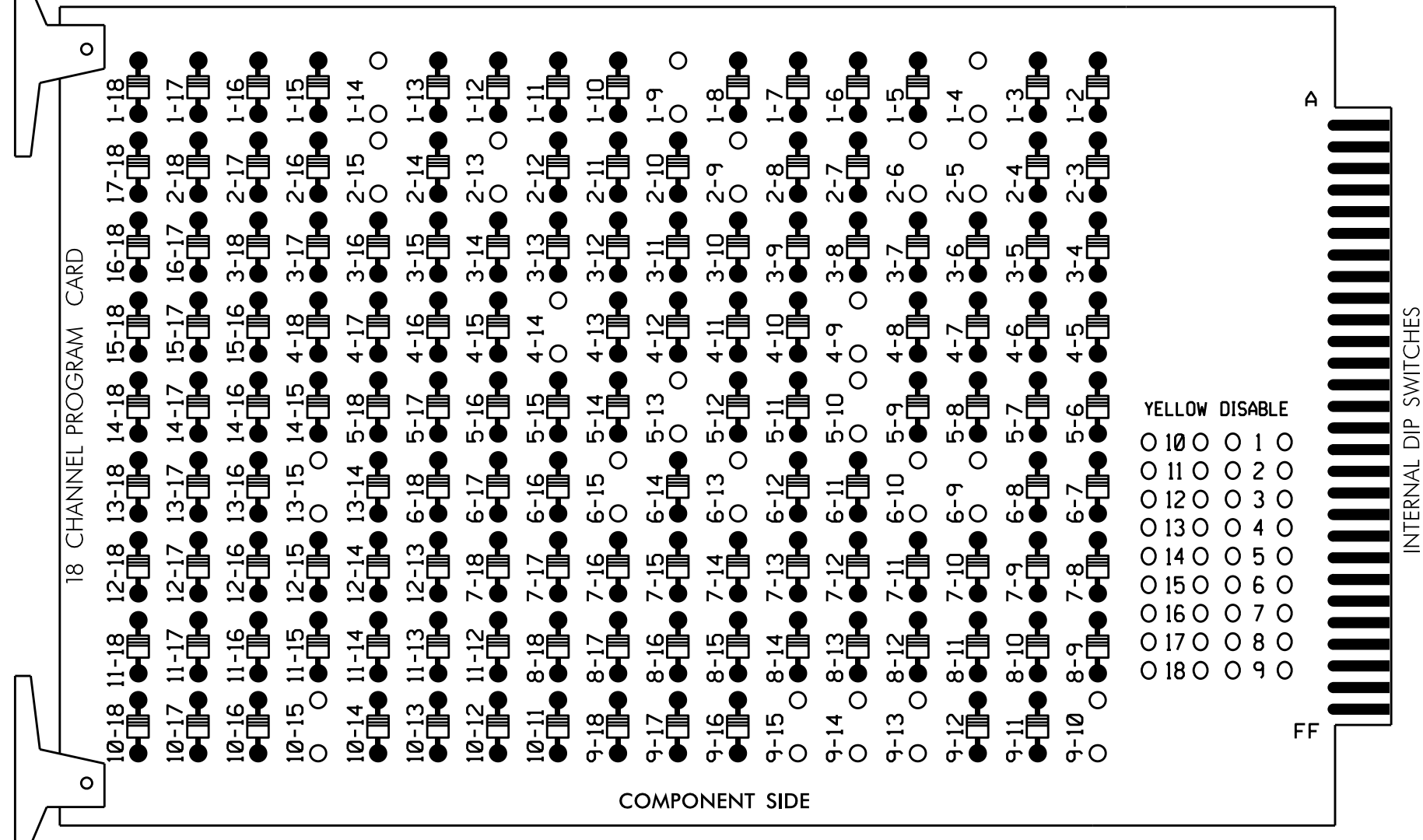


EDI MODEL 2018EClip-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

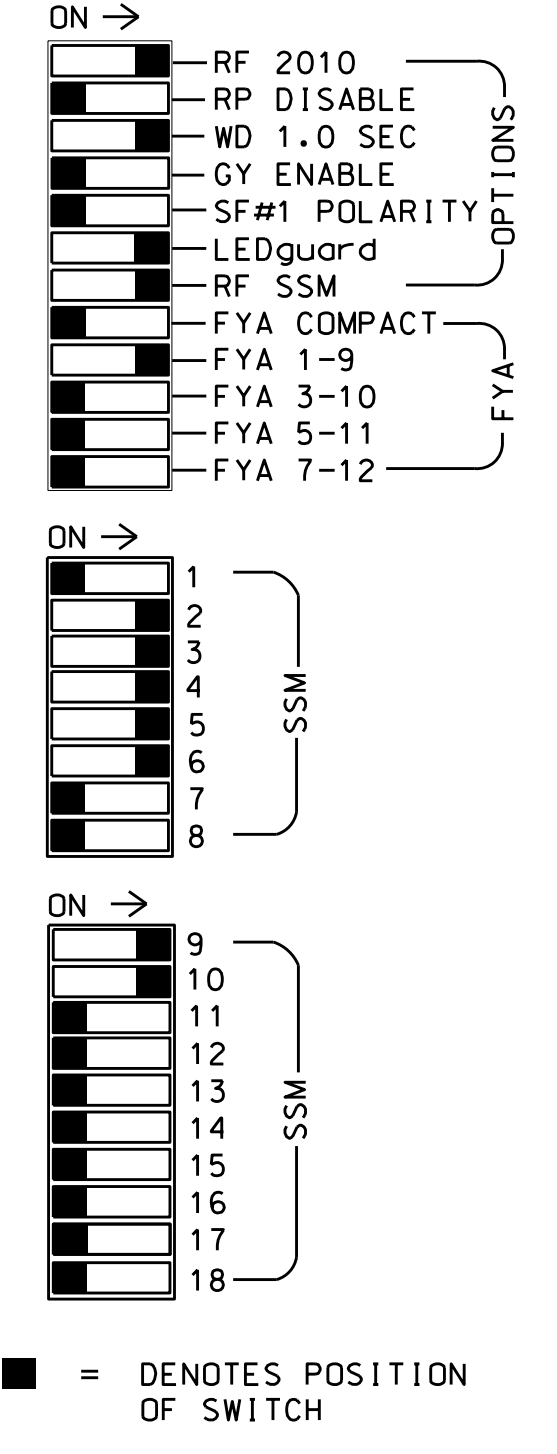
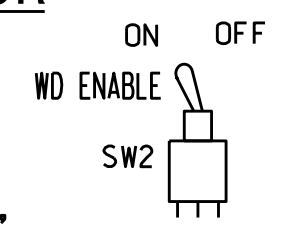
REMOVE DIODE JUMPERS 1-4, 1-9, 1-14, 2-5, 2-6, 2-9, 2-13, 2-15, 4-9, 4-14, 5-10, 5-13, 6-9, 6-10, 6-13, 6-15, 9-10, 9-13, 9-14, 9-15, 10-15 and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4 and 6 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7, S8,S9,AUX S1,AUX S2
 PHASES USED.....1,2,2 PED,3,4,4 PED,5, 6,6 PED
 OVERLAP "A".....4+6
 OVERLAP "B".....1
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED
 OVERLAP "E".....NOT USED
 OVERLAP "F".....NOT USED
 OVERLAP "G".....4

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6				
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18				
PHASE	OLG	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE				
SIGNAL HEAD NO.	63,64	21, 22,23	P21, P22	31	32	41	42	P41, P42	42	51	61,62	P61, P62	NU	NU	NU	63,64	11	32	NU	NU	NU	NU
RED		128	116	116	101	101					134					A121						
YELLOW	*	129	117	117	102	102					135											
GREEN		130	118	118	103	103					136											
RED ARROW											131					A124						
YELLOW ARROW										132	132					A122	A125	A125				
FLASHING YELLOW ARROW																A123						
GREEN ARROW	127				103			133	133							A126	A126					
Hand			113					104				119										
Walking			115					106				121										

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE	U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	L	1A	2A	2C	3A	4A	4B	SYS. DET. S1	SYS. DET. S2	SYS. DET. S3	FS	ST	ISOLATOR	ISOLATOR	ISOLATOR
		1B	2B	NOT USED	NOT USED	4B	NOT USED	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
"J"	L	5A	6A	5B	6B	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
		5B	6B	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

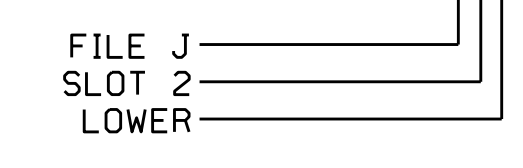
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-5,6	I2U	39	1	2	1	Y	Y			
1B	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
2C	TB4-1,2	I4U	47	9	22	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
5A	TB3-5,6	J2U	40	2	6	5	Y	Y			
5B	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
* S3	TB7-9,10	J9U	59	21	15	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

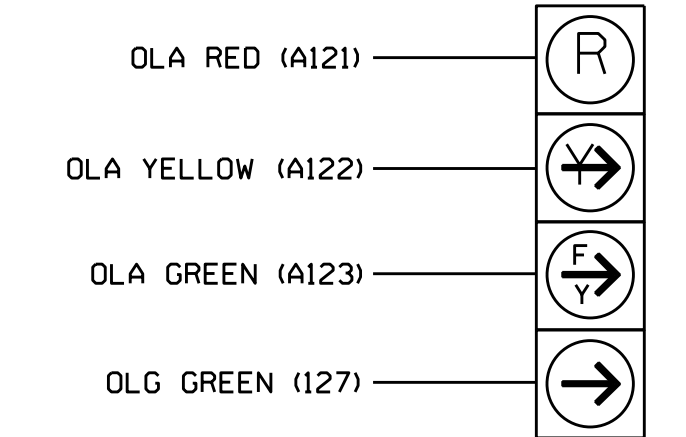
* System Detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



63,64

NOTE

The sequence display for signal heads 63 and 64 requires special logic programming. See sheet 2 for programming instructions.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

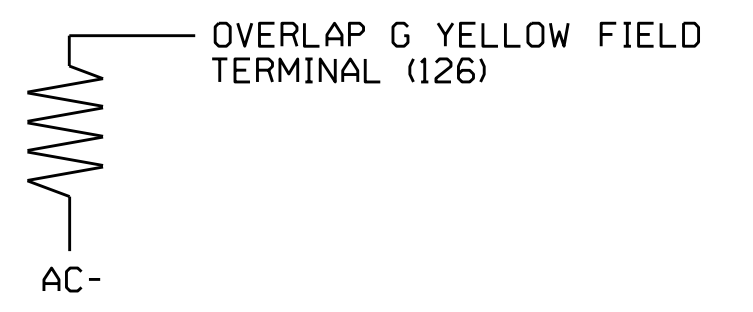
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0221
 DESIGNED: June 2016
 SEALED: 8/10/2016
 REVISED:

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
 TRANSPORTATION MOBILITY AND SAFETY DIVISION
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 Signal Management Section
 750 N. Greenfield Pkwy, Garner, NC 27529

US 19-23-74A (Patton Avenue) at NC 63 (Leicester Highway) and Entrance to Kerr Drug

Division 13 Buncombe County Asheville
 PLAN DATE: July 2016 REVIEWED BY: BAS
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Seal: NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 030530 JACOBARY M. LITTLE

DocuSigned by: Zachary M. Little 8/22/2016
 DATE: 8/22/2016
 SIG. INVENTORY NO. 13-0221

15-AUG-2016 1:50:11 S:\IT\ASIS\15_Sigmod\work\hous\51g_Maps\11-1-16\130221_Sm.ele.xxx.dgn