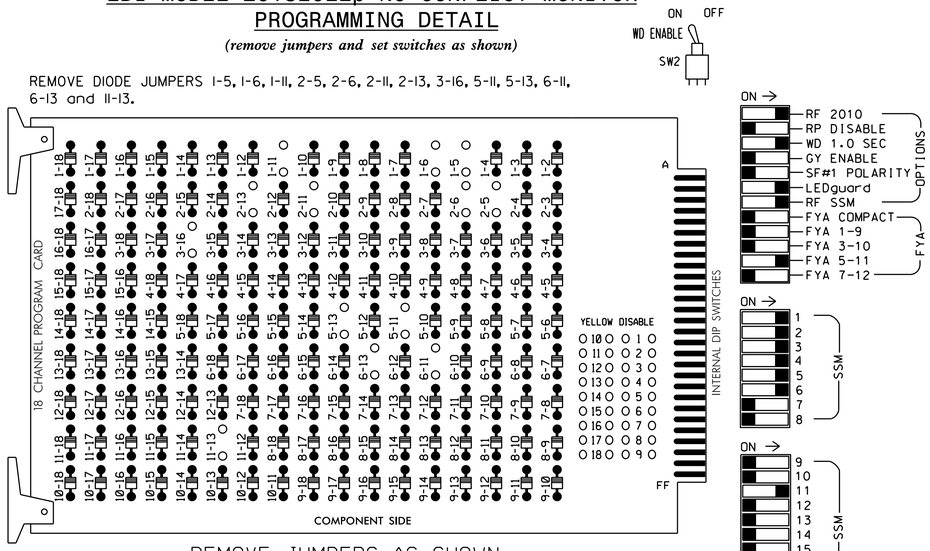
PROJECT REFERENCE NO.	SHEET NO.
U-4715B	Sig. 16.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS I-5, I-6, I-11, 2-5, 2-6, 2-11, 2-13, 3-16, 5-11, 5-13, 6-11, 6-13 and 11-13.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Start Up In Green.
4. Program phases 2 and 3 for 'STARTUP PED CALL'.
5. Program phases 2 and 6 for Yellow Flash.
6. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S7,S8, S12,AUX S4
PHASES USED.....1,2,2 PED,3,3 PED,4,5,6
OVERLAP "A".....NOT USED
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4		S5		S6	S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3		4		14	5		6	15	7	8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3		4		4 PED	5		6	6 PED	7	8	3 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	32	21,22	P21, P22	31	32	41	42	NU	42	51★	61, 62,63	NU	NU	NU	P31, P32	NU	NU	NU	51★	NU	NU
RED			128		116	116	101	101		✳		134										
YELLOW			129		117	117	102	102				135										
GREEN			130		118	118	103	103				136										
RED ARROW	125																			A114		
YELLOW ARROW	126	126								132										A115		
FLASHING YELLOW ARROW																				A116		
GREEN ARROW	127	127			118		103			133	133											
✋				113												110						
🚶				115												112						

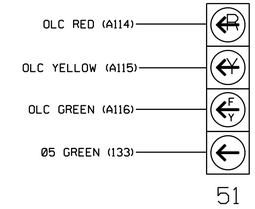
NU = Not Used

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø 1 1A	Ø 2 2A,2B	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø 3 3A	Ø 4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	NOT USED	FS DC ISOLATOR
	L	NOT USED	Ø 2 2C,2D				Ø 3 3B	Ø 4 4B					NOT USED	Ø3 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	Ø 5 5A	Ø 6 6A, 6B,6C	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø 6 6D, 6E,6F												

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			3
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y		2.0	
2C,2D	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			3
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			15
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			3
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			15
5A[1]	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A,6B,6C	TB3-5,6	J2U	40	2	6	6	Y	Y		2.0	
6D,6E,6F	TB3-7,8	J2L	44	6	16	6	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P31,P32	TB8-8,9	I13L	70	32	PED 8	3 PED					

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

[1]Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

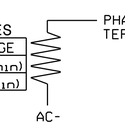
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 13-0068
DESIGNED: November 2015
SEALED: 8/11/2016
REVISED:

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

US 70 (Tunnel Road)
at
SR 2032 (New Haw Creek Road)
/ Crockett Avenue

Division 13 Buncombe County Asheville

PLAN DATE:	May 2016	REVIEWED BY:	BAS
PREPARED BY:	C. Strickland	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 030530 ENGINEER ZACHARY M. LITTLE

DocuSigned by: Zachary M. Little 8/19/2016
DC21EFD9E5341F
DATE

SIG. INVENTORY NO. 13-0068

18-AUG-2016 15:00
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