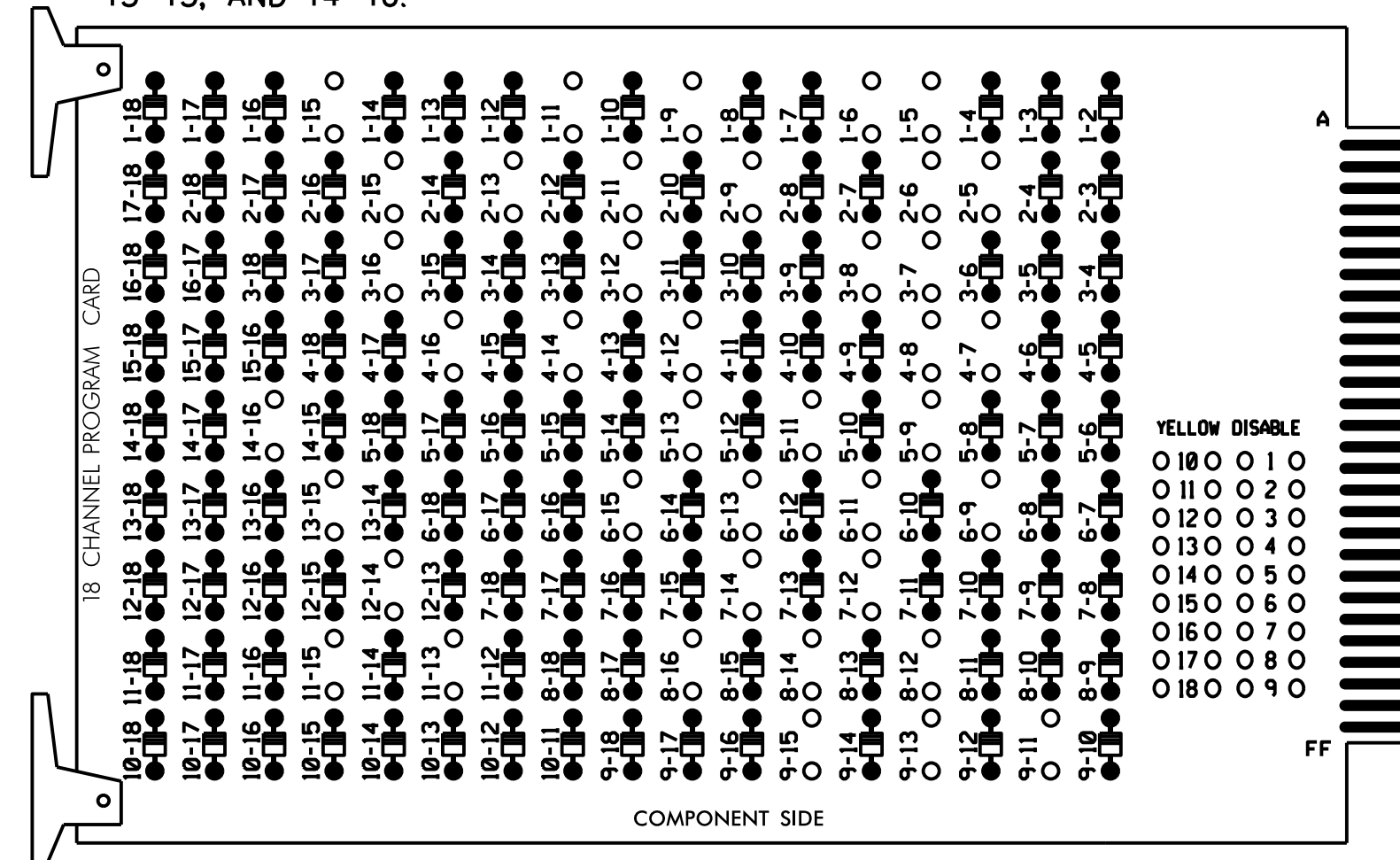


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-7, 3-8, 3-12, 3-16, 4-7, 4-8, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 7-12, 7-14, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 12-14, 13-15, AND 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 2 and 6, on the controller unit, for Start Up In Green.
- Enable Simultaneous Gap-Out, on the controller unit, for all phases.
- Program phase 8, on the controller unit, for Dual Entry.
- Program phases 2, 4, 6 and 8, for 'Start Up Ped Call'.
- The cabinet and controller are part of the City of Fayetteville Signal System.
- Program phases 2 and 6 for Yellow Flash.

EQUIPMENT INFORMATION

CONTROLLER.....SAFETRAN 2070E
 CABINET.....SAFETRAN 332 /W/ AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9
 S10,S11,S12,AUX S1,AUX S4,AUX S5
 PHASES USED.....1,2,2PED,3,4,4PED,5,6,6PED,7,8,8PED
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6					
CHNL CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18					
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE					
SIGNAL HEAD NO.	11	82	21,22	P21, P22	22	31,32	41,42	P41, P42	42	51	61,62	P61, P62	62	71	81,82	P81, P82	11	NU	NU	51	71	NU	
RED	*	128			101		*	134		*	107												
YELLOW		129			102			135			108												
GREEN		130			103			136			109												
RED ARROW					116																		
YELLOW ARROW	126			117	117			132			123												
FLASHING YELLOW ARROW																							
GREEN ARROW	127	127		118	118			133	133		124	124											
Hand				113				104			119												
Person								106			121												

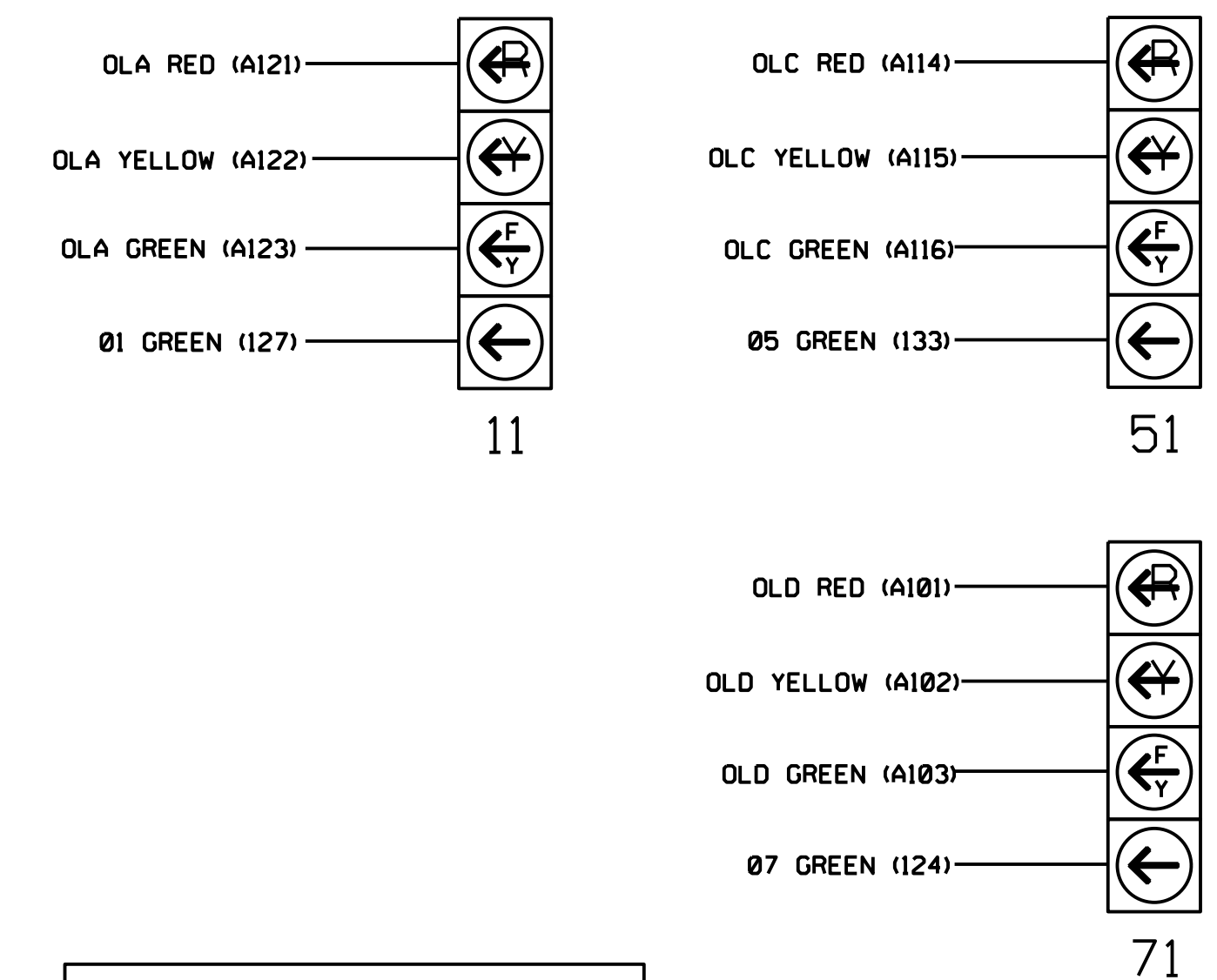
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

☆ See pictorial of head wiring in detail on page 2.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

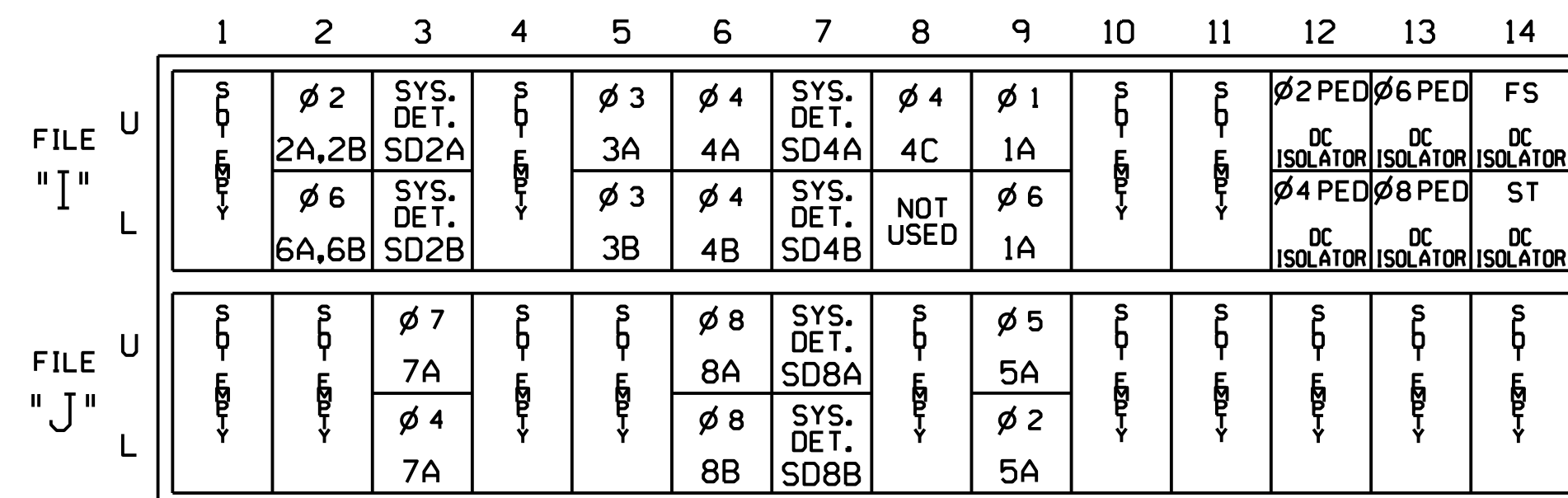
(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: C015
 DESIGNED: NOVEMBER 2016
 SEALED: 11/18/16
 REVISED:

INPUT FILE POSITION LAYOUT

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

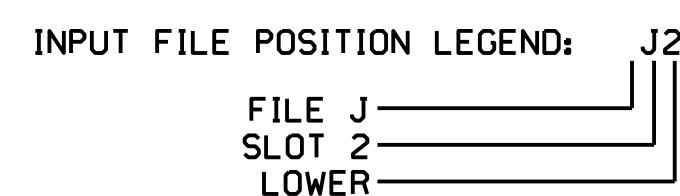
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
2A,2B	TB2-5,6	I2U	39	2	2	Y	Y		S
6A,6B	TB2-7,8	I2L	43	12	6	Y	Y		S
* SD2A	TB2-9,10	I3U	63	32	SYS				S
* SD2B	TB2-11,12	I3L	76	42	SYS				S
3A	TB4-5,6	I5U	58	3	3	Y	Y		S
3B	TB4-7,8	I5L	58	3	3	Y	Y		S
4A	TB4-9,10	I6U	41	4	4	Y	Y		S
4B	TB4-11,12	I6L	45	14	4	Y	Y		S
* SD4A	TB6-1,2	I7U	65	34	SYS				S
* SD4B	TB6-3,4	I7L	78	44	SYS				S
4C	TB6-5,6	I8U	49	24	4	Y	Y	20	S
1A ¹	TB6-9,10	I9U	60	11	1	Y	Y	15	S
	TB6-11,12	I9L	62	13	6	Y	Y		S
7A ²	TB3-9,10	J3U	64	36	7	Y	Y	15	S
	TB3-11,12	J3L	77	46	4	Y	Y		S
8A	TB5-9,10	J6U	42	8	8	Y	Y		S
8B	TB5-11,12	J6L	46	18	8	Y	Y	15	S
* SD8A	TB7-1,2	J7U	66	38	SYS				S
* SD8B	TB7-3,4	J7L	79	48	SYS				S
5A ³	TB7-9,10	J9U	59	15	5	Y	Y	15	S
	TB7-11,12	J9L	61	17	2	Y	Y		S
PED PUSH BUTTONS									
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED				
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED				
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED				
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED				

NOTE: Install DC isolators in input file slots I12 and I13.

- Add jumpers from I9-F to I9-W.
- Add jumpers from J3-F to J3-W.
- Add jumpers from J9-F to J9-W.

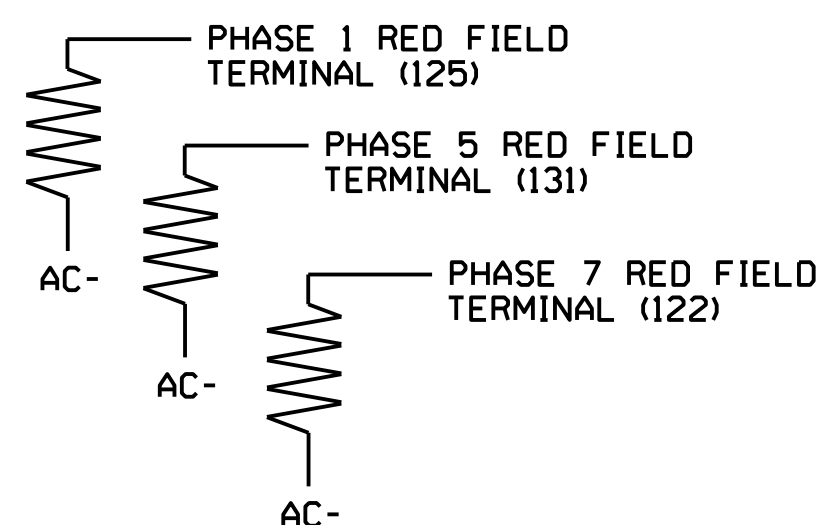


LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

NOTE: The purpose of these resistors is to load the channel red monitor inputs in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.



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 LICENSE NO. P4669



MCPHERSON CHURCH ROAD AT SYCAMORE DAIRY ROAD	
PLAN DATE: NOVEMBER 2016	REVIEWED BY: RWT
PREPARED BY: BLR	REVIEWED BY:
REVISIONS	INIT. DATE

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEERS
 SEAL 032711
 RUSSELL W. THOMPSON
 11/21/2016
 DATE
 SIG. INVENTORY NO. C015