

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP C

Select TMG VEH OVL P [C] and 'PPLT FYA'

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TMG VEH OVL P...[C] TYPE: .....[PPLT FYA]
PROTECTED PHASE (LEFT TURN)..... 5
PERMISSIVE PHASE (OPPOSING THRU)... 6
FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: C004
 DESIGNED: NOVEMBER 2016
 SEALED: 11/17/2016
 REVISED:

Electrical Detail Sheet 2 of 2

default \\NCF-DATA\Proj\360655_U-5742-Fay-Sig\Project\Signals\Design\100\FINAL SEALED PLANS\Revised 11/20/16\FtBragg.at.Churchill.dgn 11/17/2016 2:24:36 PM

Prepared In the Offices of:



Hatch Mott MacDonald

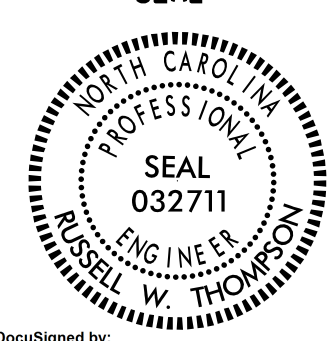
PO Box 700
Fayetteville, NC 27526
www.hatchmott.com

HATCH MOTT MACDONALD & E, LLC
LICENSE NO. F4669



FORT BRAGG ROAD AT CHURCHILL AVENUE																
DIV 06	CUMBERLAND COUNTY															
PLAN DATE: NOVEMBER 2016	REVIEWED BY: RWT															
PREPARED BY: BLR	REVIEWED BY:															
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SEAL



DocuSigned by:
Russell W Thompson 11/21/2016

SIGNATURE	DATE
SIG. INVENTORY NO. C004	