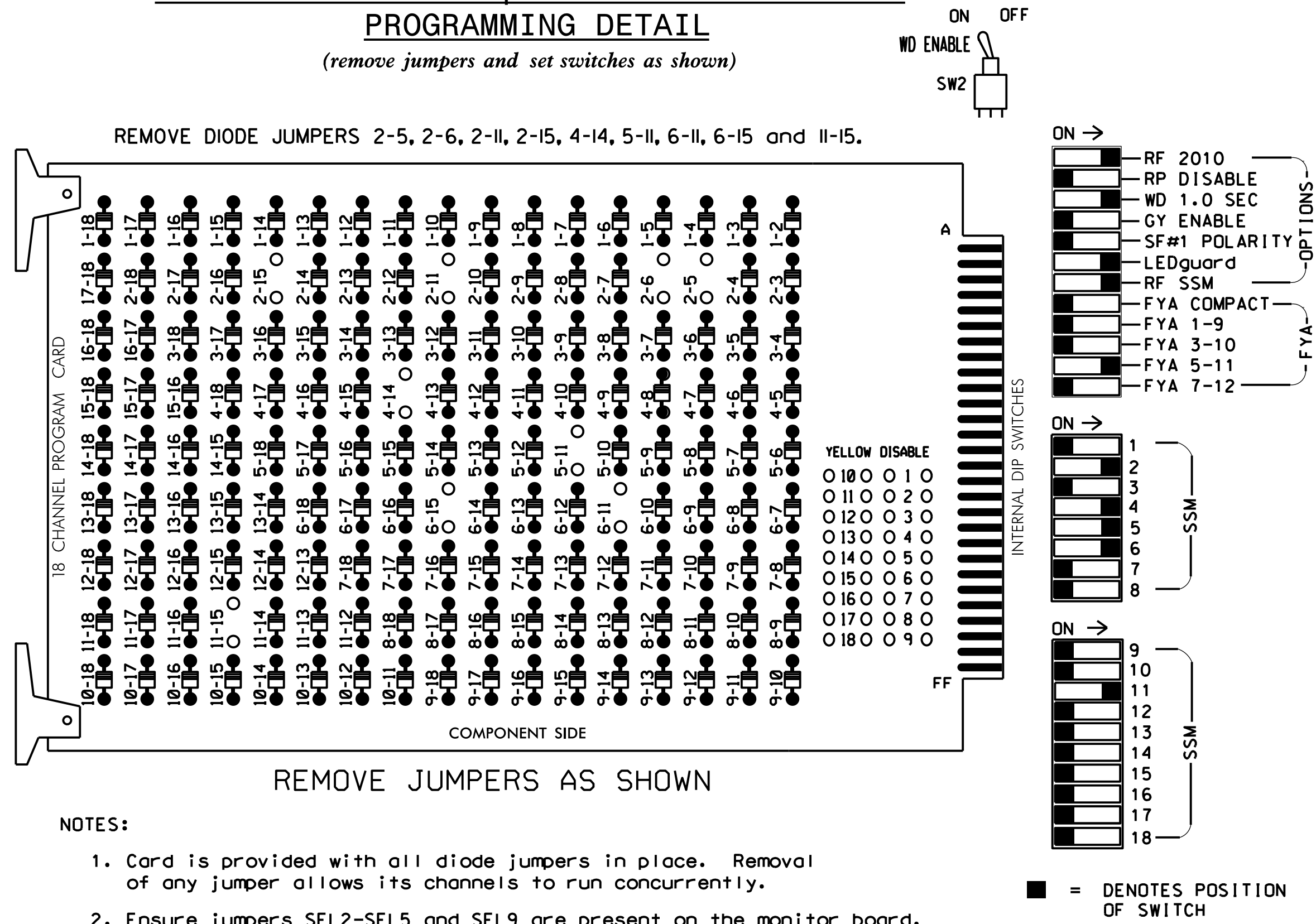


EDI MODEL 2018EClip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 2 and 6 for Yellow Flash.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 4 and 6 for 'STARTUP PED CALL'.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S5,S6,S7,S8,S9
 AUX S4
 PHASES USED.....2,4,5,6,2PED,4PED
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED
 * See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	
EMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	5	8 PED	OLA	OLB	SPARE	OLC	OLD	
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	62	P41, P42	42	51*	61,62	P61, P62	NU	NU	NU	NU	51*	NU	
RED	128			101	*		*			134								
YELLOW	129			102						135								
GREEN	130			103						136								
RED ARROW																	A114	
YELLOW ARROW					102		132											A115
FLASHING YELLOW ARROW																		A116
GREEN ARROW					103		133	133										
						104				119								
						106				121								

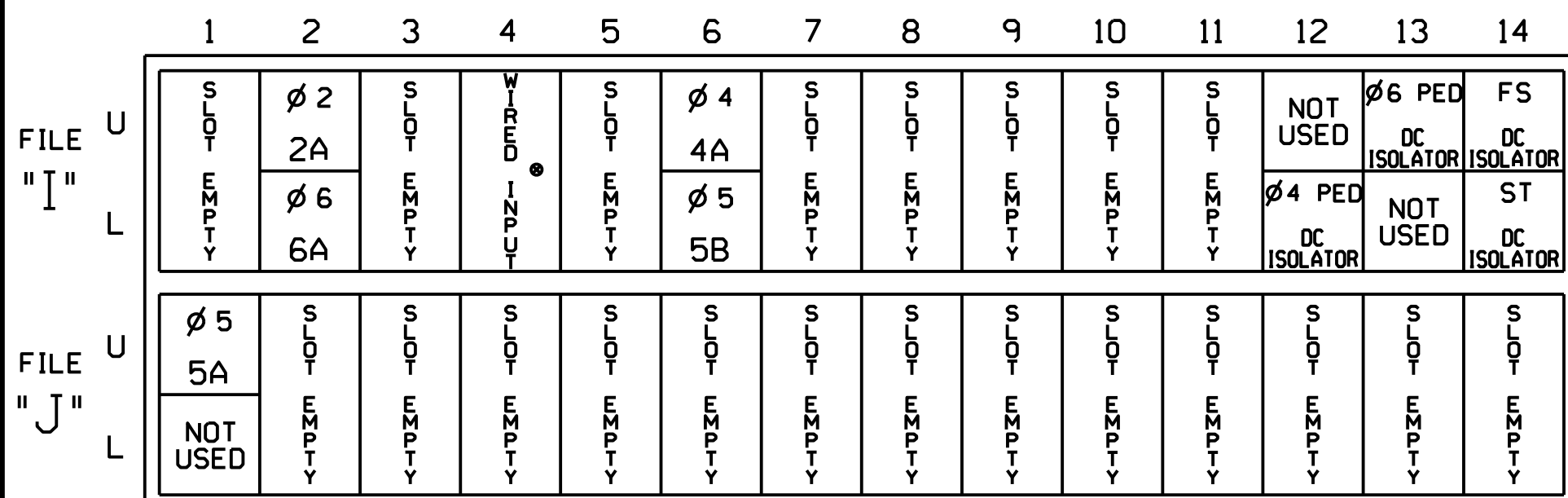
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

* See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



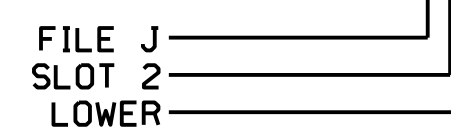
EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 * Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	EXTEND TIME	DELAY TIME	DETECTOR TYPE
2A	TB2-5,6	I2U	39	2	2			S
4A	TB4-9,10	I6U	41	4	4		3	S
5A	TB3-1,2	J1U	55	5	5		15	S
	TB3-3,4	I4U	47	22	2			G
5B	TB4-11,12	I6L	45	14	5		15	S
6A	TB2-7,8	I2L	43	12	6			S
P41,P42	TB8-5,6	I12U	69	PED 4	4PED			S
P61,P62	TB8-7,9	I13U	68	PED 6	6PED			S

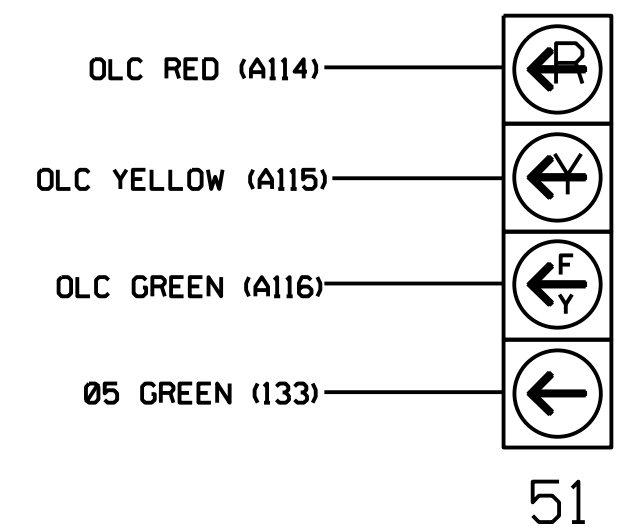
Add jumper from J1-F to I4-F.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

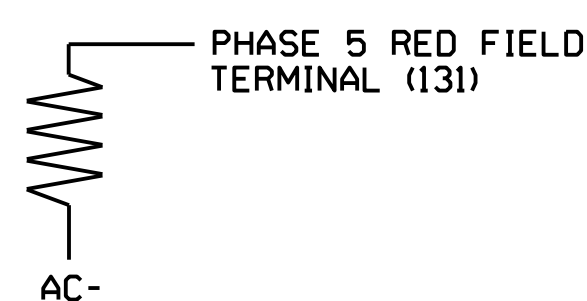


LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: C004
 DESIGNED: NOVEMBER 2016
 SEALED: 11/17/2016
 REVISED:

Electrical Detail Sheet 1 of 2

Prepared In the Offices of
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 HATCH MOTT MACDONALD & E, LLC
 LICENSE NO. P4663

FORT BRAGG ROAD AT CHURCHILL AVENUE

DIV 06 CUMBERLAND COUNTY FAYETTEVILLE

PLAN DATE: NOVEMBER 2016 REVIEWED BY: RWT
 PREPARED BY: BLR REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by:
Russell W Thompson 11/21/2016
 SEAL 032711
 SIG. INVENTORY NO. C004

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