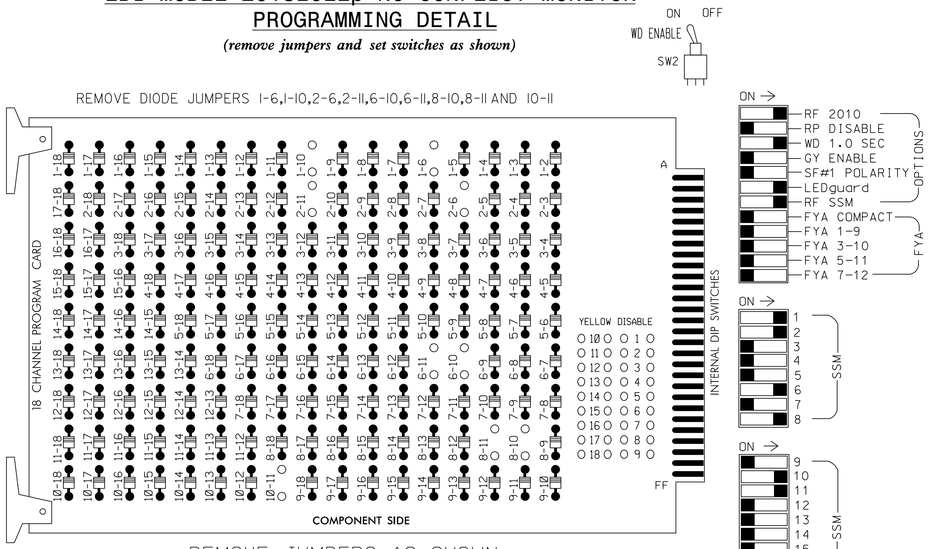
PROJECT REFERENCE NO.	SHEET NO.
U-5742	Sig.238.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for volume density operation.
4. Program controller to start up in phase 2 Green and 6 Green.
5. The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S2, AUX S4
PHASES USED.....1,2,6,8
OVERLAP "A".....NOT USED
OVERLAP "B".....1+8
OVERLAP "C".....2+8
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	21,22	NU	NU	NU	NU	NU	61,62	NU	NU	81,82 83	NU	NU	84	NU	23,24	NU	NU
RED		128						134						A124		A114		
YELLOW		129						135										
GREEN		130						136										
RED ARROW	125										107							
YELLOW ARROW	126										108			A125		A115		
GREEN ARROW	127										109			A126		A116		

NU = Not Used

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select [2. CONTROLLER]
2. From CONTROLLER Submenu select [2. VEHICLE OVERLAPS]

Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'

```
TMG VEH OVLP...[B] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . . . X . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'NORMAL'

```
TMG VEH OVLP...[C] TYPE: .......NORMAL
  PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . X . . . . . X . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

END PROGRAMMING

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø 1 1A	Ø 1 1B	Ø2/SYS 2A/S2A	Ø2/SYS 2C/S2C	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED	Ø 1 1C	Ø2/SYS 2B/S2B	NOT USED										ST DC ISOLATOR
FILE "J"	U	SLOT EMPTY	Ø6/SYS 6A/S6A	Ø6/SYS 6C/S6C	SLOT EMPTY	SLOT EMPTY	Ø 8 8A	Ø 8 8C	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L		Ø6/SYS 6B/S6B	NOT USED			Ø 8 8B	NOT USED							

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES			S
1B	TB2-5,6	I2U	39	2	1	YES			S
1C	TB2-7,8	I2L	43	12	1	YES		15	S
2A/S2A	TB2-9,10	I3U	63	32	2/SYS	YES			N
2B/S2B	TB2-11,12	I3L	76	42	2/SYS	YES			N
2C/S2C	TB4-1,2	I4U	47	22	2/SYS	YES			N
6A/S6A	TB3-5,6	J2U	40	6	6/SYS	YES			N
6B/S6B	TB3-7,8	J2L	44	16	6/SYS	YES			N
6C/S6C	TB3-9,10	J3U	64	36	6/SYS	YES			N
8A	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES			S
8C	TB7-1,2	J7U	66	38	8	YES			S

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-1312
DESIGNED: May 2016
SEALED: 7/20/2016
REVISED:

Electrical Detail

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared For: Transportation Mobility and Safety Division, Traffic Engineering Branch, Signals Management Section, Department of Transportation, State of North Carolina
750 N.Greenfield Pkwy,Garner,NC 27529

Honeycutt Road at NC 210 (Murchison Road) NB Ramps

Division 6 Cumberland County Fort Bragg

PLAN DATE: August 2016	REVIEWED BY: SL Phillips
PREPARED BY: SP Pennington	REVIEWED BY: KP Baumann

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 032607 ENGINEER STACIE L. PHILLIPS

DocuSigned by: 0C87A59ED60B437... 9/22/2016
DATE

SIG. INVENTORY NO. 06-1312

PLANS PREPARED IN THE OFFICE OF:
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9/21/2016
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