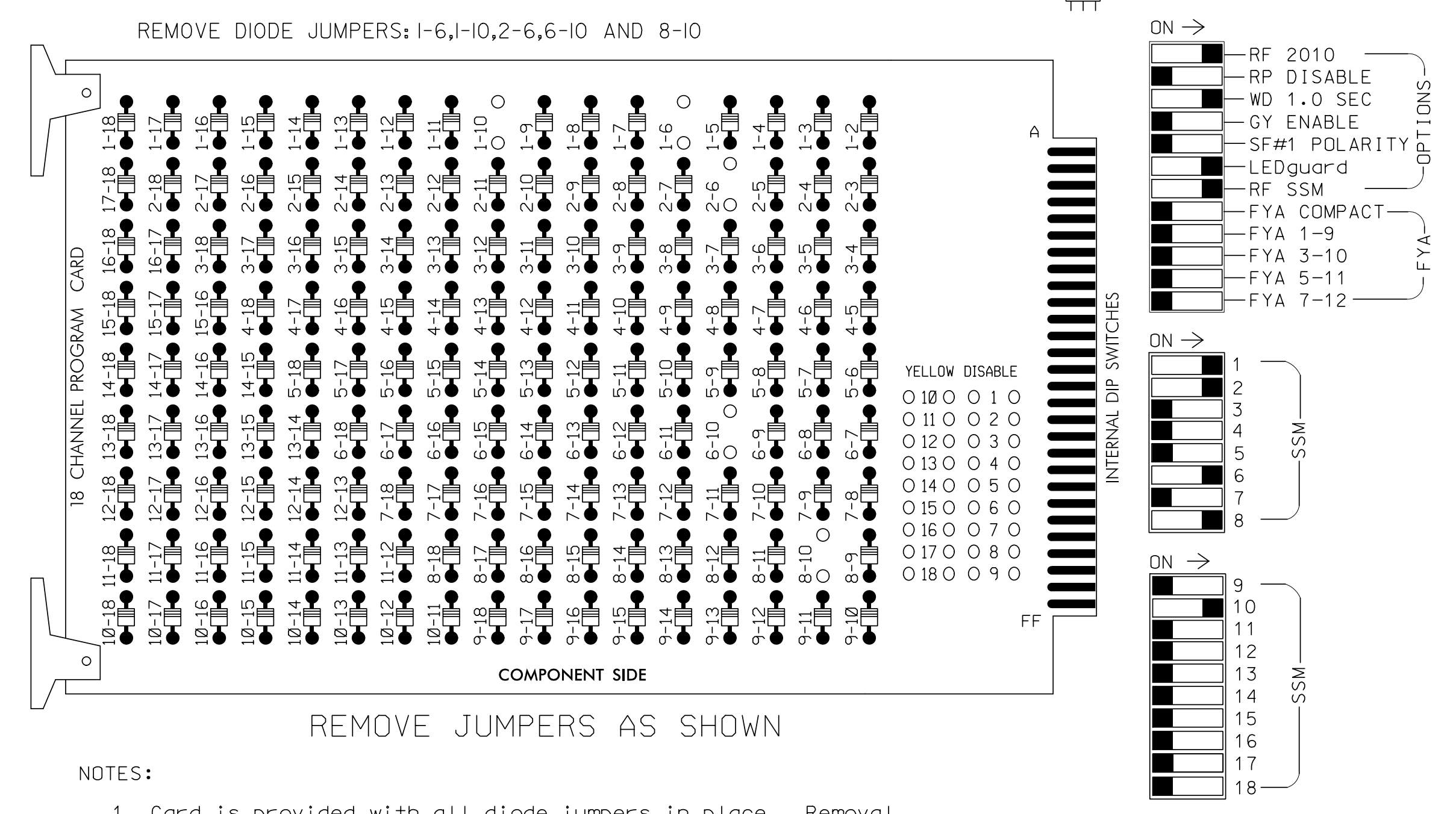


### EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

### NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for volume density operation.
4. Program controller to start up in phase 2 Green and 6 Green.
5. The cabinet and controller are part of the Fayetteville Signal System.

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	21,22 23	NU	NU	NU	NU	NU	61,62	NU	NU	81,82 83	NU	13,14	NU	NU	NU	NU	NU
RED		128						134					A124					
YELLOW		129						135										
GREEN		130						136										
RED ARROW	125										107							
YELLOW ARROW	126										108		A125					
GREEN ARROW	127										109		A126					

NU = Not Used

### EQUIPMENT INFORMATION

CONTROLLER.....2070E  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S2  
 PHASES USED.....1,2,6,8  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....1+8  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....NOT USED

### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select **2. CONTROLLER**
2. From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

TOGGLE ONCE

OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'

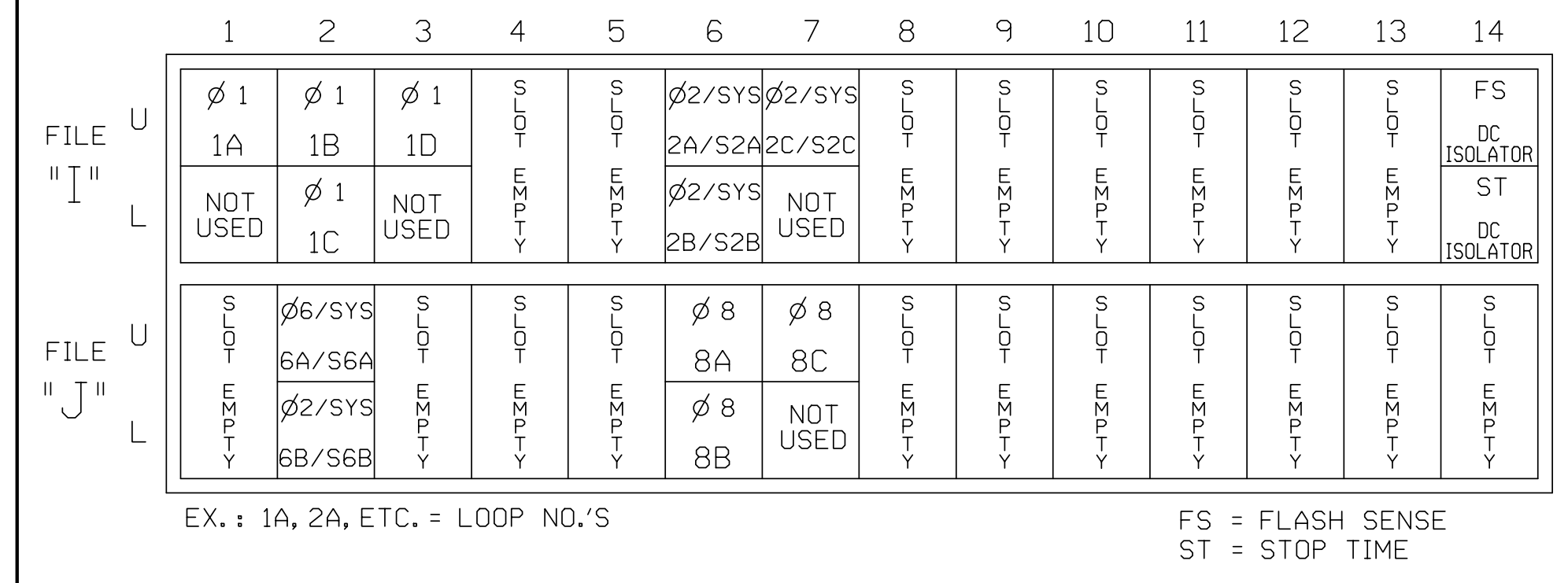
TMG VEH OVLP...[B] TYPE: .....**NORMAL**  
 PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6  
 INCLUDED X . . . . . X . . . . .  
 LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1302  
 DESIGNED: May 2016  
 SEALED: 5/20/2016  
 REVISED:

### INPUT FILE POSITION LAYOUT

(front view)



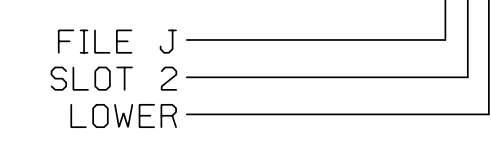
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES			S
1B	TB2-5,6	I2U	39	2	1	YES			S
1C	TB2-7,8	I2L	43	12	1	YES		10	S
1D	TB2-9,10	I3U	63	32	1	YES		10	S
2A/S2A	TB4-9,10	I6U	41	4	2	YES			N
2B/S2B	TB4-11,12	I6L	45	14	2	YES			N
2C/S2C	TB6-1,2	I7U	65	34	2	YES			N
6A/S6A	TB3-5,6	J2U	40	6	6	YES			N
6B/S6B	TB3-7,8	J2L	44	16	6	YES			N
8A	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES			S
8C	TB7-1,2	J7U	66	38	8	YES			S

INPUT FILE POSITION LEGEND: J2L



### FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the same approach, make the following flasher circuit changes:

1. On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
2. On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

8/21/2016 K:\REAL\_TPOK\SIGNALS\4011036345 Fayetteville File Electrical\061302-2016e.dgn Susan Pennington

Electrical Detail

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED**

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NC 87-210 (Murchison Road) at I-295 WB Ramp/Loop B

Division 6 Cumberland County Fayetteville

PLAN DATE: July 2016 REVIEWED BY: KP Baumann

PREPARED BY: SP Pennington REVIEWED BY: SL Phillips

REVISIONS INIT. DATE

9/1/2016

SIG. INVENTORY NO. 06-1302