ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP[A]	TYPE:PP	LT FYA
PROTECTED LEFT TURNA OPPOSING THROUGH		
FLASHING ARROW OUTPU	JTCH9 I	SOLATE
DELAY START OF: FYA. ACTION PLAN SF BIT [
	Tog	gle Twice

OVERLAP C

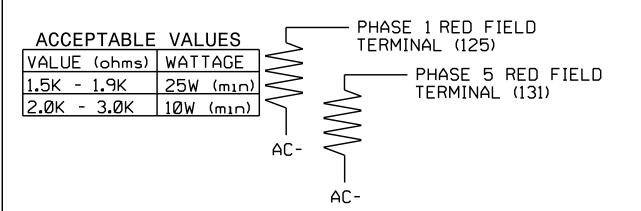
Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP[C] TYPE:PPLT FYA
PROTECTED LEFT TURN PHASE 5 OPPOSING THROUGH PHASE 6
FLASHING ARROW OUTPUTCH11 ISOLATE
DELAY START OF: FYAO.O CLEARANCEO.O ACTION PLAN SF BIT DISABLE

END PROGRAMMING

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



NOTE: The purpose of these resistors is to load the channel red monitor inputs in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-1148 DESIGNED: August 2016 SEALED: 10/14/2016 REVISED:

Electrical Detail Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DETAILS FOR:

ELECTRICAL AND PROGRAMMING

SR 1415 (Yadkin Road) Walmart/Walgreens and

Federal Credit Union Entrances

PLAN DATE: October 2016 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 06-1148