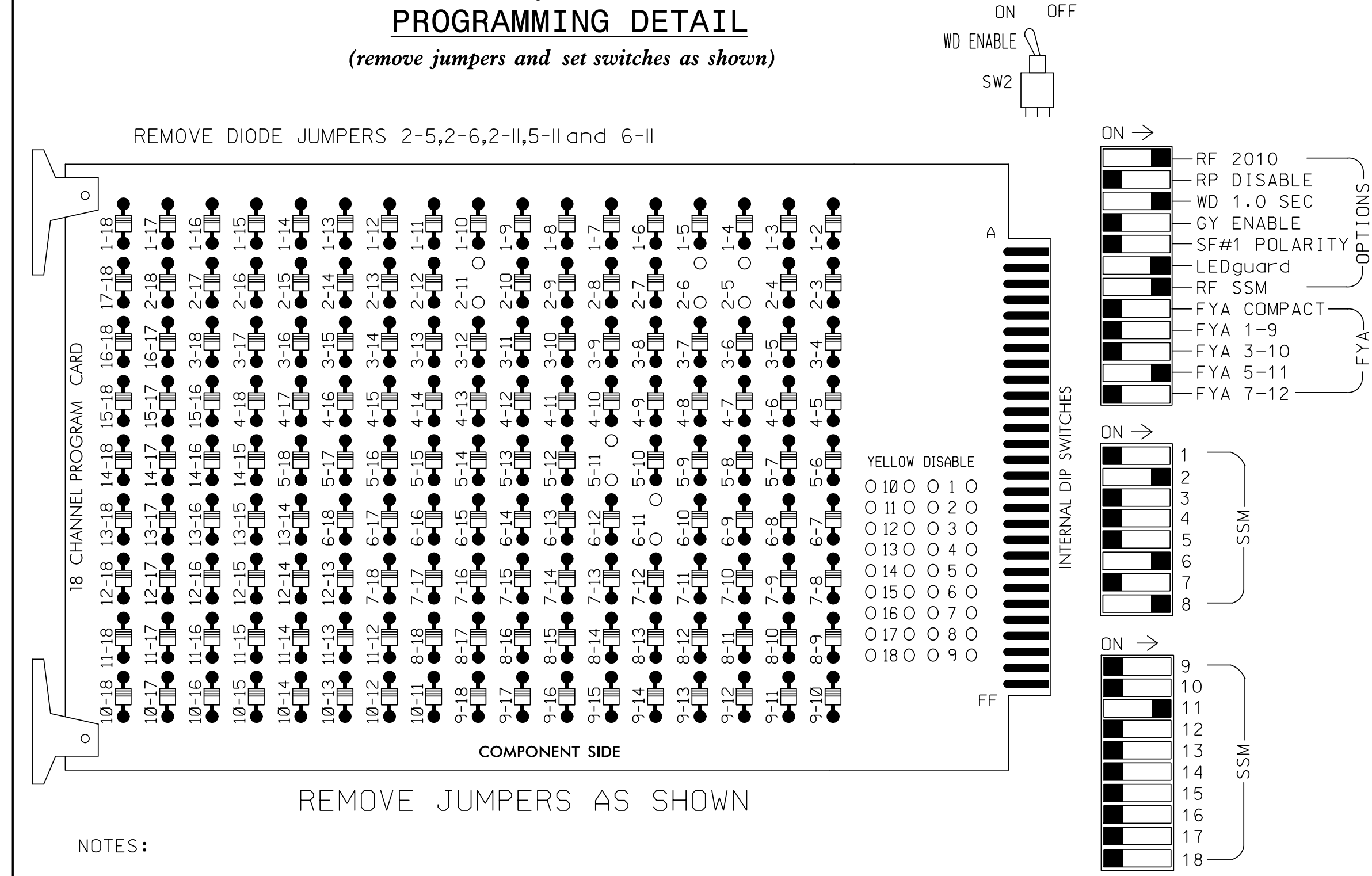


EDI MODEL 2018EClip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S7,S8,S11
 AUX S4
 PHASES USED.....2,5,6,8
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....*
 OVERLAP "D".....NOT USED
 * See overlap programming detail on this sheet

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	51*	61,62	NU	NU	81,82	NU	NU	NU	51*	NU	NU	NU	
RED		128						134			107								
YELLOW		129					*	135			108								
GREEN		130						136			109								
RED ARROW																		A114	
YELLOW ARROW																			A115
FLASHING YELLOW ARROW																			A116
GREEN ARROW								133											

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 2	2A,2B	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	FS
L	NOT USED													DC ISOLATOR
U	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17	FS
L	NOT USED	∅ 6					8A	8C						DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

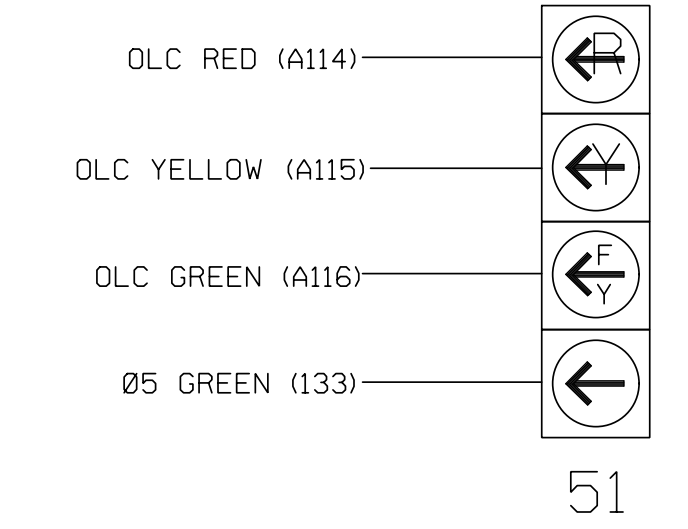
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
2A,2B	TB2-5,6	I2U	39	2	2	YES			N
5A ¹	TB3-1,2	J1U	55	5	5	YES		15	S
		I4U	47	22	2	YES		3	G
6A	TB3-5,6	J2U	40	6	6	YES			N
6B	TB3-7,8	J2L	44	16	6	YES			N
8A	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES		5	S
8C	TB7-1,2	J7U	66	38	8	YES		15	S

¹Add jumper from J1-W to I4-W, on rear of input file.
 INPUT FILE POSITION LEGEND: J2L
 FILE J
 SLOT 2
 LOWER

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**
 Toggle twice
 OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE:PPLT FYA

PROTECTED LEFT TURN.... PHASE 5
 OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
 ACTION PLAN SF BIT DISABLE..... 0

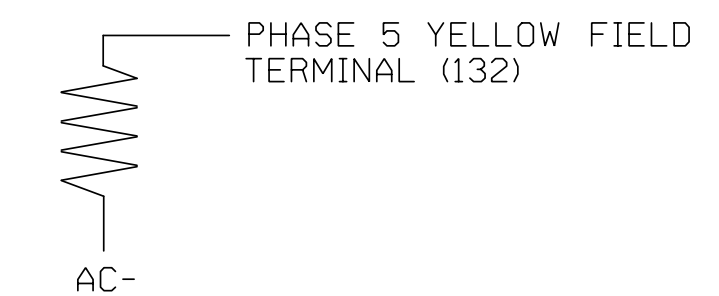
END PROGRAMMING

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0545
 DESIGNED: July 2016
 SEALED: 8/3/2016
 REVISED:

Electrical Detail

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared For: **Kimley-Horn**

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Division 6 Cumberland County Fayetteville

PLAN DATE: August 2016 REVIEWED BY: SL Phillips
 PREPARED BY: SP Pennington REVIEWED BY: KP Baumann

REVISIONS: INIT. DATE

Seal: NORTH CAROLINA PROFESSIONAL ENGINEER STACIE L. PHILLIPS 032607

9/22/2016

SIG. INVENTORY NO. 06-0545

9/21/2016 K:\REAL_T\10K\SIGNALS\4011036345 Fayetteville Electrical\Colls\654 - Signal Design\5th_Submit\Final\4171_060545-2016e.dgn Susan Pennington