ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

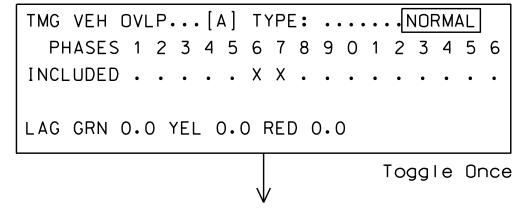
(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

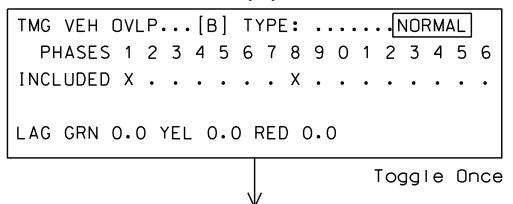
OVERLAP A

Select TMG VEH OVLP [A] and 'NORMAL'



OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'



OVERLAP C

Select TMG VEH OVLP [C] and 'NORMAL'

TMG VEH OVLP...[C] TYPE:NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the Same approach, make the following flasher circuit changes:

- 1. On rear of PDA remove wire from Term, T2-4 and terminate on T2-2.
- 2. On rear of PDA remove wire from Term, T2-5 and terminate on T2-3.
- 3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0422 DESIGNED: January 2016 SEALED: 7/5/2016 REVISED:

Electrical Detail - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING DETAILS FOR:

US 401 Bus. (Raeford Road) SR 1219 (Ireland Drive)/

Shopping Center Drive Fayetteville

PLAN DATE: June 2016 REVIEWED BY: PREPARED BY: C. Strickland REVIEWED BY: REVISIONS

INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED