ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

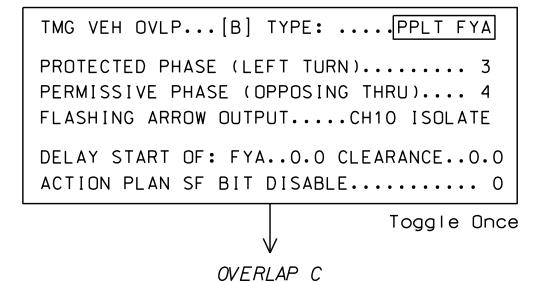
OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

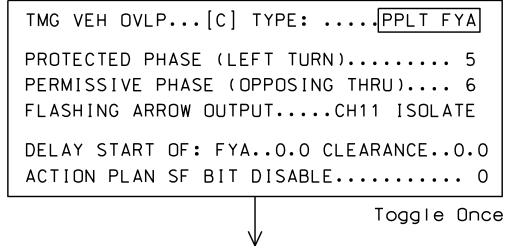
TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED PHASE (LEFT TURN)..... 1 PERMISSIVE PHASE (OPPOSING THRU).... 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'



Select TMG VEH OVLP [C] and 'PPLT FYA'



OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

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TMG VEH OVLP...[D] TYPE: .... PPLT FYA
PROTECTED PHASE (LEFT TURN)..... 7
PERMISSIVE PHASE (OPPOSING THRU).... 8
FLASHING ARROW OUTPUT....CH12 ISOLATE
DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE..... 0
```

END PROGRAMMING

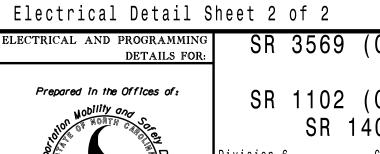
FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the Same approach, make the following flasher circuit changes:

- 1. On rear of PDA remove wire from Term, T2-4 and terminate on T2-2.
- 2. On rear of PDA remove wire from Term. T2-5 and terminate on T2-3.
- 3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0388 DESIGNED: January 2016 SEALED: 6-03-16 REVISED: N/A



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 3569 (Old Raeford Road) SR 1102 (Gillis Hill Road)/

SR 1402 (Rim Road) Cumberland County Fayetteville

PLAN DATE: May 2016 REVIEWED BY: PREPARED BY: James Peterson REVIEWED BY: REVISIONS INIT. DATE SIG. INVENTORY NO. 06-0388