

## ECONOLITE ASC/3 LOGIC PROCESSOR PROGRAMMING DETAIL

*(program controller as shown)*

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 1 COPY FROM: 1 ACTIVE: M FALSE
IF   CTR PHASE TIMING 1 IS ON

THEN SIG SET OVLP GREEN 1 OFF

ELSE
  
```

LOGIC FOR TURNING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEADS 83,84)

ENTER A "2" IN THE LP# FIELD. PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 2 COPY FROM: 2 ACTIVE: M FALSE
IF   VEH YELLOW ON PH 1 IS ON

THEN SIG SET OLP YELLOW 1 ON

ELSE
  
```

LOGIC FOR YELLOW ARROW CLEARANCE WHEN LEAVING PHASE 1 (HEADS 83,84)

ENTER A "3" IN THE LP# FIELD. PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 3 COPY FROM: 3 ACTIVE: M FALSE
IF   CTR PHASE TIMING 1 IS ON
AND  VEH RED ON PHASE 1 IS ON

THEN SIG SET OLP RED 1 ON

ELSE
  
```

LOGIC FOR RED SIGNAL CLEARANCE WHEN LEAVING PHASE 1 (HEADS 83,84)

END PROGRAMMING

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-3 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
LP 1-15	E	E	E	.	.	.	.	.	.	.	.	.	.	.	.	.
LP 16-30	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
LP 31-45	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
LP 46-60	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
LP 61-75	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.
LP 76-90	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.	.

END PROGRAMMING

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

*(program controller as shown)*

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

### OVERLAP A

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'

```

TMG VEH OVLP...[A] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . . X . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN 1 . . . . . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
  
```

END PROGRAMMING

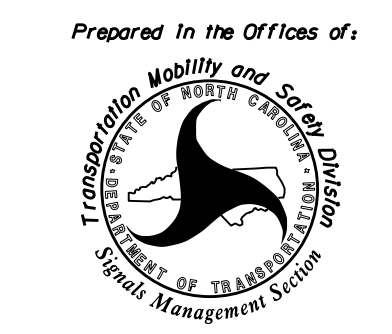
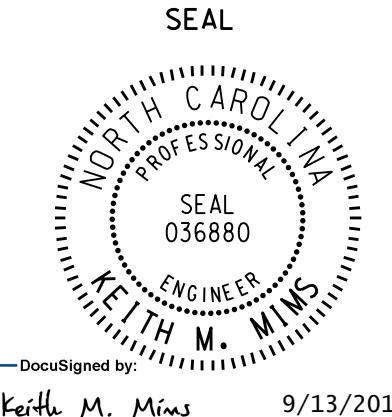
THIS ELECTRICAL DETAIL IS FOR  
 THE SIGNAL DESIGN: 06-0369  
 DESIGNED: February 2016  
 SEALED: 8-24-16  
 REVISED: N/A

## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Electrical Detail Sheet 2 of 2		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
ELECTRICAL AND PROGRAMMING DETAILS FOR:   Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	<b>SR 1403 (Reilly Road) at SR 1404 (Morganton Road)</b>  Division 6 Cumberland County Fayetteville PLAN DATE: June 2016 REVIEWED BY: BAS PREPARED BY: James Peterson REVIEWED BY:	SEAL  DocuSigned by: Keith M. Mims 9/13/2016 2F8078EBCD3445 DATE SIG. INVENTORY NO. 06-0369
REVISIONS: _____ INIT. DATE _____		

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