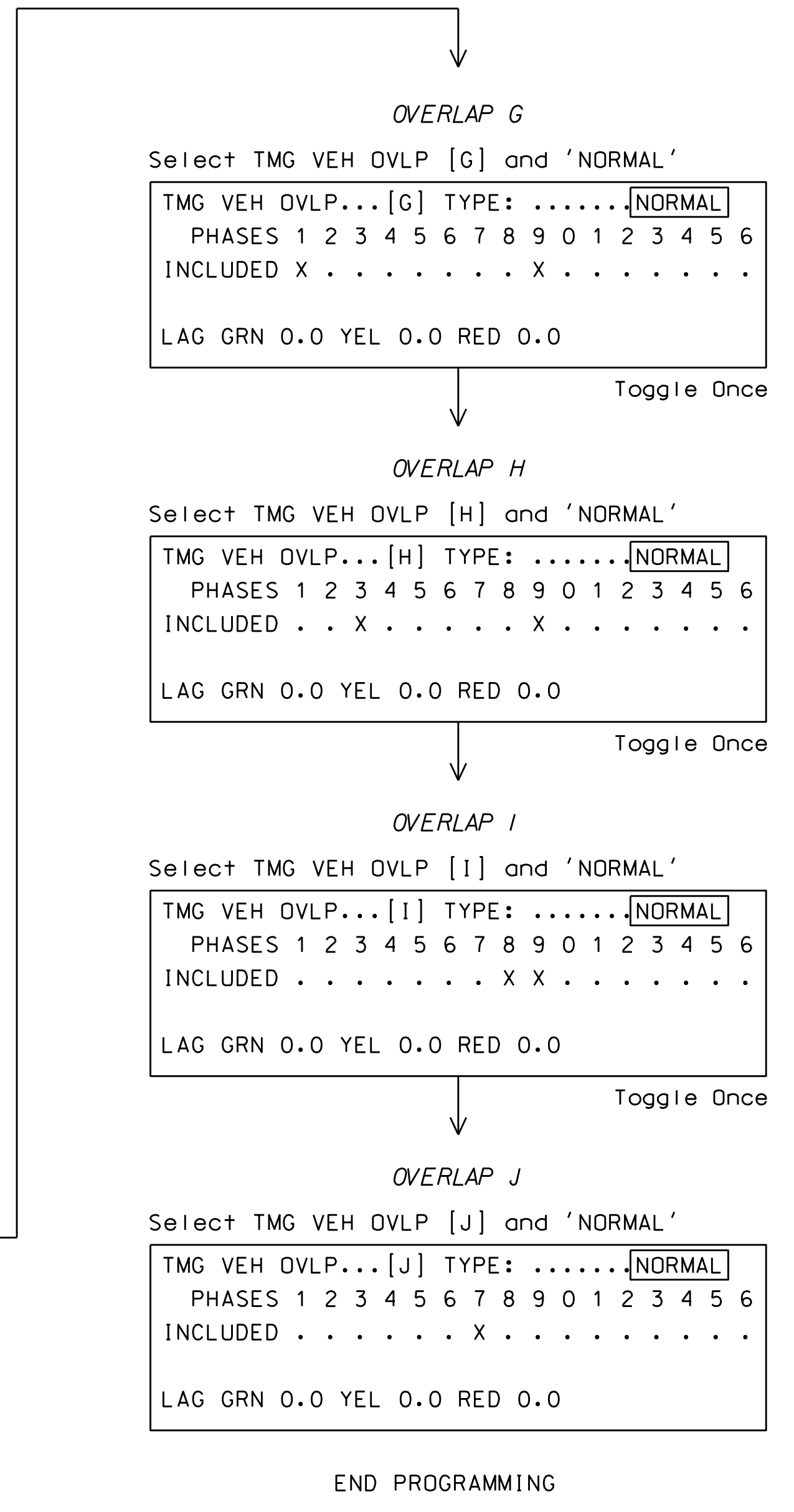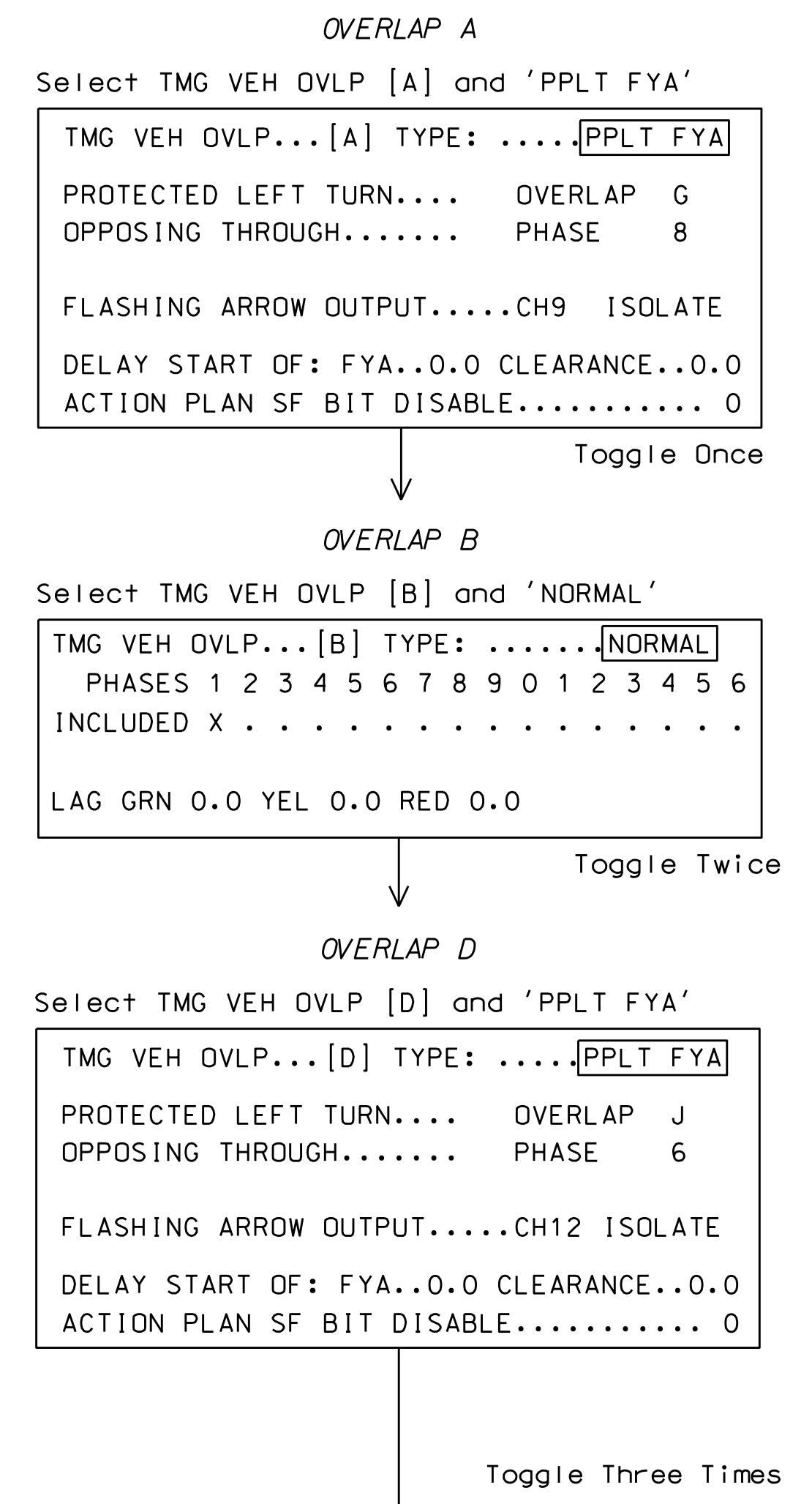# ECONOLITE ASC/3-2070
OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select **2. CONTROLLER**

2. From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN....    OVERLAP  G
OPPOSING THROUGH.......    PHASE    8

FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'

```
TMG VEH OVLP...[B] TYPE: ...... NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . . . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```
Toggle Twice

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

```
TMG VEH OVLP...[D] TYPE: ..... PPLT FYA

PROTECTED LEFT TURN....    OVERLAP  J
OPPOSING THROUGH.......    PHASE    6

FLASHING ARROW OUTPUT.....CH12 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 0
```
Toggle Three Times

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```
TMG VEH OVLP...[G] TYPE: ...... NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . . . . . . X . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```
Toggle Once

OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```
TMG VEH OVLP...[H] TYPE: ...... NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . . . . . X . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```
Toggle Once

OVERLAP I

Select TMG VEH OVLP [I] and 'NORMAL'

```
TMG VEH OVLP...[I] TYPE: ...... NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . . X X . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```
Toggle Once

OVERLAP J

Select TMG VEH OVLP [J] and 'NORMAL'

```
TMG VEH OVLP...[J] TYPE: ...... NORMAL
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . . . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0
```

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the
Same approach, make the following flasher circuit changes:

1. On rear of PDA - remove wire from Term. T2-4 and terminate on T2-2.
2. On rear of PDA - remove wire from Term. T2-5 and terminate on T2-3.
3. On rear of PDA - locate wire that connects terminal O1-6 to terminal TA-2
   on the rear of the auxiliary output file. Remove this wire from terminal
   O1-6 and terminate it on terminal O1-5.
4. Remove flasher unit #2

The changes listed above tie all phases and overlaps to flasher unit #1 as follows:

Flasher Unit #1, Circuit #1 O1, O6
                            O3, O8
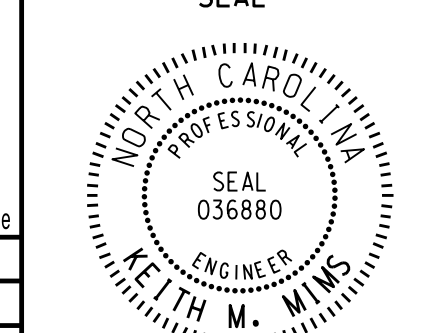   OLA, OLB, OLC, OLD, OLE, OLF

Flasher Unit #1, Circuit #2 O2, O5
                            O4, O7

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during
Ped Clearance Interval. Consult Ped Signal Module user's manual
for instructions on selecting this feature.

Electrical Detail - Sheet 2 of 4

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0364
DESIGNED: January 2016
SEALED: 10/4/2016
REVISED: N/A