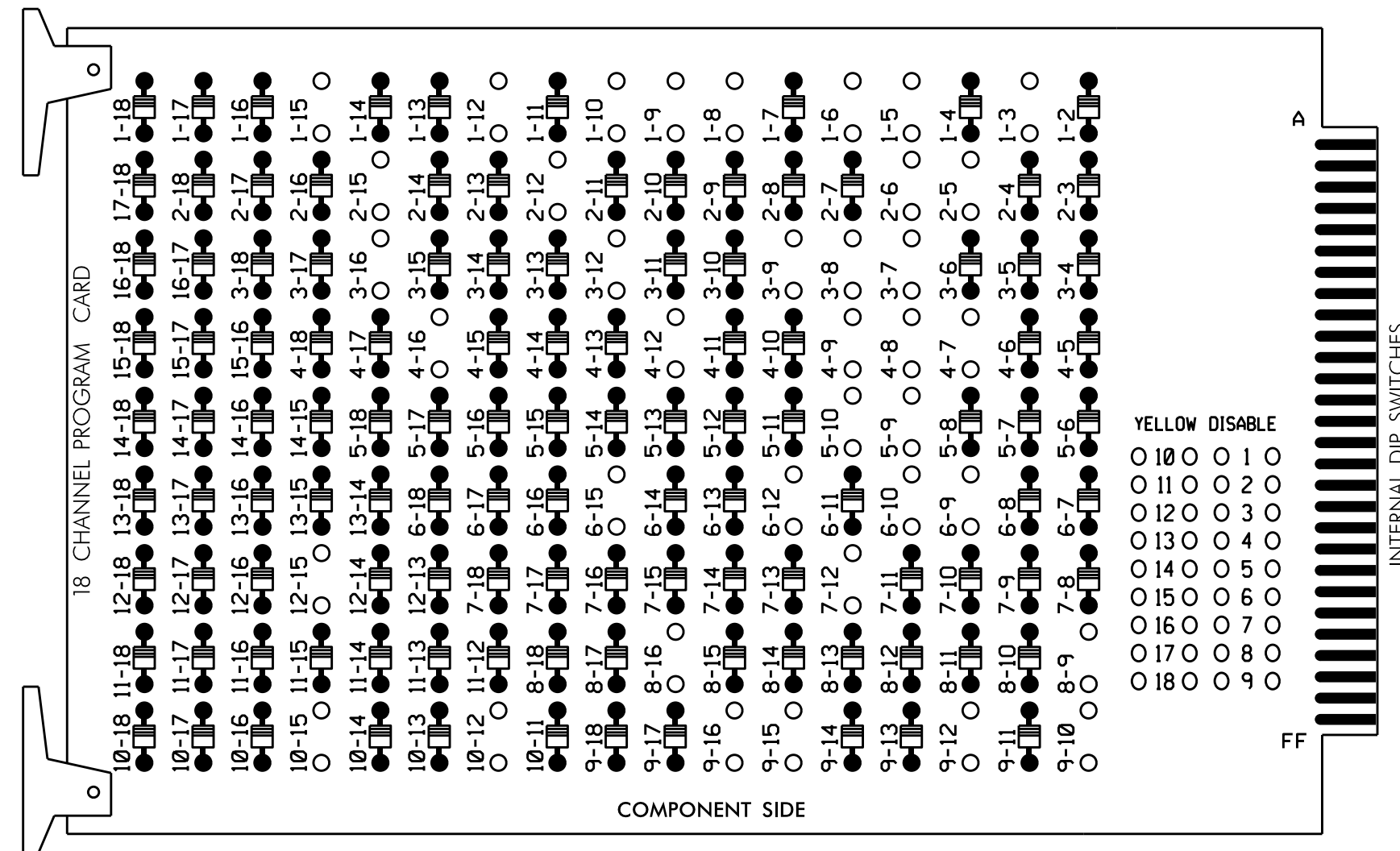


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

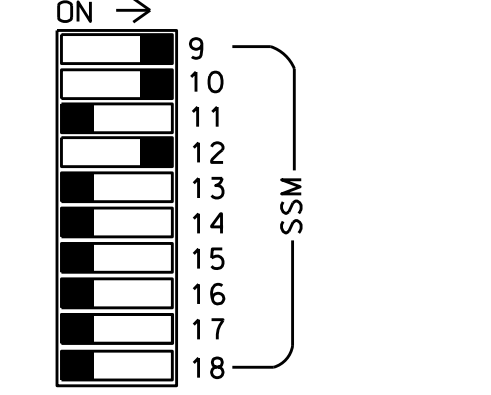
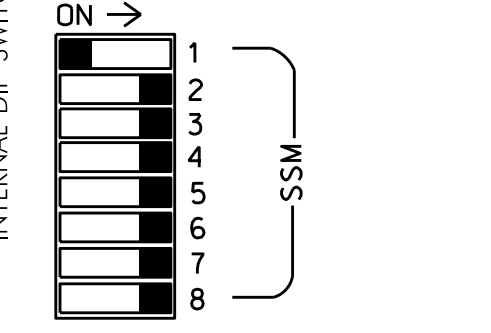
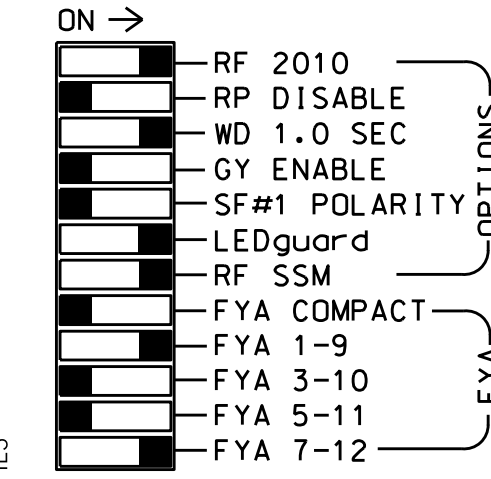
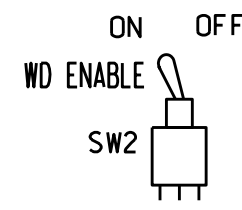
REMOVE DIODE JUMPERS 1-3, 1-5, 1-6, 1-8, 1-9, 1-10, 1-12, 1-15, 2-5, 2-6, 2-12, 2-15, 3-7, 3-8, 3-9, 3-12, 3-16, 4-7, 4-8, 4-9, 4-12, 4-16, 5-9, 5-10, 6-9, 6-10, 6-12, 6-15, 7-12, 8-9, 8-16, 9-10, 9-12, 9-15, 9-16, 10-12, 10-15, and 12-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Green and 6 Walk.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S9,S10,
 S11,S12,AUX S1,AUX S2,AUX S5
 PHASES USED.....1,2,3,4,5,6,6PED,7,8,8PED,9*
 OVERLAP A.....**
 OVERLAP B.....1
 OVERLAP C.....NOT USED
 OVERLAP D.....**
 OVERLAP G.....1+9
 OVERLAP H.....3+9
 OVERLAP I.....8+9
 OVERLAP J.....7

* Phase 9 used during preempt only.
 ** See overlap programming detail on sheet 2.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	OLG	2	2 PED	OLH	4	4 PED	5	6	6 PED	7	OL1	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	83,84	21,22	NU	31,32	41,42	NU	51	61,62	P61, P62	63	71,72	81,82	P81, P82	83,84	11,12	NU	NU	63	NU
RED		128			101			134				107	A121					A101	
YELLOW	*	129			102			135				108							
GREEN		130			103			136				109							
RED ARROW				116			131				122			A124					
YELLOW ARROW				117			132				123			A122	A125			A102	
FLASHING YELLOW ARROW														A123				A103	
GREEN ARROW	127			118			133			124	124			A126					
Hand icon									119			110							
Walking person icon									121			112							

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE	U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	U	∅ 1	∅ 1	∅ 2	S	∅ 3	∅ 3	∅ 4	S	∅ 1	S	S	S	∅ 6 PED	FS
	L	1A	1C	2A	←	3A	3C	4A	←	1E	←	←	←	DC ISOLATOR	DC ISOLATOR
"J"	U	∅ 1	∅ 1	∅ 2	S	∅ 3	∅ 3	∅ 4	S	∅ 1	S	S	S	∅ 8 PED	ST
	L	1B	1D	2B	←	3B	3D	4B	←	1F	←	←	←	DC ISOLATOR	DC ISOLATOR
"U"	U	∅ 5	∅ 6	S	S	∅ 7	∅ 8	∅ 8	S	S	S	S	S	S	PRE1
	L	5A	6A	←	←	7A	8A	8C	←	←	←	←	←	AC ISOLATOR	
"L"	U	NOT USED	∅ 6	S	S	∅ 7	∅ 8	∅ 8	S	S	S	S	S	NOT USED	
	L	6B	←	←	7B	8B	8D	←	←	←	←	←	←		

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME
 PRE1 = RR PREEMPT

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES			S
1B	TB2-3,4	I1L	56	1	1	YES			S
1C	TB2-5,6	I2U	39	2	1	YES		10	S
1D	TB2-7,8	I2L	43	12	1	YES		10	S
1E	TB6-9,10	I9U	60	11	1	YES		10	S
1F	TB6-11,12	I9L	62	13	1	YES		10	S
2A	TB2-9,10	I3U	63	32	2	YES			N
2B	TB2-11,12	I3L	76	42	2	YES			N
3A	TB4-5,6	I5U	58	3	3	YES			S
3B	TB4-7,8	I5L	58	3	3	YES			S
3C	TB4-9,10	I6U	41	4	3	YES			S
3D	TB4-11,12	I6L	45	14	3	YES			S
4A	TB6-1,2	I7U	65	34	4	YES			S
4B	TB6-3,4	I7L	78	44	4	YES			S
5A	TB3-1,2	J1U	55	5	5	YES			S
6A	TB3-5,6	J2U	40	6	6	YES			N
6B	TB3-7,8	J2L	44	16	6	YES			N
7A	TB5-5,6	J5U	57	7	7	YES			S
7B	TB5-7,8	J5L	57	7	7	YES			S
8A	TB5-9,10	J6U	42	8	8	YES			S
8B	TB5-11,12	J6L	46	18	8	YES			S
8C	TB7-1,2	J7U	66	38	8	YES			S
8D	TB7-3,4	J7L	79	48	8	YES			S
PED PUSH BUTTONS									
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED				
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED				

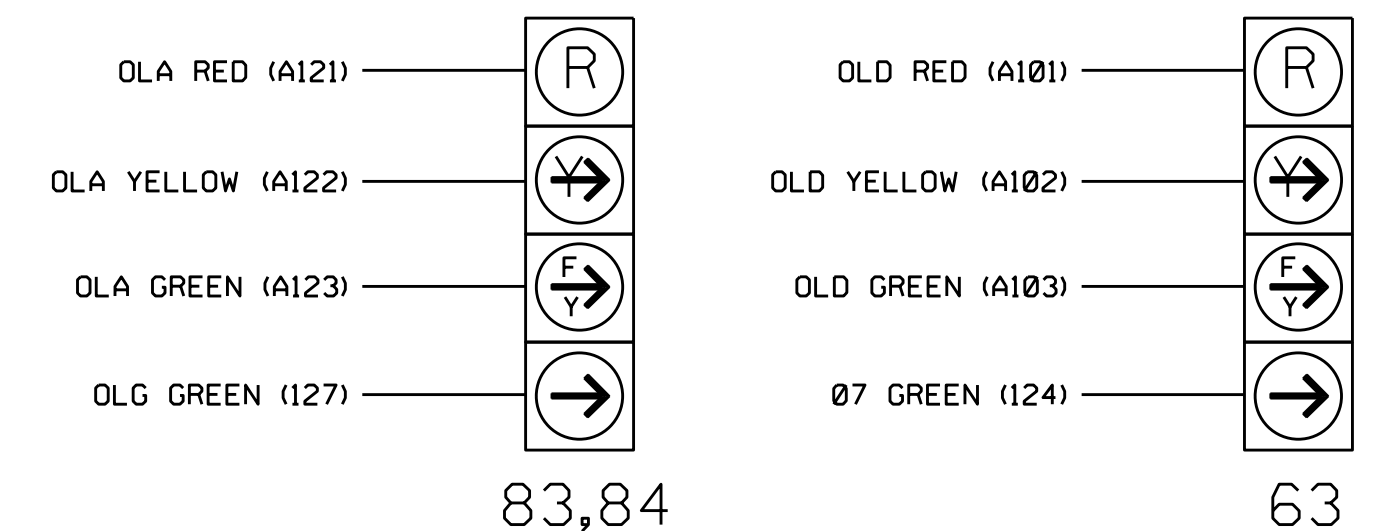
NOTE:
 INSTALL DC ISOLATOR IN INPUT FILE SLOT 113.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

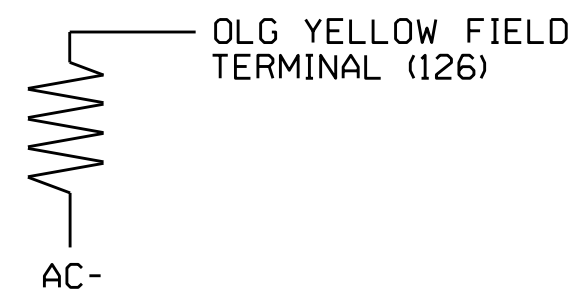


THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0364
 DESIGNED: January 2016
 SEALED: 10/4/2016
 REVISED: N/A

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SR 1403 (S. Reilly Road) at SR 1400 (Cliffdale Road)

Division 6 Cumberland County Fayetteville

PLAN DATE: September 2016 REVIEWED BY: BAS

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal: Keith M. Mims, Professional Engineer, No. 036880

DocuSigned by: Keith M. Mims 10/17/2016

SIG. INVENTORY NO. 06-0364