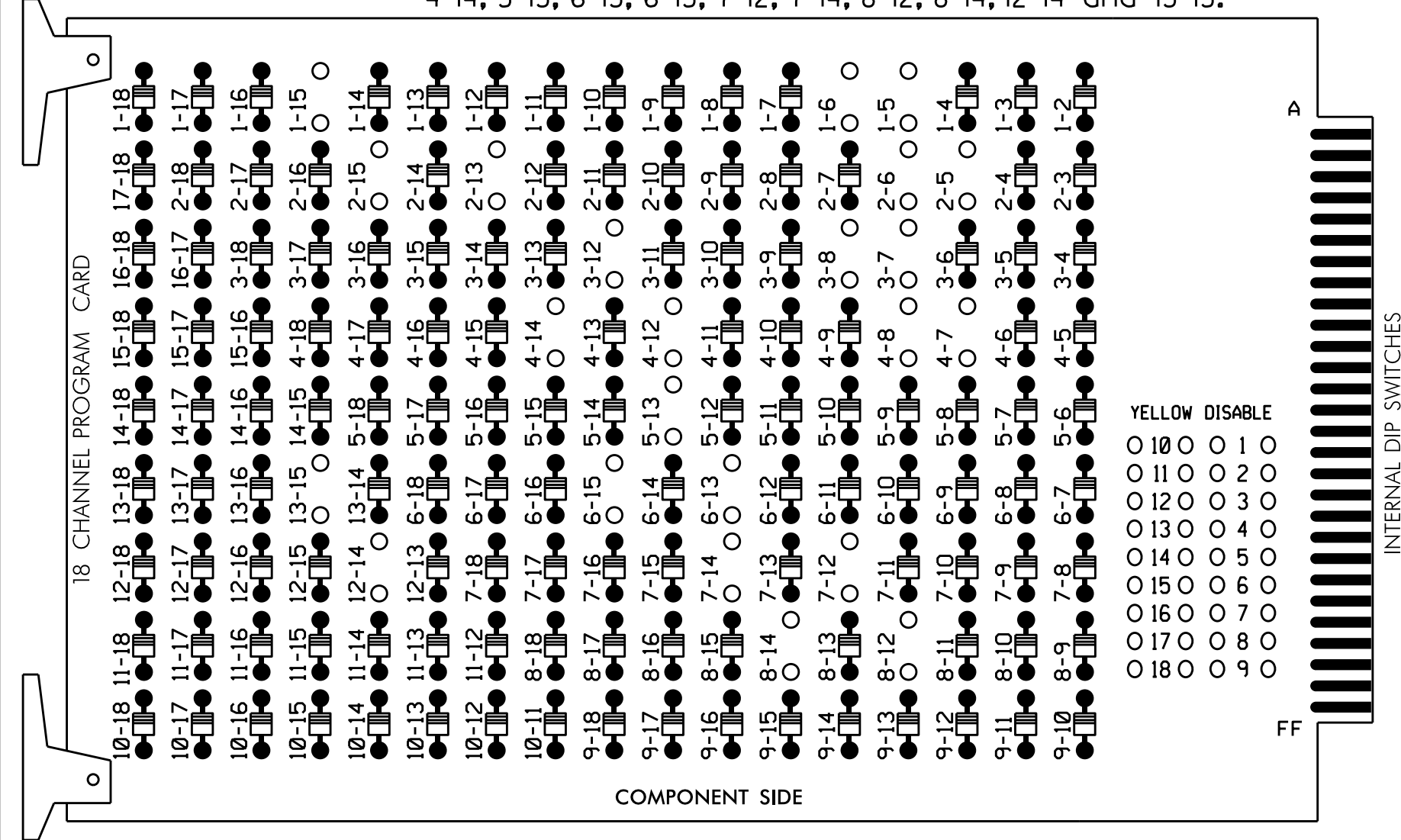


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

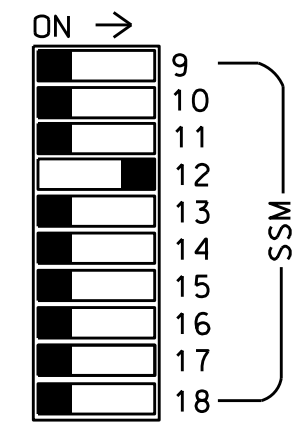
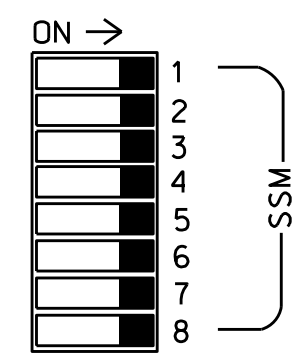
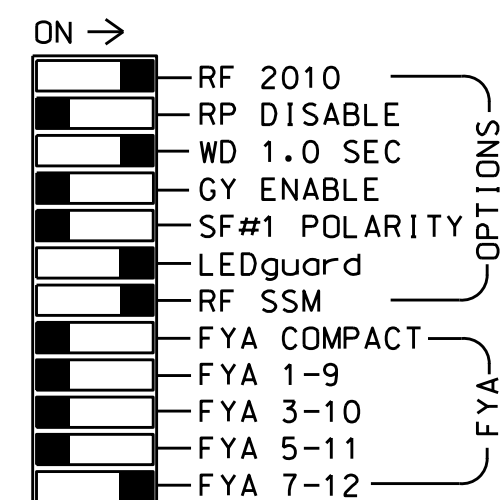
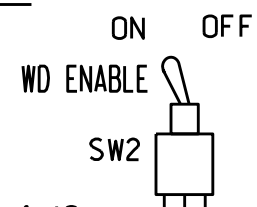
REMOVE DIODE JUMPERS 1-5, 1-6, 1-15, 2-5, 2-6, 2-13, 2-15, 3-7, 3-8, 3-12, 4-7, 4-8, 4-12, 4-14, 5-13, 6-13, 6-15, 7-12, 7-14, 8-12, 8-14, 12-14 and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for volume density operation.
- Program controller to start up in phase 2 Walk and 6 Walk.
- The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 W/AUX
 SOFTWARE.....ECONOLITE ASC/3-2070
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,
 S10,S11,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8,2 PED,
 4 PED,6 PED
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....*
 * See overlap programming detail on sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22 23	P21, P22	22	31,32	41,42	P41, P42	51	61,62 63	P61, P62	63	71*	81,82	NU	NU	NU	NU	71*	NU
RED	128				101			134		*	107								
YELLOW		129			102			135			108								
GREEN		130			103			136			109								
RED ARROW	125				116			131											A101
YELLOW ARROW	126			117	117			132			123								A102
FLASHING YELLOW ARROW																			A103
GREEN ARROW	127			118	118			133		124	124								
Hand			113				104			119									
Person			115				106			121									

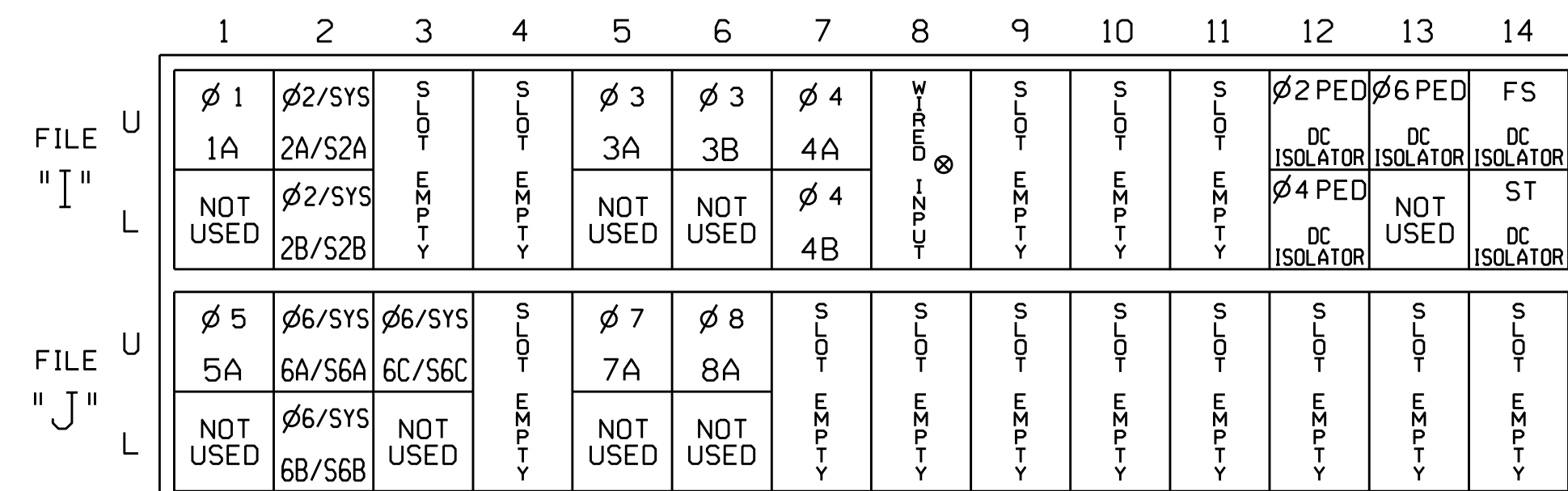
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

PHASE 7 RED FIELD TERMINAL (122)



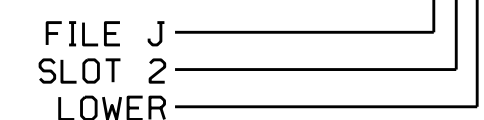
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES		3	S
2A/S2A	TB2-5,6	I2U	39	2	2/SYS	YES			N
2B/S2B	TB2-7,8	I2L	43	12	2/SYS	YES			N
3A	TB4-5,6	I5U	58	3	3	YES		3	S
3B	TB4-9,10	I6U	41	4	3	YES		3	S
4A	TB6-1,2	I7U	65	34	4	YES			S
4B	TB6-3,4	I7L	78	44	4	YES			S
5A	TB3-1,2	J1U	55	5	5	YES		3	S
6A/S6A	TB3-5,6	J2U	40	6	6/SYS	YES			N
6B/S6B	TB3-7,8	J2L	44	16	6/SYS	YES			N
6C/S6C	TB3-9,10	J3U	64	36	6/SYS	YES			N
7A ¹	TB5-5,6	J5U	57	7	7	YES		15	S
		I8U	49	24	4	YES		3	S
8A	TB5-9,10	J6U	42	8	8	YES			S
PED PUSH BUTTONS									
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED				
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED				
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED				

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

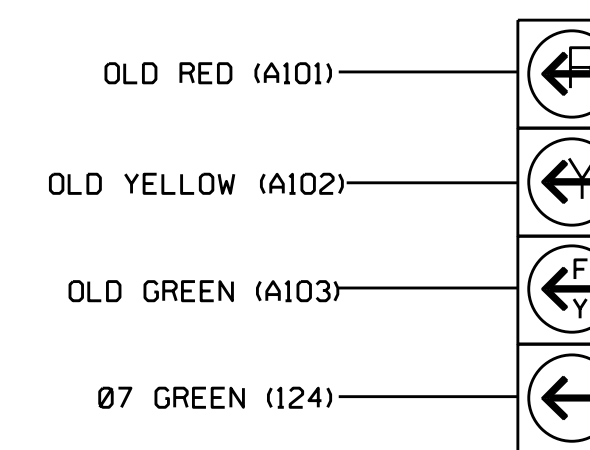
¹Add jumper from J5-W to I8-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



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THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0324
 DESIGNED: July 2016
 SEALED: 9-28-16
 REVISED: N/A

Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Electrical and Programming Details For: SR 1007 (All American Expy./Owen Dr.) at Owen Drive/Walter Reed Road

Division 6 Cumberland County Fayetteville

PLAN DATE: September 2016 REVIEWED BY: BAS

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal of Keith M. Mims, Professional Engineer, No. 036880

DocuSigned by: Keith M. Mims 10/3/2016

SIG. INVENTORY NO. 06-0324

03-001-2016-08-13
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 J.peterson