

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... OVERLAP   G
OPPOSING THROUGH.....  PHASE    2

FLASHING ARROW OUTPUT.....CH9  ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'NORMAL'

```

TMG VEH OVLP...[B] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . X . X . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```

TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... OVERLAP   H
OPPOSING THROUGH.....  PHASE    6

FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'NORMAL'

```

TMG VEH OVLP...[D] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . X X . . . . . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Three Times

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```

TMG VEH OVLP...[G] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X . . X . . . . . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

Toggle Once

OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

```

TMG VEH OVLP...[H] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . X . X . . . . . . . . . . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0
    
```

END PROGRAMMING

ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switches S1 and S7 as OLG and OLH, program LD SWITCH 1 as OVLP '7' TYPE '0' and LD SWITCH 5 as OVLP '8' TYPE '0' as shown below.


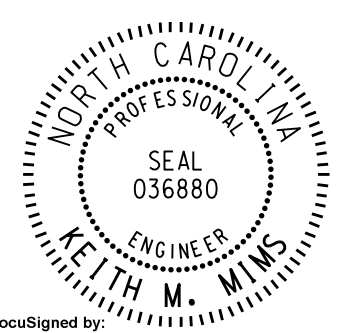
- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **3. LOAD SW ASSIGN**

	LD SWITCH ASSIGN		PHASE		DIMMING		---FLASH---		
	/OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR
→ 1	7	0	.	.	.	+	A	R	X
2	2	V	.	.	.	+	A	Y	.
3	3	V	.	.	.	+	A	R	X
4	4	V	.	.	.	+	A	R	.
→ 5	8	0	.	.	.	-	A	R	.
6	6	V	.	.	.	-	A	Y	X
7	7	V	.	.	.	-	A	R	.
8	8	V	.	.	.	-	A	R	X
9	1	0	.	.	.	+	A	Y	X
10	2	0	.	.	.	+	A	Y	X
11	3	0	.	.	.	-	A	Y	.
12	4	0	.	.	.	-	A	Y	.
13	2	P	.	.	.	+	A	.	.
14	4	P	.	.	.	-	A	.	.
15	6	P	.	.	.	+	A	.	.
16	8	P	.	.	.	-	A	.	.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0244
DESIGNED: November 2015
SEALED: 7-20-16
REVISED: N/A

Electrical Detail Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

	ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 24 (Bragg Boulevard) at US 401 Bus. NC 87 (MLK Freeway) Ramps		SEAL 
	Prepared In the Offices of:		Division 6 Cumberland County Fayetteville		
PLAN DATE: June 2016		REVIEWED BY: BAS		Keith M. Mims 10/12/2016	
PREPARED BY: James Peterson		REVIEWED BY:		DATE	
REVISIONS		INIT.		DATE	
SIG. INVENTORY NO. 06-0244					