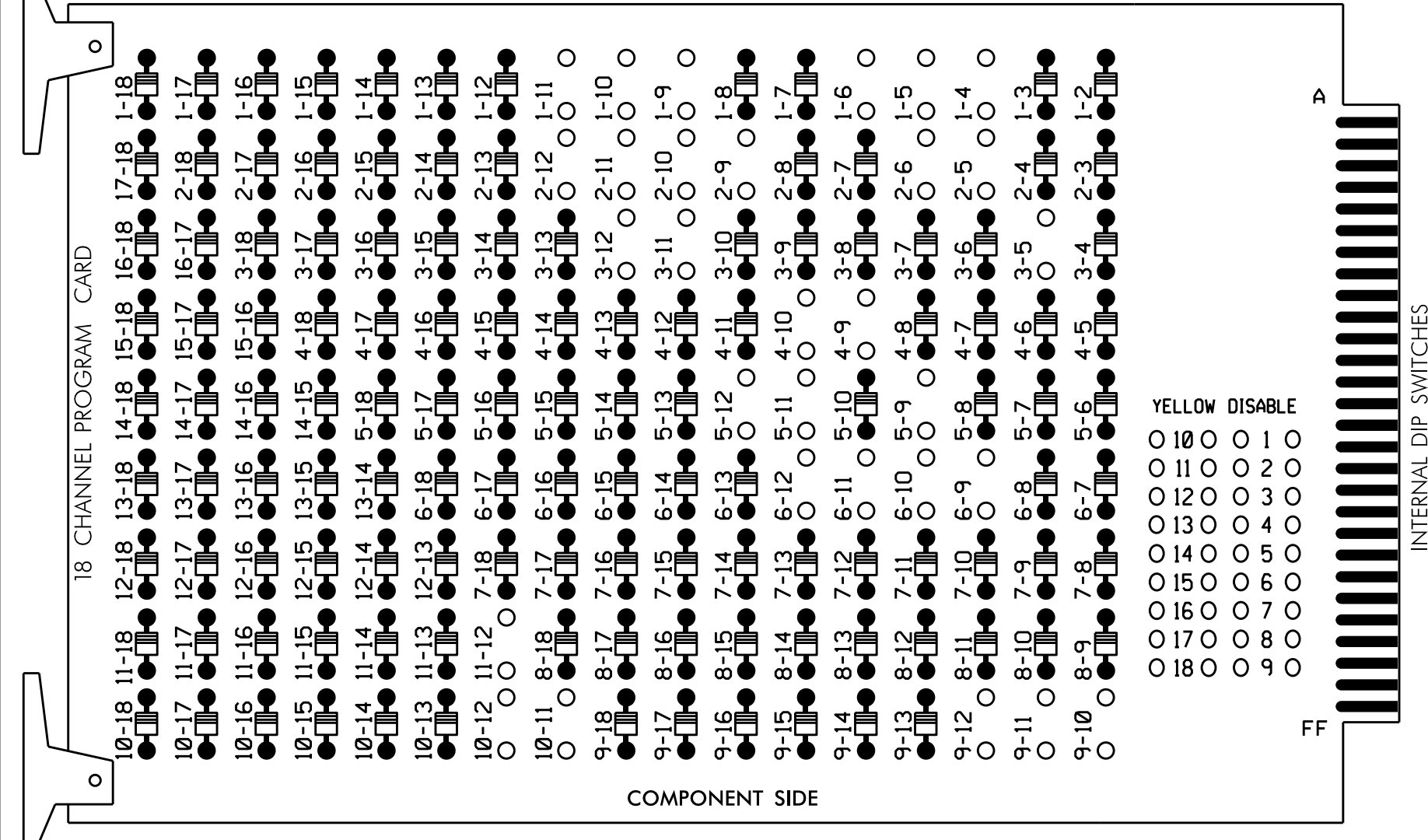


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

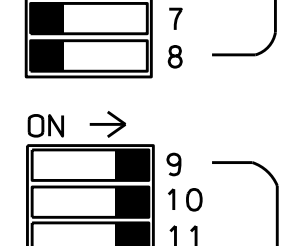
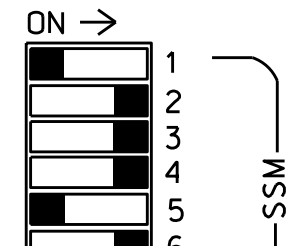
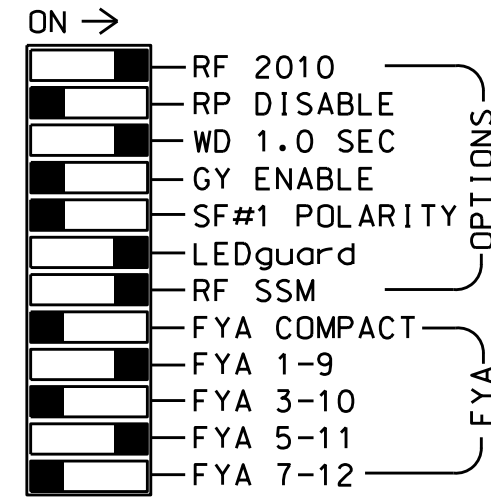
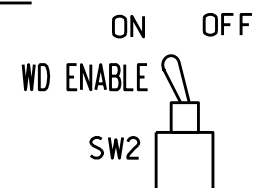
REMOVE DIODE JUMPERS 1-4, 1-5, 1-6, 1-9, 1-10, 1-11, 2-5, 2-6, 2-9, 2-10, 2-11, 2-12, 3-5, 3-11, 3-12, 4-9, 4-10, 5-9, 5-11, 5-12, 6-9, 6-10, 6-11, 6-12, 9-10, 9-11, 9-12, 10-11, 10-12 AND 11-12.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program controller to start up in phase 2 Green and 6 Green.
- The cabinet and controller are part of the Fayetteville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....332 W/AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,AUX S1,AUX S2  
 AUX S4,AUX S5  
 PHASES USED.....1,2,3,4,5,6  
 OVERLAP "A".....\*  
 OVERLAP "B".....\*  
 OVERLAP "C".....\*  
 OVERLAP "D".....\*  
 OVERLAP "G".....\*  
 OVERLAP "H".....\*  
 \* See overlap programming detail on sheet 2

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	DLG	2 PED	2	3	4	4 PED	OLH	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	24,25 26	NU	31,32 33	41,42	43	44	NU	51	64,65 66	NU	NU	NU	11	61,62 63	NU	51	21,22 23	NU
RED		128		116		101	101								A124				A101
YELLOW	*	129		117			102		*	135					A125				A102
GREEN		130		118			103			136					A126				103
RED ARROW							101								A121				A114
YELLOW ARROW							102	102							A122				A115
FLASHING YELLOW ARROW															A123				A116
GREEN ARROW	127						103	103		133									

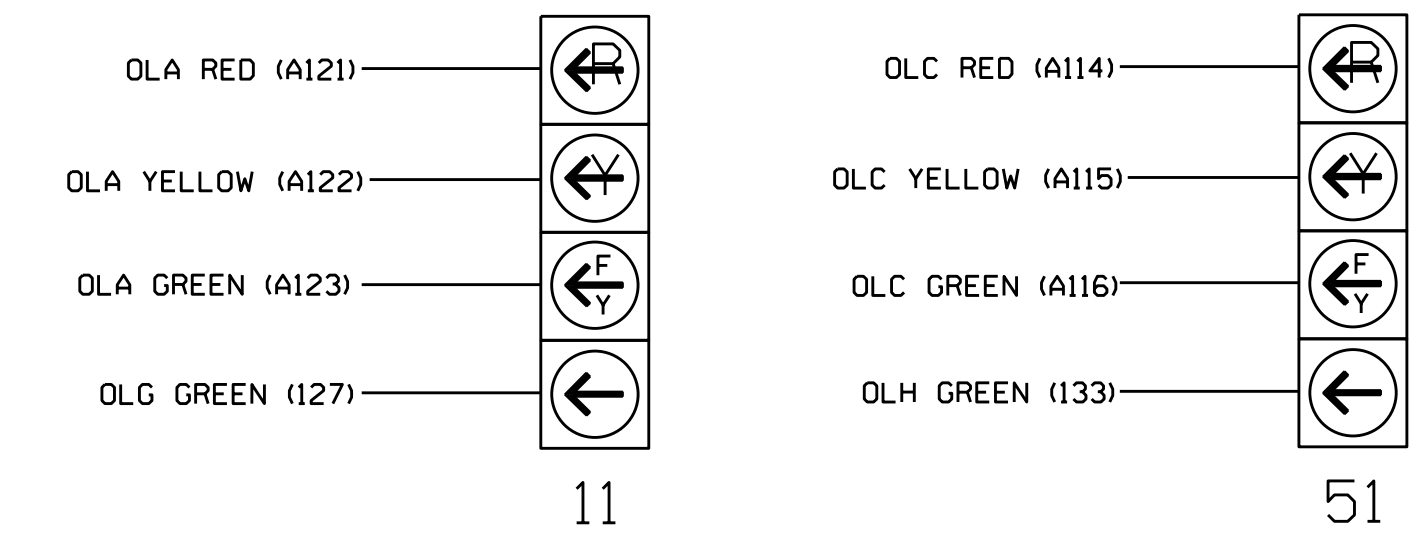
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**INPUT FILE POSITION LAYOUT**

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1 1A	∅ 2 2A,2B	∅ 2 2E,2F,2G	∅ 3 3A	∅ 3 3B	∅ 4 4A	∅ 4 4C	SYS. DET. S2A	S	S	S	S	S	FS
L	NOT USED	∅ 2 2C,2D	NOT USED	NOT USED	NOT USED	∅ 4 4B	NOT USED	SYS. DET. S2B	W	W	W	W	W	DC ISOLATOR ST
U	∅ 5 5A	∅ 6 6A,6B	∅ 6 6E,6F,6G	S	S	SYS. DET. S2C	S	SYS. DET. S6A	S	S	S	S	S	S
L	NOT USED	∅ 6 6C,6D	NOT USED	W	W	SYS. DET. S6C	W	SYS. DET. S6B	W	W	W	W	W	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

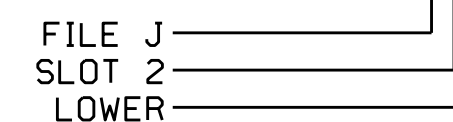
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A <sup>1</sup>	TB2-1,2	I1U	56	1	1	YES		15	S
	-	J4U	48	26	6	YES			S
2A,2B	TB2-5,6	I2U	39	2	2	YES			S
2C,2D	TB2-7,8	I2L	43	12	2	YES			S
2E,2F,2G	TB2-9,10	I3U	63	32	2	YES			S
3A	TB4-5,6	I5U	58	3	3	YES			S
3B	TB4-9,10	I6U	41	4	3	YES		15	S
4A	TB6-1,2	I7U	65	34	4	YES			S
4B	TB6-3,4	I7L	78	44	4	YES			S
4C	TB6-5,6	I8U	49	24	4	YES		15	S
*S2A	TB6-9,10	I9U	60	11	SYS	NO			N
*S2B	TB6-11,12	I9L	62	13	SYS	NO			N
5A <sup>2</sup>	TB3-1,2	J1U	55	5	5	YES		15	S
	-	I4U	47	22	2	YES			S
6A,6B	TB3-5,6	J2U	40	6	6	YES			S
6C,6D	TB3-7,8	J2L	44	16	6	YES			S
6E,6F,6G	TB3-9,10	J3U	64	36	6	YES			S
*S2C	TB7-1,2	J7U	66	38	SYS	NO			N
*S6C	TB7-3,4	J7L	79	48	SYS	NO			N
*S6A	TB7-9,10	J9U	59	15	SYS	NO			N
*S6B	TB7-11,12	J9L	61	17	SYS	NO			N

\* System detector only. Remove any assigned vehicle phase.

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

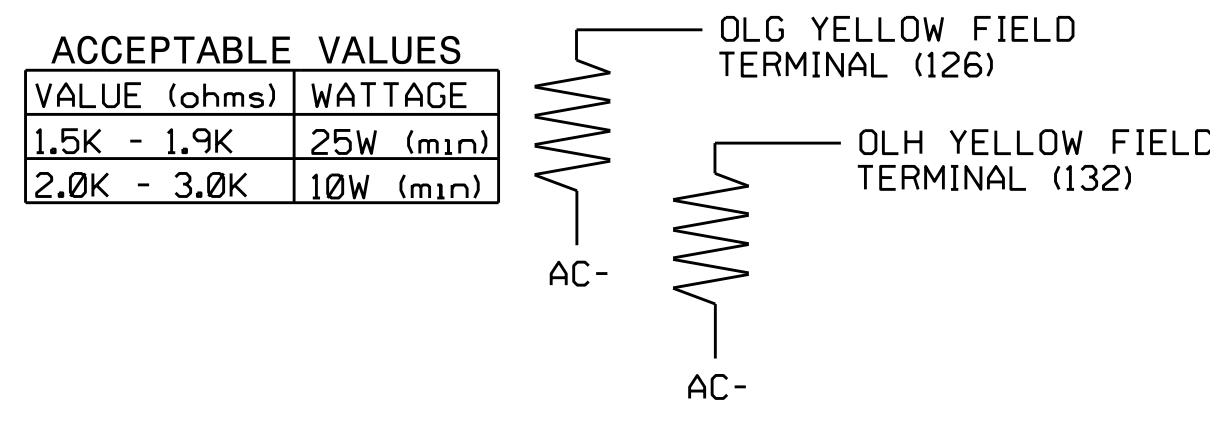
<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown)



Electrical Detail Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
 TRANSPORTATION MOBILITY AND SAFETY ADMINISTRATION  
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION  
 Signal Management Section  
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 24 (Bragg Boulevard) at US 401 Bus. NC 87 (MLK Freeway) Ramps

Division 6 Cumberland County Fayetteville  
 PLAN DATE: June 2016 REVIEWED BY: BAS  
 PREPARED BY: James Peterson REVIEWED BY:

SEAL  
 NORTH CAROLINA PROFESSIONAL ENGINEER  
 KEITH M. MIMS  
 036880

DocuSigned by: Keith M. Mims 10/12/2016  
 2F8078EEC03445 DATE

SIG. INVENTORY NO. 06-0244