

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

1. From Main Menu select 8. UTILITIES
2. From UTILITIES Submenu select 1. COPY/CLEAR
3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

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COPY / CLEAR UTILITY
FROM          TO
PHASE TIMING... > PHASE TIMING...
TIMING PLAN... > TIMING PLAN...
PH DET OPT PLAN. > PH DET OPT PLAN.
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
TOGGLE TO SELECT A "FROM" AND A "TO"
THEN PRESS ENTER
    
```

4. From Main Menu select 6. DETECTORS
5. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
6. Place cursor in VEH DET PLAN [] position and enter "2".

- Place cursor in VEH DETECTOR [] position and enter "5".
- Set delay time to "0".

```

VEH DETECTOR [ 5]  VEH DET PLAN [ 2] ← NOTICE VEH
TYPE: S-STANDARD  DET PLAN 2
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
5 5
EXTEND TIME... 0.0 DELAY TIME... 0.0 ← ENSURE DELAY
USE ADDED INITIAL . CROSS SWITCH PH.. 0  IS SET TO '0'
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

- Place cursor in VEH DETECTOR [] position and enter "22".
- Set assigned phase to "0".

```

VEH DETECTOR [22]  VEH DET PLAN [ 2] ← NOTICE VEH
TYPE: G-GREEN EXTENSION/DELAY  DET PLAN 2
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
22 0 ← ENSURE PHASE
EXTEND TIME... 0.0 DELAY TIME... 0.0  IS SET TO "0"
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
    
```

END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

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TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED PHASE (LEFT TURN)..... 5
PERMISSIVE PHASE (OPPOSING THRU)... 6
FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 5
    
```

← NOTICE ACTION PLAN SF BIT "5"

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0334
DESIGNED: January 2016
SEALED: 7/7/2016
REVISED: N/A

Electrical Detail - Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

<p style="font-size: x-small;">ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="font-size: x-small;">Prepared In the Offices of:</p> <p style="font-size: x-small;">750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>US 401 Bus. (Raeford Road) at Montclair Road</p> <p style="font-size: x-small;">Division 6 Cumberland County Fayetteville</p> <table style="width: 100%; font-size: x-small;"> <tr> <td>PLAN DATE: June 2016</td> <td>REVIEWED BY: BAS</td> </tr> <tr> <td>PREPARED BY: S. Armstrong</td> <td>REVIEWED BY:</td> </tr> </table> <table style="width: 100%; font-size: x-small;"> <tr> <td>REVISIONS</td> <td>INIT.</td> <td>DATE</td> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </table>	PLAN DATE: June 2016	REVIEWED BY: BAS	PREPARED BY: S. Armstrong	REVIEWED BY:	REVISIONS	INIT.	DATE				<p>SEAL</p> <p style="font-size: x-small;">DocuSigned by: Keith M. Mims 7/11/2016</p> <p style="font-size: x-small;">SIG. INVENTORY NO. 06-0334</p>
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