IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu select 8. UTILITIES
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

COPY / CLEAR UTILITY PHASE TIMING.... > PHASE TIMING.... TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

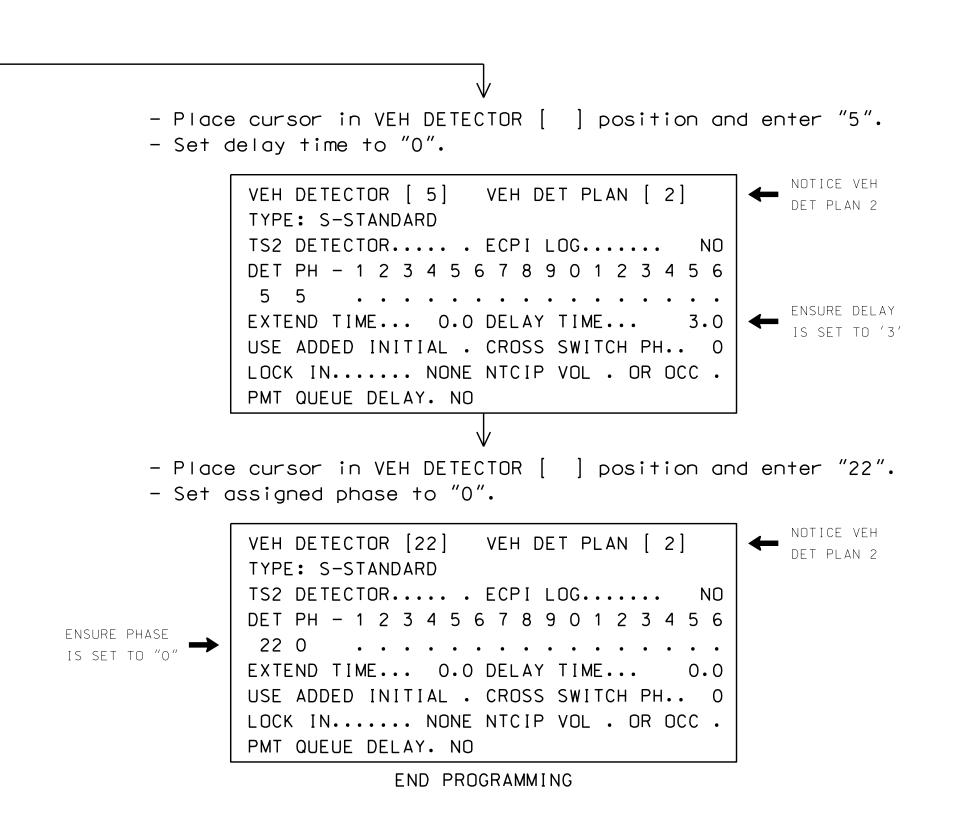
- 4. From Main Menu select | 6. DETECTORS |
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [] position and enter "2".
 - Place cursor in VEH DETECTOR [] position and enter "1".
- Set delay time to "0".

VEH DETECTOR [1] VEH DET PLAN [2]

NOTICE VEH
DET PLAN 2 TYPE: S-STANDARD TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE DELAY EXTEND TIME... 0.0 DELAY TIME... 3.0 ← USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY, NO

- Place cursor in VEH DETECTOR [] position and enter "26". - Set assigned phase to "0".

VEH DETECTOR [26] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO "O" EXTEND TIME... O.O DELAY TIME... O.O USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0328 DESIGNED: January 2016 SEALED: 6/15/2016 REVISED: N/A

PROJECT REFERENCE NO. U-5742 Sig. 84 2

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select | 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED PHASE (LEFT TURN)..... 1 PERMISSIVE PHASE (OPPOSING THRU).... 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1 | NOTICE ACTION Toggle Twice OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED PHASE (LEFT TURN)..... 5 PERMISSIVE PHASE (OPPOSING THRU).... 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

END PROGRAMMING

Electrical Detail Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DETAILS FOR: Prepared in the Offices of:

ELECTRICAL AND PROGRAMMING

Brighton Road/ Fred Anderson Nissan Entrance

US 401 Business (Raeford Rd)

ivision 6 PLAN DATE: June 2016 REVIEWED BY: PREPARED BY: Keith Mims REVIEWED BY: REVISIONS

INIT. DATE

Zacpary M. Little 6/20/2016 SIG. INVENTORY NO. 06-0328

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