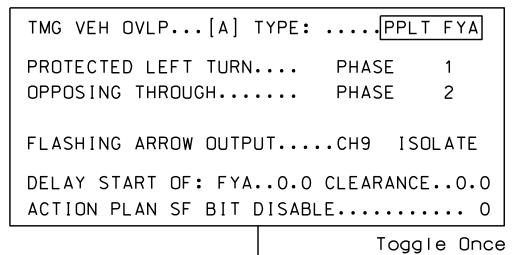
ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'



OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

_	-		
TMG VEH OVLP[B]	TYPE:	····PPLT	FYA
PROTECTED LEFT TURN OPPOSING THROUGH			
FLASHING ARROW OUTP	UT	.CH10 ISO	_ATE
DELAY START OF: FYA ACTION PLAN SF BIT			
		Toggl	e Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP[C]	TYPE:	····PPLT	FYA
PROTECTED LEFT TURN.	• • • •	PHASE	5
OPPOSING THROUGH	• • • •	PHASE	6
FLASHING ARROW OUTPU			
ACTION PLAN SF BIT [DISABLE	<u> </u>	0
		Toggl	e Once

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 7 OPPOSING THROUGH..... PHASE 8 FLASHING ARROW OUTPUT....CH12 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the Same approach, make the following flasher circuit changes:

- 1. On rear of PDA remove wire from Term. T2-4 and terminate on T2-2.
- 2. On rear of PDA remove wire from Term. T2-5 and terminate on T2-3.
- 3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0016 DESIGNED: April 2016 SEALED: 10-14-16 REVISED: N/A

Electrical Detail Sheet 2 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING

(Eastern Boulevard) SR 2299 (Russell Street)

I-95 Business / US 301

PLAN DATE: October 2016 REVIEWED BY: PREPARED BY: James Peterson REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 06-0016