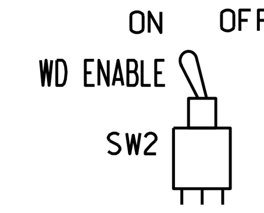
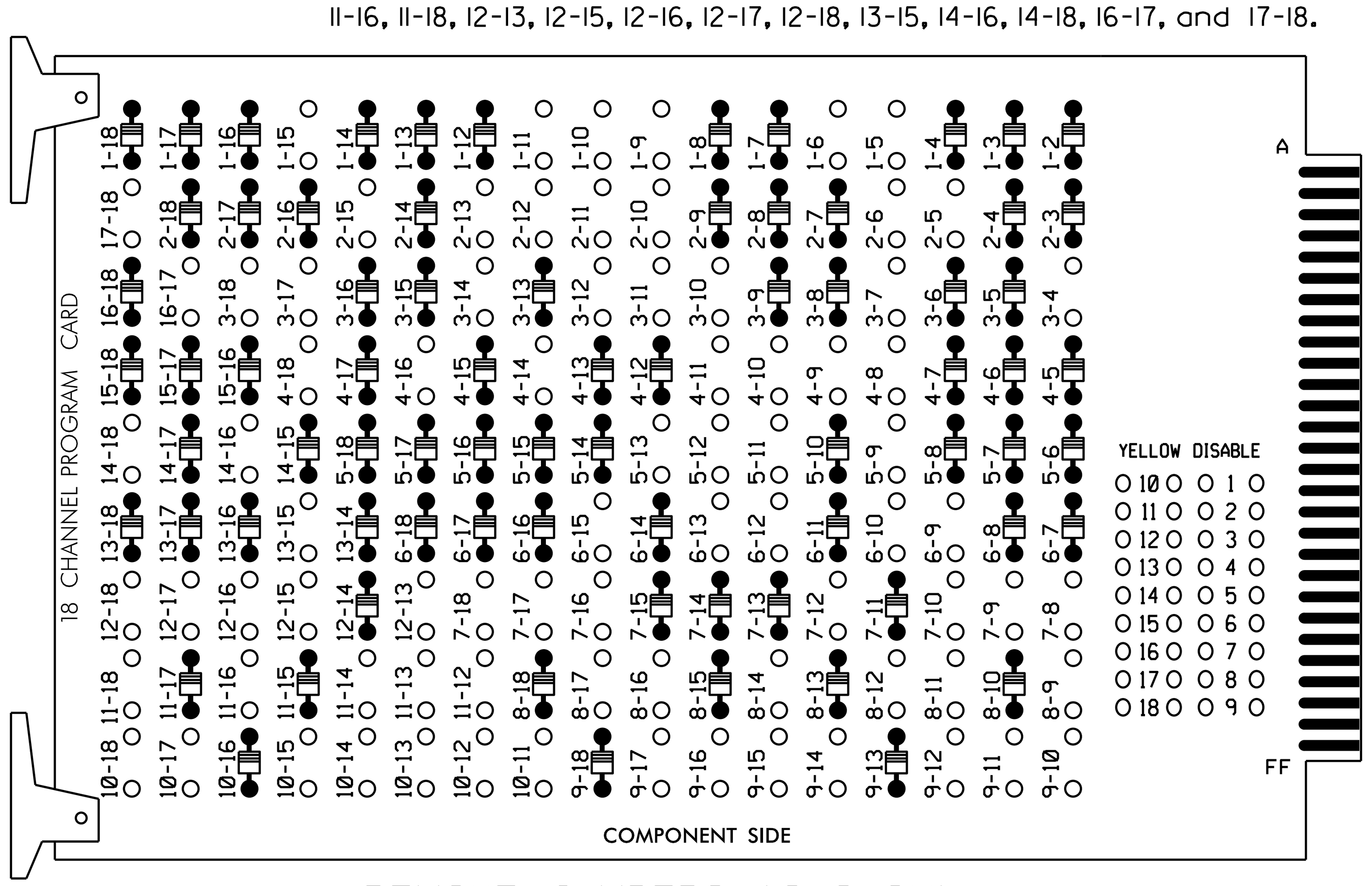
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

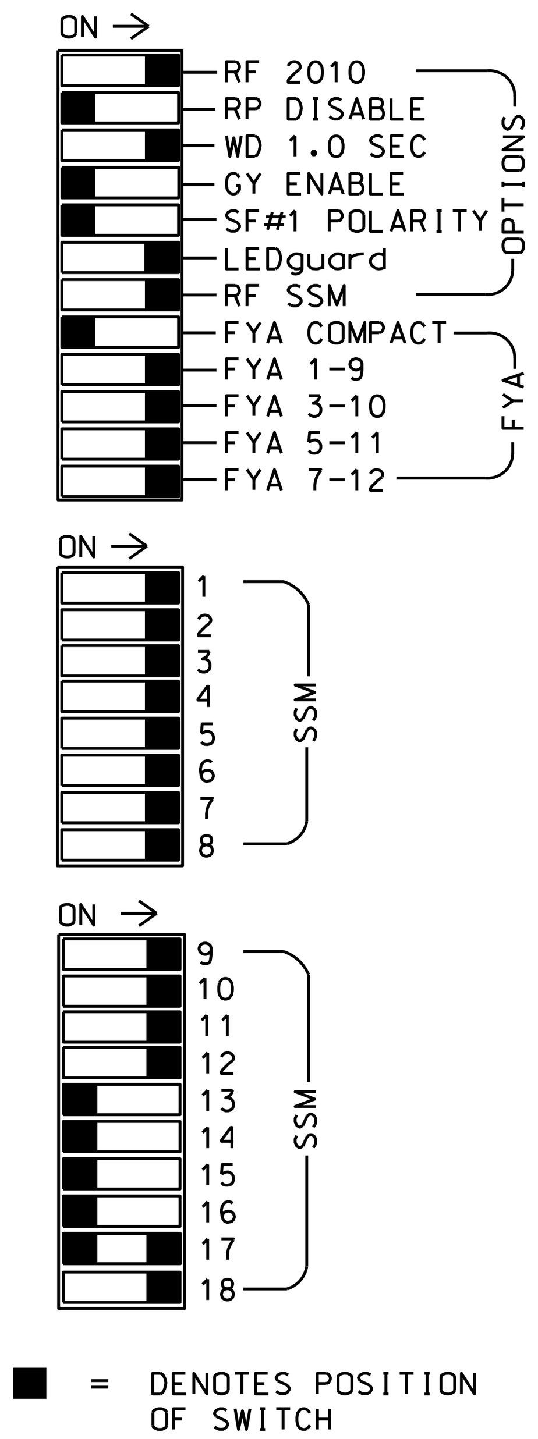
(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS:

1-5, 1-6, 1-9, 1-10, 1-11, 1-15, 2-5, 2-6, 2-10, 2-11, 2-12, 2-13, 2-15, 3-4, 3-7, 3-10, 3-11, 3-12, 3-14, 3-17, 3-18, 4-8, 4-9, 4-10, 4-11, 4-14, 4-16, 4-18, 5-9, 5-11, 5-12, 5-13, 6-9, 6-10, 6-12, 6-13, 6-15, 7-8, 7-9, 7-10, 7-12, 7-16, 7-17, 7-18, 8-9, 8-11, 8-12, 8-14, 8-16, 8-17, 9-10, 9-11, 9-12, 9-14, 9-15, 9-16, 9-17, 10-11, 10-12, 10-13, 10-14, 10-15, 10-17, 10-18, 11-12, 11-13, 11-14, 11-16, 11-18, 12-13, 12-15, 12-16, 12-17, 12-18, 13-15, 14-16, 14-18, 16-17, and 17-18.





REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4	S5	S6	S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3	4	14	5		6	15	7	8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	OLG	4	4 PED	5		6	6 PED	OLH	8	8 PED	OLA	OLB	OLE	OLC	OLD	OLF
SIGNAL HEAD NO.	11,12	83,84★	21,22 23	P21, P22	64★	41,42	P41, P42	43,44★	51,52	61,62 63	P61, P62	24,25★	81,82	P81, P82	83,84★	64★	31,32 33	43,44★	24,25★	71,72
RED			128			101				134			107		A121	A124		A114	A101	
YELLOW			129		✳	102				135		✳	108							
GREEN			130			103				136			109							
RED ARROW	125								131								A111			A104
YELLOW ARROW	126								132						A122	A125	A112	A115	A102	A105
FLASHING YELLOW ARROW															A123	A126		A116	A103	
GREEN ARROW	127	127			118			133	133			124					A113			A106
(Don't Walk)				113			104				119			110						
(Walk)				115			106				121			112						

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

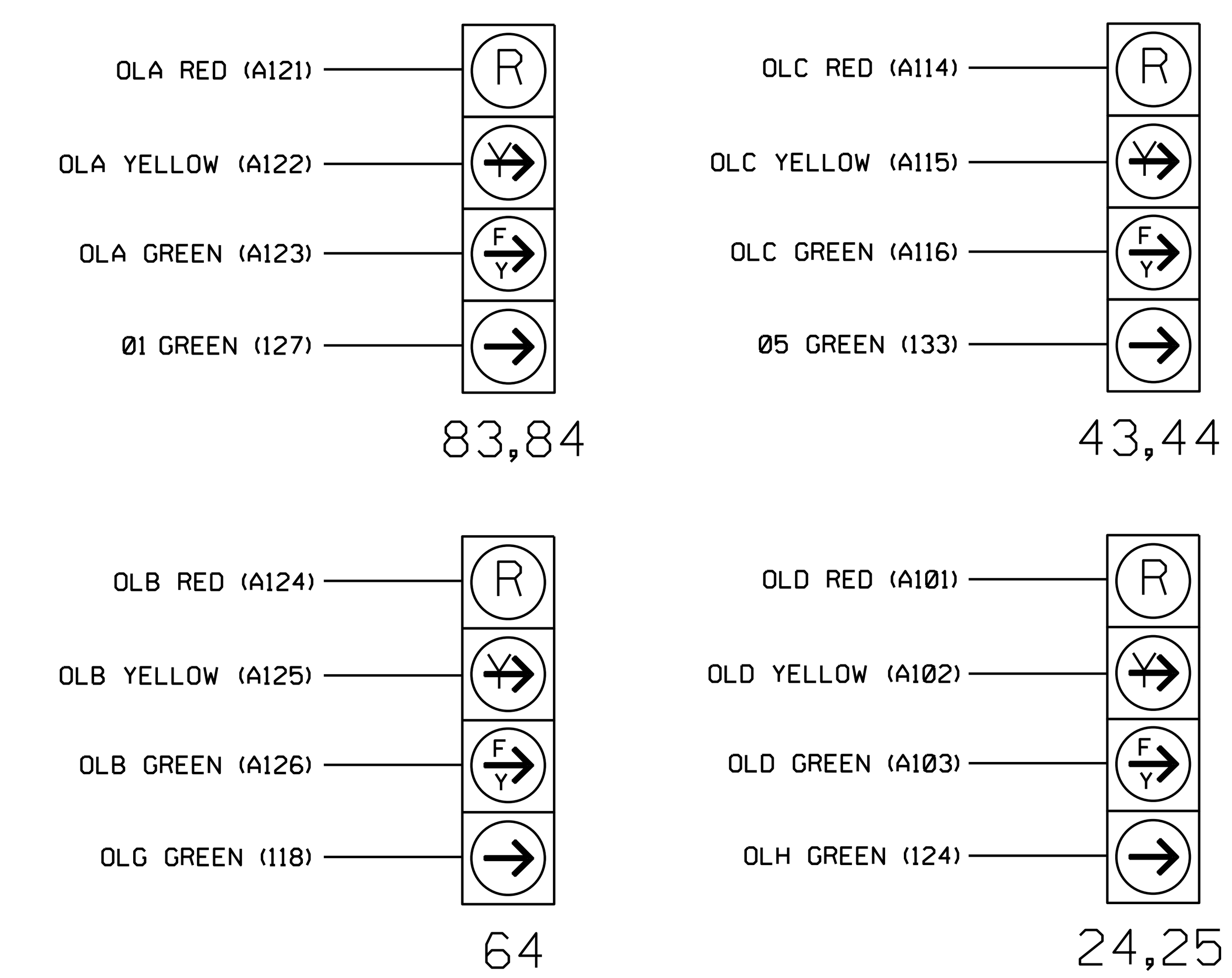
NOTE: Output functions for load switches S4 and S10 have been reassigned. See sheet 4 for details.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø1 1C	Ø2 2A	Ø2 2C	Ø3 3A	Ø3 3B	Ø4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	Ø6 PED DC ISOLATOR	FS DC ISOLATOR
	L	Ø1 1B	Ø1 1D	Ø2 2B	NOT USED	NOT USED	Ø3 3C	Ø4 4B					Ø4 PED DC ISOLATOR	Ø8 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø5 5C	Ø6 6A	Ø6 6C	SLOT EMPTY	Ø7 7A	Ø8 8A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	Ø5 5B	Ø5 5D	Ø6 6B	NOT USED		Ø7 7B	Ø8 8B							

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES			S
1B	TB2-3,4	I1L	56	1	1	YES			S
1C	TB2-5,6	I2U	39	2	1	YES		15	S
1D	TB2-7,8	I2L	43	12	1	YES		15	S
2A	TB2-9,10	I3U	63	32	2	YES			N
2B	TB2-11,12	I3L	76	42	2	YES			N
2C	TB4-1,2	I4U	47	22	2	YES			N
3A	TB4-5,6	I5U	58	3	3	YES		3	S
3B	TB4-9,10	I6U	41	4	3	YES			S
3C	TB4-11,12	I6L	45	14	3	YES			S
4A	TB6-1,2	I7U	65	34	4	YES			S
4B	TB6-3,4	I7L	78	44	4	YES			S
5A	TB3-1,2	J1U	55	5	5	YES			S
5B	TB3-3,4	J1L	55	5	5	YES			S
5C	TB3-5,6	J2U	40	6	5	YES		15	S
5D	TB3-7,8	J2L	44	16	5	YES		15	S
6A	TB3-9,10	J3U	64	36	6	YES			N
6B	TB3-11,12	J3L	77	46	6	YES			N
6C	TB5-1,2	J4U	48	26	6	YES			N
7A	TB5-9,10	J6U	42	8	7	YES			S
7B	TB5-11,12	J6L	46	18	7	YES			S
8A	TB7-1,2	J7U	66	38	8	YES			S
8B	TB7-3,4	J7L	79	48	8	YES			S
PED PUSH BUTTONS									
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED				
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED				
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED				
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED				

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

INPUT FILE POSITION LEGEND: J2L

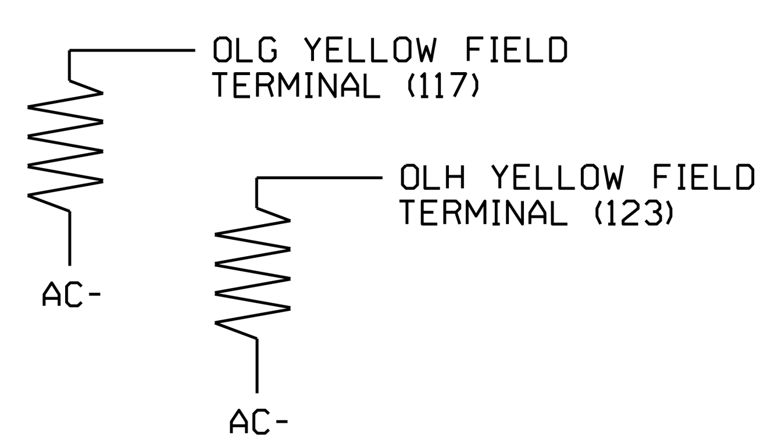
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0155
DESIGNED: January 2016
SEALED: 8/29/2016
REVISED: N/A

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division, Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

US 401 Bus (Raeford Road)
at
NC 59 (Hope Mills Road)/
SR 1596 (Glensford Drive)

Division 6 | Cumberland County | Fayetteville

PLAN DATE:	June 2016	REVIEWED BY:	BAS
PREPARED BY:	S. Armstrong	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 036880 ENGINEER KEITH M. MIMS

DocuSigned by: Keith M. Mims 2F80786E8CD34A5... 10/13/2016 DATE

SIG. INVENTORY NO. 06-0155

06-OCT-2016 06:24
S:*ITS&SU*ITS Signals*Workgroups*Sig Man*Armstrong*060155_sm_ele_xxx.dgn
sarmstrong