ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select | 2. CONTROLLER |

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

OVERLAP A

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'

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TMG VEH OVLP...[A] TYPE:OTHER/ECONOLITE
   PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . X . . . . . . . . . . . . . .
PROTECT  . . . . . . . . . . . . . . . .
PED PRTC . . . . . . . . . . . . . . . .
NOT OVLP . . . . . . . . . . . . . . . .
FLSH GRN . 1 . . . . . . . . . . . . . .
LAG X PH . . . . . . . . . . . . . . . .
LAG 2 PH . . . . . . . . . . . . . . . .


LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
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Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

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TMG VEH OVLP...[C] TYPE: .....PPLT FYA

PROTECTED PHASE (LEFT TURN).........5
PERMISSIVE PHASE (OPPOSING THRU)....6
FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE...........0
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END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0002
DESIGNED: February 2016
SEALED: 7-20-16
REVISED: N/A

Electrical Detail Sheet 2 of 2