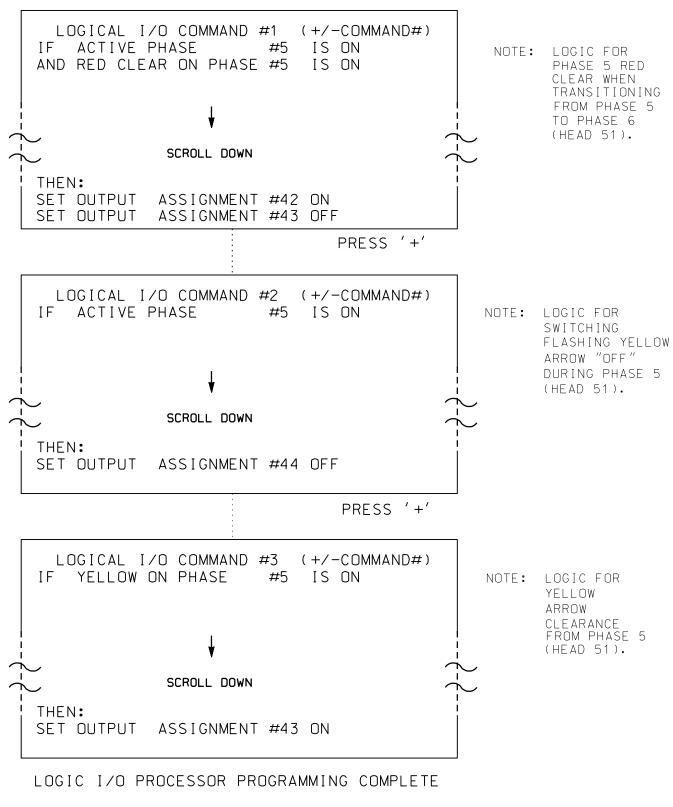
# LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

## (program controller as shown below)

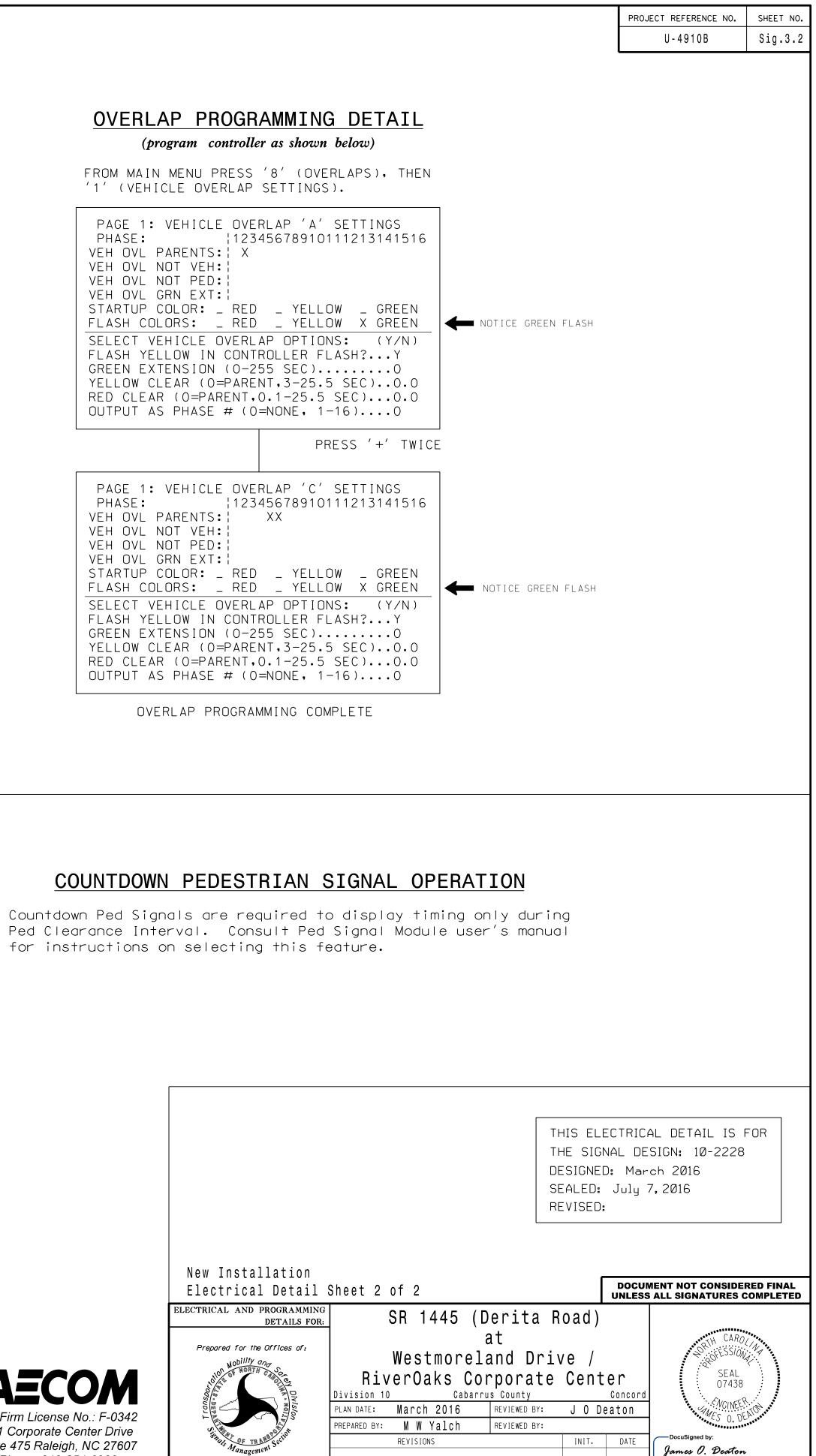
- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE S	C
USE TO INTERPRET LOGIC PF	7(
OUTPUT 42 = Overlap C OUTPUT 43 = Overlap C OUTPUT 44 = Overlap C	



	Ρ	А	G	Ε		1	:		V	Е	Η	Ι
	Ρ	Η	A	S	E	:						
	VE	Η		0	V	L		Ρ	А	R	Е	Ν
	VE	Η		0	V	L		Ν	0	Т		V
	VE	Η		0	V	L		Ν	0	Т		Ρ
	VE	Η		0	V	L		G	R	Ν		E
	ST	А	R	Т	U	Ρ		С	0	L	0	R
	FL	A	S	Η		С	0	L	0	R	S	:
-	FL SE											
-		L	E	С	Т		٧	E	Η	Ι	С	L
_	SE	L A	E S	C H	Τ	Y	V E	E	H L	I O	C W	L
_	SE FL	L A E	E S E	C H N	Т	Y E	V E X	E L T	H L E	I O N	C W S	L
	SE FL GR	L A E L	E S E L	C H N O	T	Y E	V E X C	E L T L	H L E E	I O N A	C W S R	L



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