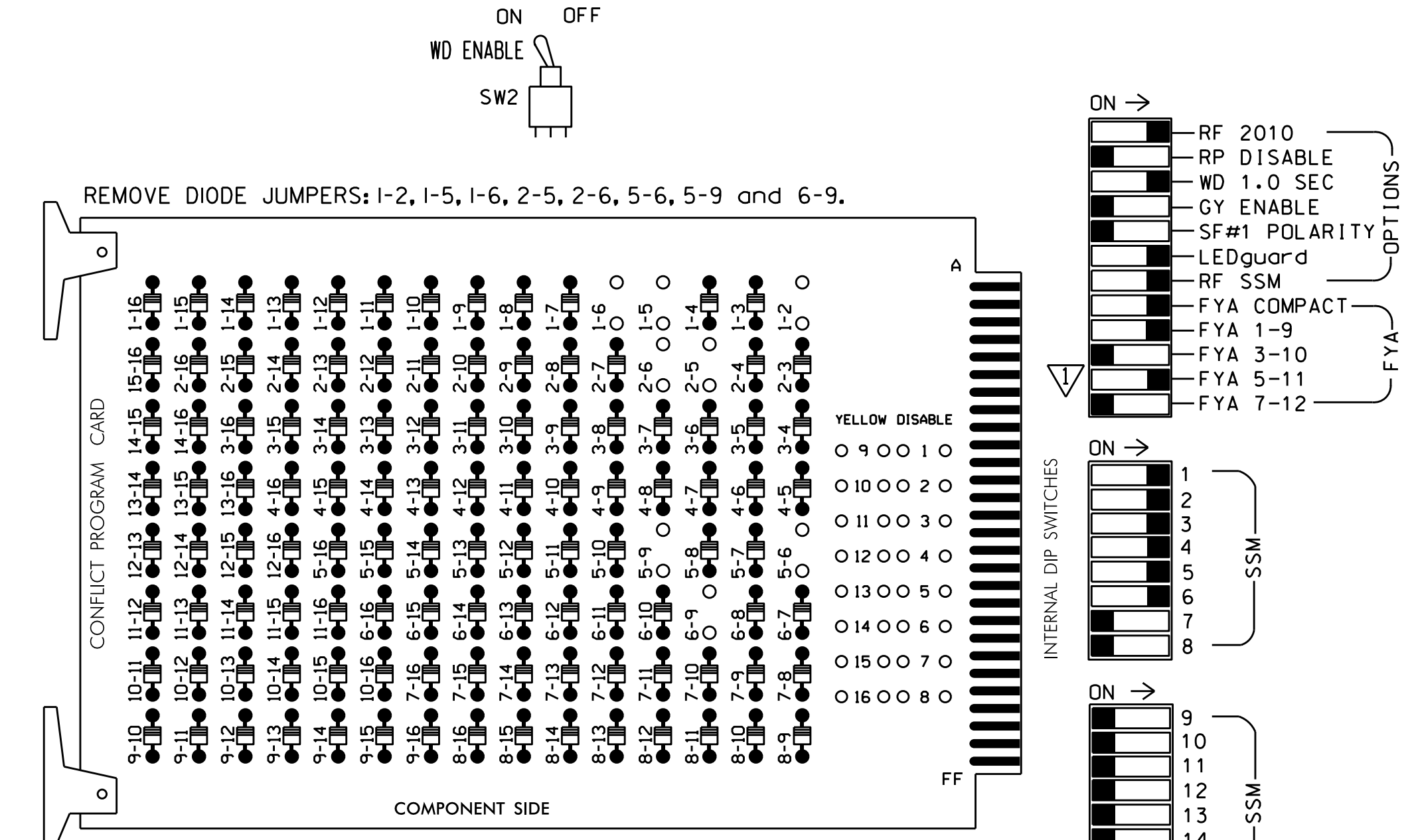


EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- REMOVE DIODE JUMPERS: 1-2, 1-5, 1-6, 2-5, 2-6, 5-6, 5-9 and 6-9.
- REMOVE JUMPERS AS SHOWN
- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Make sure jumpers SEL2-SEL5 are present on the monitor board.
 - Special cabinet wiring is required to utilize FYA COMPACT mode. See Ped Yellow Conflict Monitor Wiring Detail on this sheet.
- = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 7,8,9,10, 11,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the US 117 (Wallace) CLS.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	OLA	2	1 GRN	2	3	4	4	6	6	7	8	8
SIGNAL HEAD NO.	11*	22,23	11*	31	32	41	42	21*	61,62	NU	NU	NU
RED		128		116	116	101	101		134			
YELLOW		129		117	117	102	102		135			
GREEN		130		118	118	103	103		136			
RED ARROW	125								131			
YELLOW ARROW	126								132			
FLASHING YELLOW ARROW	127								133			
GREEN ARROW		114		118		103						
												*

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail below.

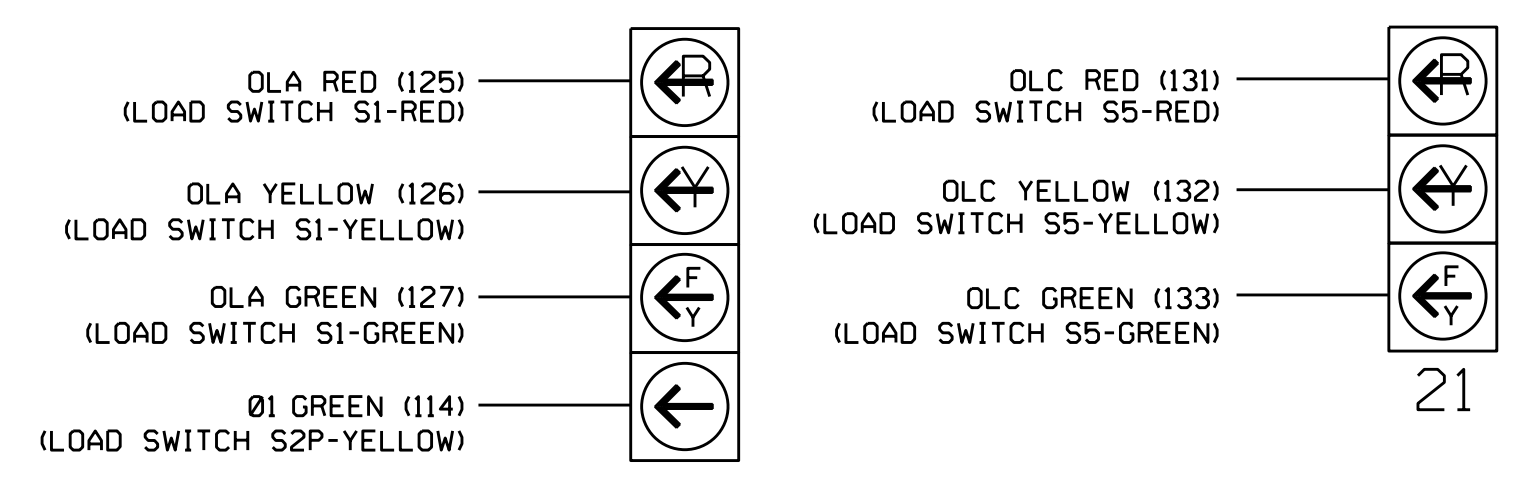
NOTE: Load Switches S1, S2P and S5 require output remapping. See sheets 3 and 4 of this electrical detail for instructions.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....McCain/CONTROL TECHNOLOGIES (DWG.NO.9500-336-NC DOT)
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....POLE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S1,S2,S2P,S3,S4,S5,S6.
 PHASES USED.....1,2,3,4,6
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....6
 OVERLAP "D".....NOT USED

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



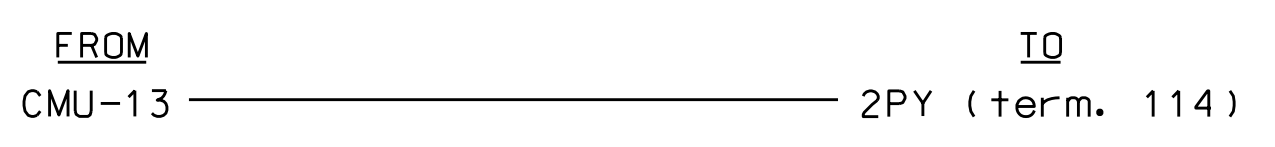
NOTE: 1. The sequence display for head 11 requires special logic programming. See sheet 2 for programming instructions.

PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode on the 2010ECL-NC Monitor, the cabinet must be wired such that the phase 2 Ped Yellow load switch output is wired to the conflict monitor as follows: From 2 PY (field term. 114) to chan. 9 green (monitor pin 13).

- Follow the instructions below to make the appropriate connections:
- STEP 1: Fold down rear panel of output file.
- STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).
- STEP 3: Within the harness, find the wire that corresponds to the conflict monitor card edge pin CMU-13. Solder this wire to the 2PY terminal (114), which is located on the rear of the output file.



NOTE: Some cabinet manufacturers use a Molex plug to accomplish this wiring configuration. If connectors are used, simply plug the two connectors together that are labeled with the pin-out as shown above.

INPUT FILE POSITION LAYOUT

(front view)

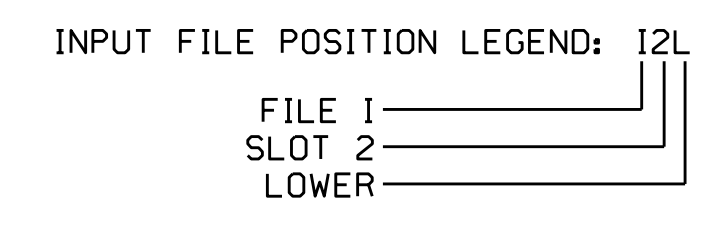
FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
∅ 1	∅ 2	∅ 3	∅ 4	NOT USED	∅ 6	S-OF	S-OF	S-OF	S-OF	S-OF	S-OF	S-OF	S-OF	FS
1A	2A	3A	4A		6A	ST	ST	ST	ST	ST	ST	ST	ST	DC ISOLATOR
WIRED INPUT	2B	3B	4B	2C	6B									DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 * Wired Input - turn off Channel 2.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB21-1,2	I1U	56	18	1	1	Y	Y			15
2A	TB21-3,4	I2U	39	1	2	2	Y	Y	Y		3
2B	TB23-3,4	I2L	43	5	12	2	Y	Y			
3A	TB21-5,6	I3U	58	20	3	3	Y	Y			3
3B	TB23-5,6	I3L	49	11	24	3	Y	Y			10
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			3
4B	TB23-7,8	I4L	45	7	14	4	Y	Y			10
2C	TB23-9,10	I5L	48	10	26	2	Y	Y	Y		3
6A	TB21-11,12	I6U	40	2	6	6	Y	Y			
6B	TB23-11,12	I6L	44	6	16	6	Y	Y			

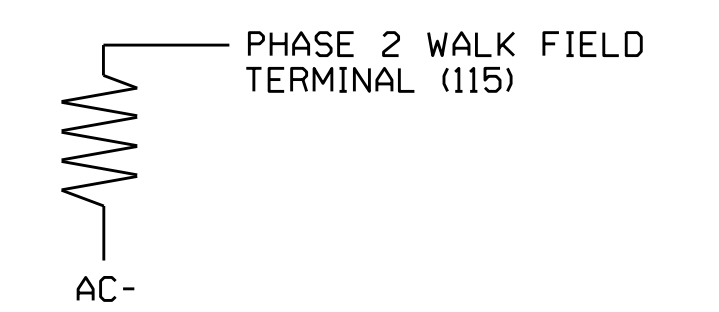
* Add jumper from I1-F to I1-W, on rear of input file.



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



ELECTRICAL DETAIL SHEET 1 OF 4

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0137
 DESIGNED: February 2016
 SEALED: 3-24-16
 REVISED: N/A

REVISION SEAL

Prepared In the Office of:

US 117 (N. Norwood Street) at SR 1173 (E Southerland Street)

Division 08 Duplin County Wallace

PLAN DATE: January 2014 REVIEWED BY: JTR

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS

Added loops and switched on FYA S-11 on the conflict monitor. (JP) KMM 3/31/2016

SIGNATURE DATE

SIG. INVENTORY NO. 03-0137

31-JAN-2016 10:38 S:\IT\SS\175\Sig\work\030137_sme.ele_20140129.dgn J.peterson