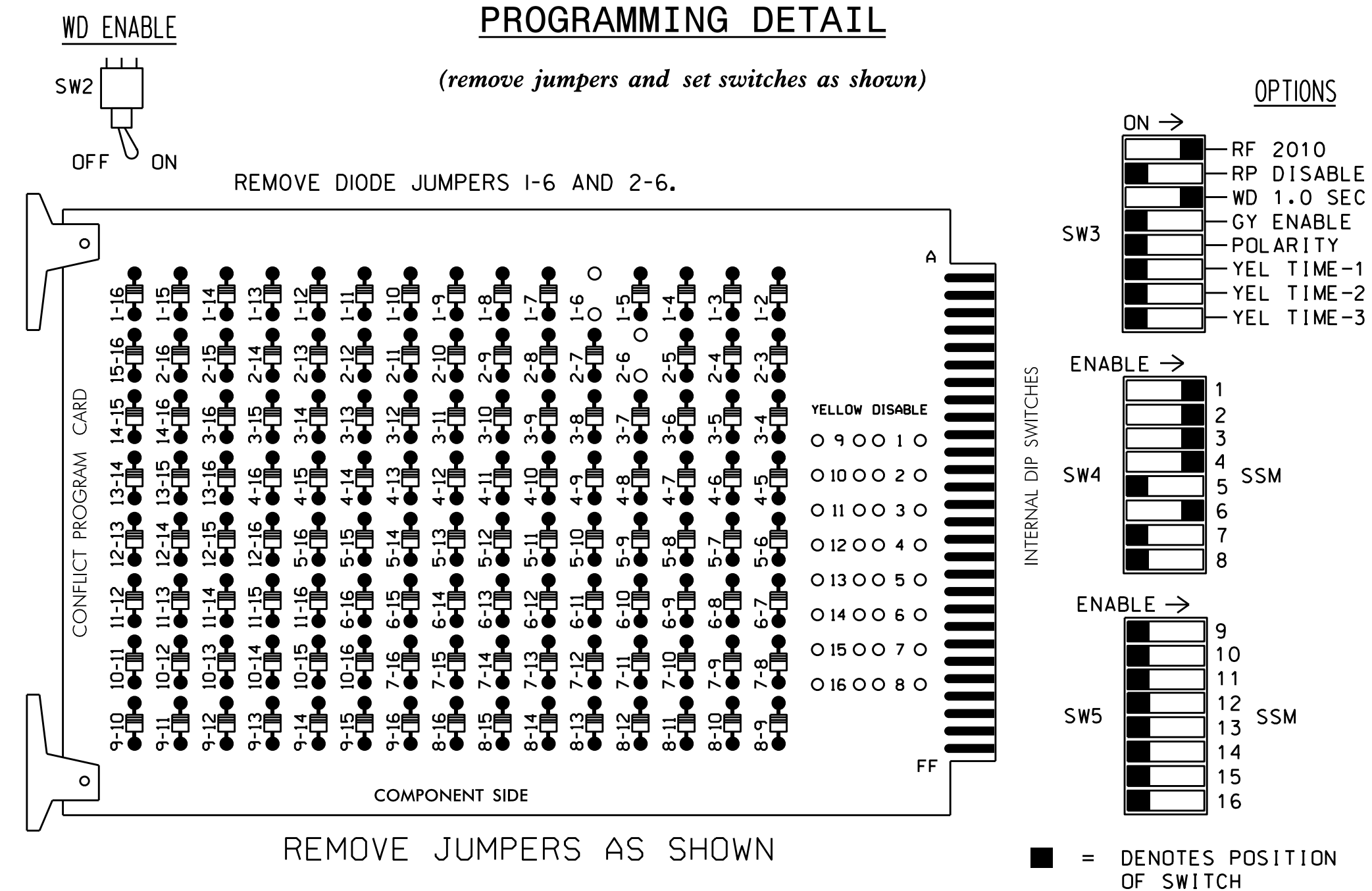


EDI MODEL 2010ECL CONFLICT MONITOR

PROGRAMMING DETAIL



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- To prevent red failures on unused monitor channels, see Red Monitor Board Programming Detail this sheet.
- Program phases 2 and 6, on the controller unit, for Start Up In Green.
- Enable Simultaneous Gap-Out, on the controller unit, for all phases.
- Program phases 2 and 6, on the controller unit, for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Yellow Flash.

EQUIPMENT INFORMATION

CONTROLLER.....EAGLE TYPE 2070L
 CABINET.....McCAIN/CONTROL TECHNOLOGIES
 (DWG.NO.9500-332-NCDOT)
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S1,S2,S3,S4,S6
 PHASES USED.....1,2,3,4,6
 OVERLAPS.....NONE

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	61	21,22	NU	31	32,33	41	42,43	NU	61	62,63	NU	NU
RED	*	128		116	116	101	101		134			
YELLOW		129		117	117	102	102		135			
GREEN		130		118	118	103	103		136			
RED ARROW												
YELLOW ARROW	126											
GREEN ARROW	127			118	103							

NU = Not Used

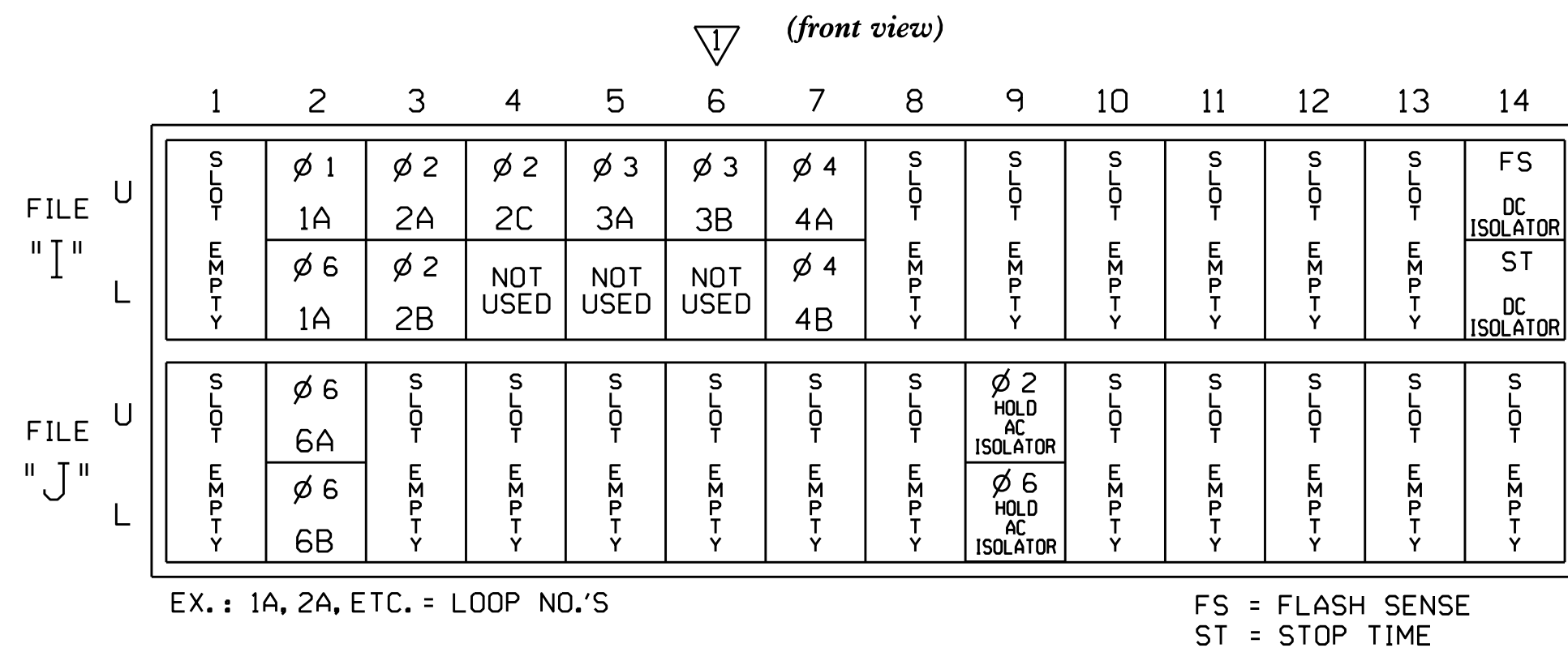
* Denotes install load resistor. See load resistor installation detail this sheet.

BACKUP PROTECTION NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 6 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

INPUT FILE POSITION LAYOUT

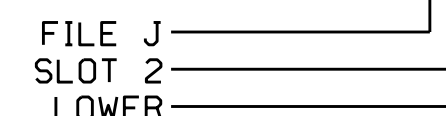


INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-5,6	I2U	39	1	2	1	Y	Y			15
	TB2-7,8	I2L	43	5	12	6	Y	Y	Y		3
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
2C	TB4-1,2	I4U	47	9	22	2	Y	Y	Y		3
3A	TB4-5,6	I5U	58	20	3	3		Y		3.2	
3B	TB4-9,10	I6U	41	3	4	3	Y	Y			5
3C	TB4-11,12	I6L	45	7	14	3	Y	Y			15
4A	TB6-1,2	I7U	65	27	34	4		Y		3.8	
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			10
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			

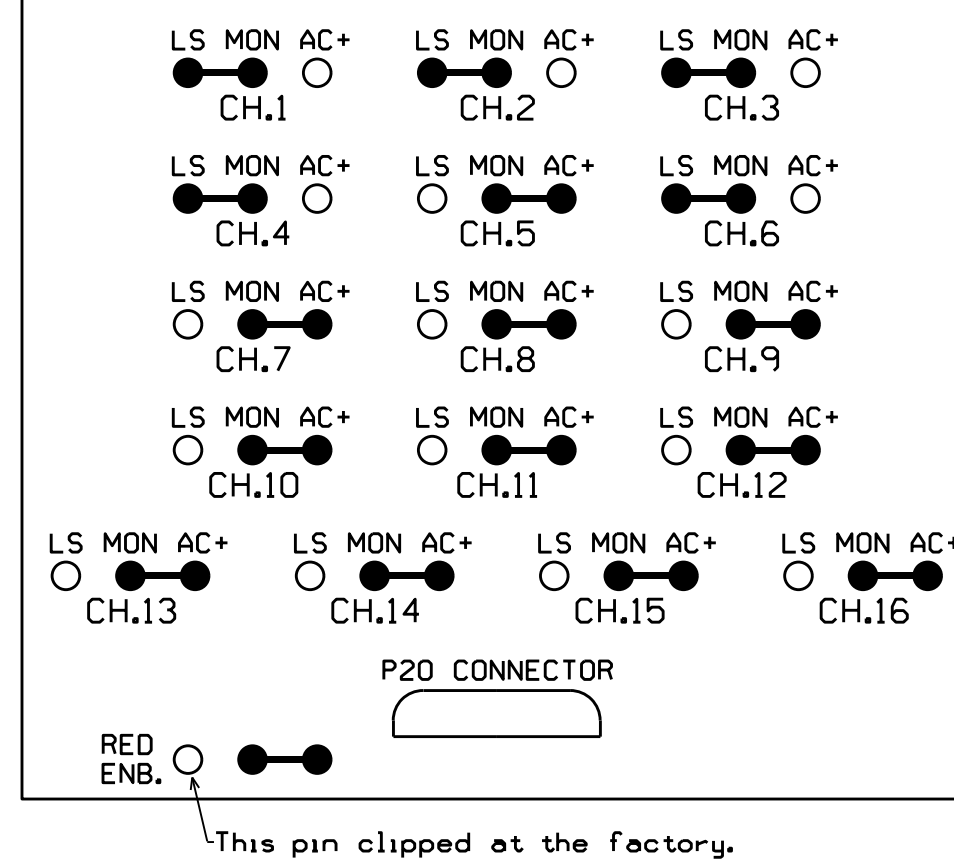
¹Add jumpers from TB2-5 to TB2-7, and from TB2-6 to TB2-8.

INPUT FILE POSITION LEGEND: J2L

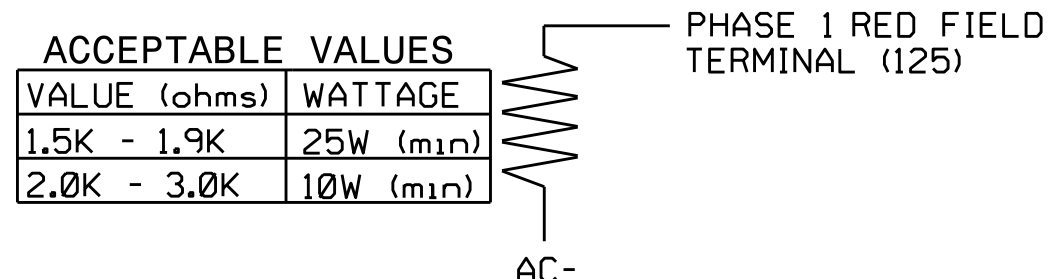


RED MONITOR BOARD PROGRAMMING

(position jumpers as shown below)



LOAD RESISTOR INSTALLATION DETAIL

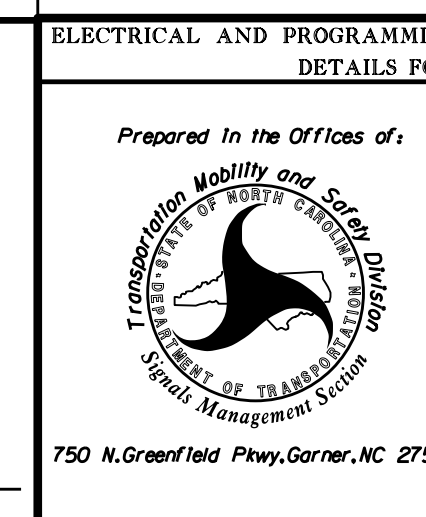
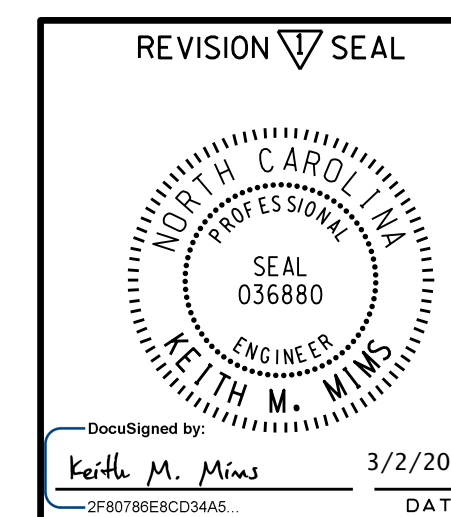


NOTE: The purpose of this resistor is to load the channel red monitor input in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0170
 DESIGNED: January 2016
 SEALED: 2/9/2016
 REVISED: N/A

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



US 74-76 (Andrew Jackson Hwy.)
 at
 NC 87 (Maco Road)/
 SR 1419 (Northwest Road)

PLAN DATE: April 2007	REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	DATE
Changed head 32 from 5-section to 3-section, deleted loop 3C, revised stretch times and monitor jumpers, added a note. (WSA) (DTJ)	3/2/2016

Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by George C. Brown, #022013, on 4/19/07. This document is only certified as to the revisions.

SIGNATURE DATE
 SIG. INVENTORY NO. 03-0170