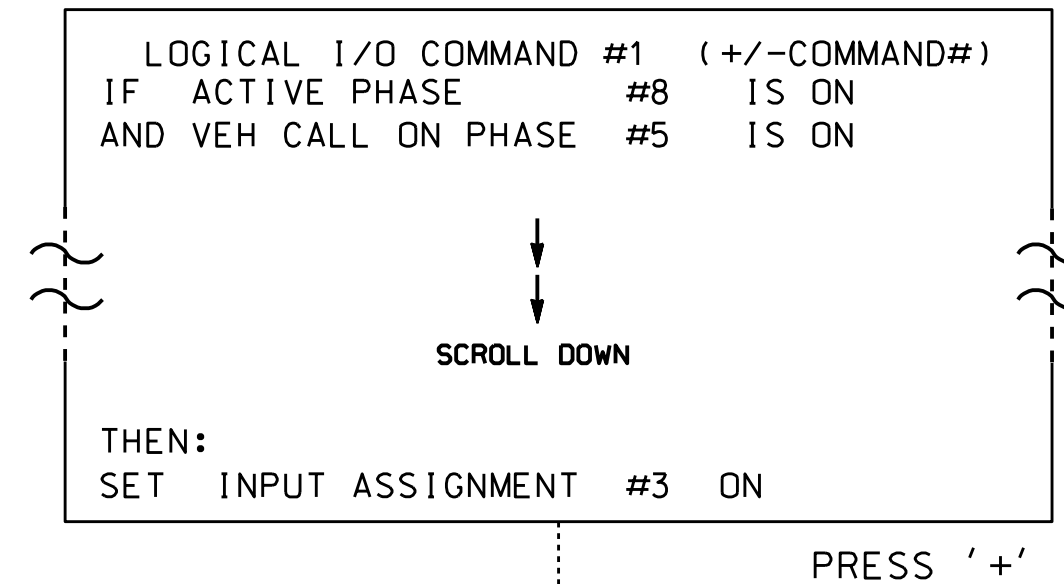


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

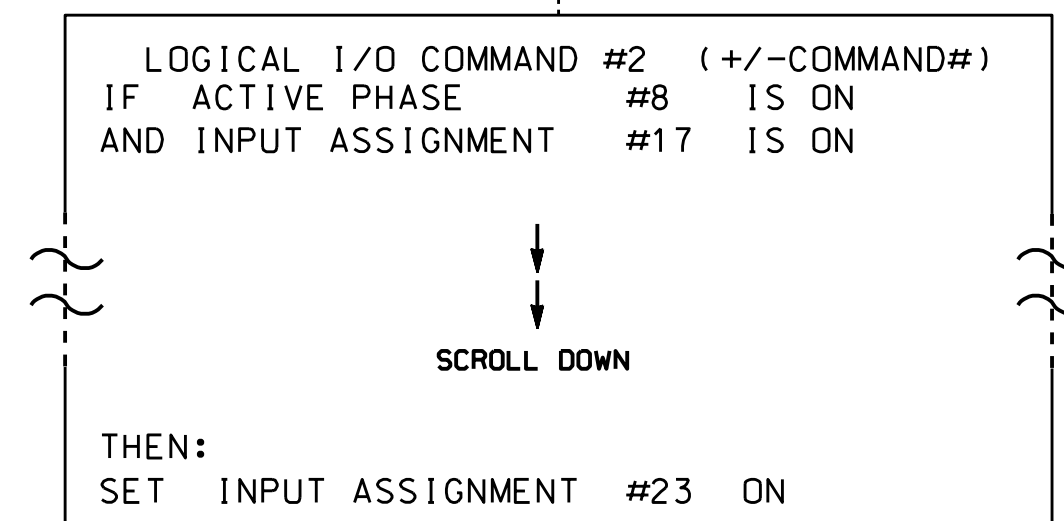
(program controller as shown below)

1. From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Act Logic Commands 1 and 2.
2. From Main Menu press '6' (Outputs), then '3' (Logical I/O Processor).
3. The programming shown below is necessary for signals to operate as shown on the signal design plans.



If the controller is serving phase 8 (PED) and a vehicle call exists on phase 5, the I/O processor will set a phase 4 vehicle call and serve the protected turn and the right turn overlap.

PRESS '+'



If the controller is serving phase 8 (PED) and a vehicle call is placed on the main street phase 5 loop, the I/O processor will set a phase 5 vehicle call with NO DELAY and serve the protected turn and the right turn overlap.

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

INPUT REFERENCE SCHEDULE

INPUT 3 = Phase 4 Veh Call
INPUT 17 = Phase 5 Veh Call (main street loop)
INPUT 23 = Phase 5 Veh Call (virtual detector)

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS: _ RED _ YELLOW _ GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3=25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1=25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...5
    
```

OVERLAP PROGRAMMING COMPLETE

DYNAMIC BACK-UP CONTROL PROGRAMMING

(program controller as shown below)

1. From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Dynamic/Backup Control Function 1.
2. From Phase Control Functions Menu press '2' (Dynamic/Backup Control Functions).

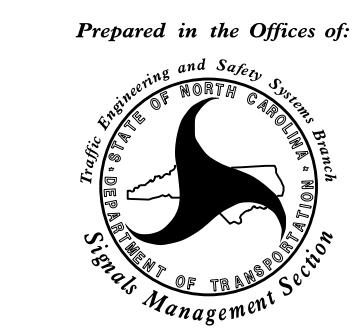
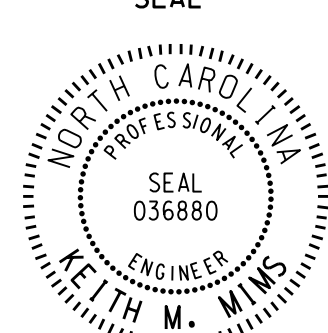
```

DYNAMIC/BACKUP CONTROL FUNCTION #01
OVERLAPS: ABCDEFGHIJKLMNPO
IF OVERLAPS ARE ACTIVE:
OR PHASES: 12345678910111213141516
IF PHASES ARE ON: X
OMIT PHASES:
CALL PHASES: X
    
```

BACKUP PROTECTION PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 03-0236
DESIGNED: September 2015
SEALED: 10/21/2015
REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:  Signal Management Systems, Inc. 750 N. Greenfield Pkwy, Garner, NC 27529	US 117-NC 132 (S. College Rd.) at University Drive		SEAL 
	Division 3 PLAN DATE: October 2015 PREPARED BY: S. Armstrong	New Hanover County REVIEWED BY: T. Joyce REVIEWED BY:	Wilmington DATE: 10/27/2015 DATE:
	REVISIONS INIT. DATE	DocuSigned by: Keith M. Minns 2F8078E8C03445 DATE: 10/27/2015 SIG. INVENTORY NO. 03-0236	