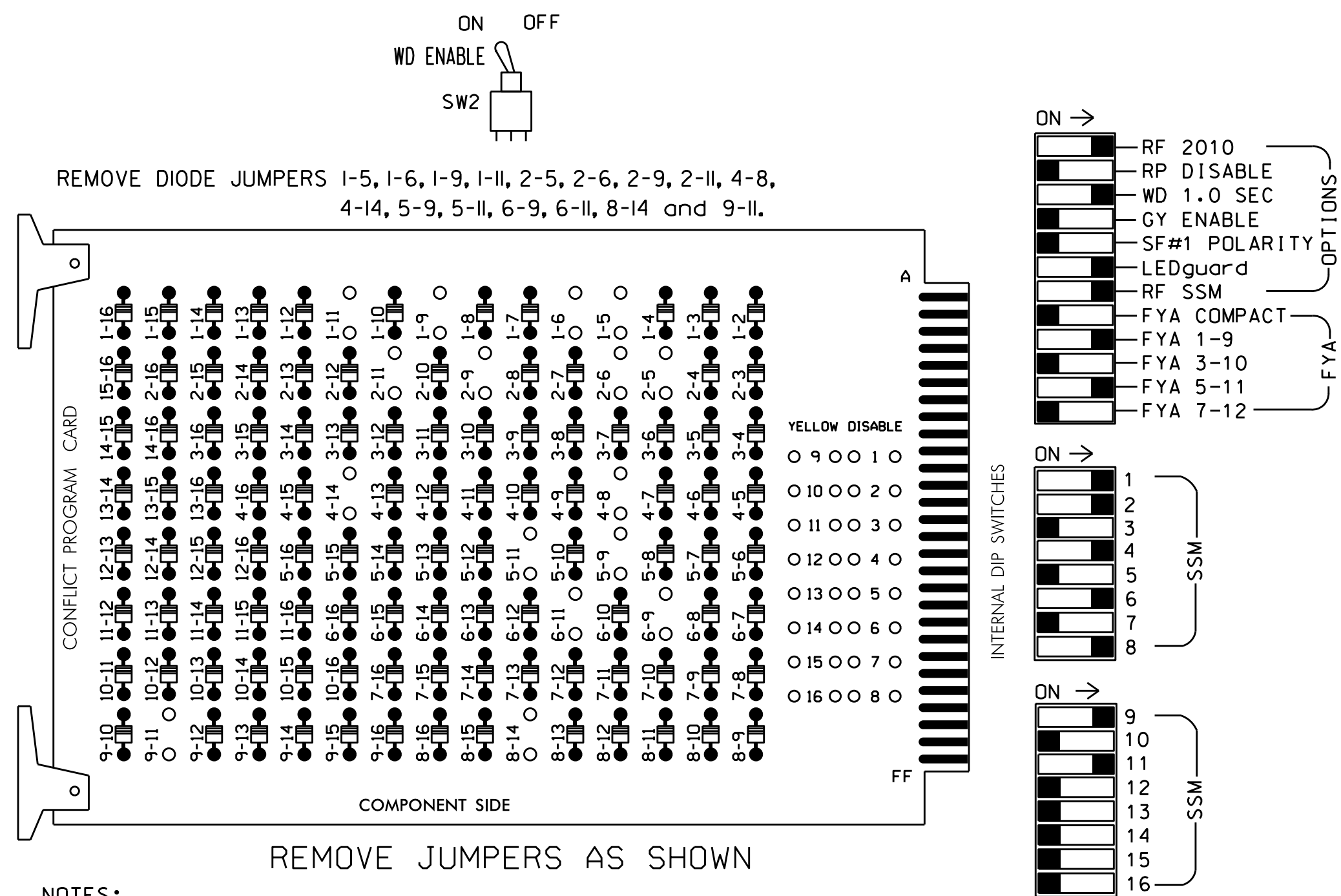


EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 3,5,7, 10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phase 4 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Wilmington Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S4P,S5,S6,S8,S9,S12
 PHASES USED.....1,2,4,4 PED,5,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	9	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	82	21, 22,23	NU	41,42	P41, P42	51★	61, 62,63	NU	81,82	NU	11★	NU	NU	51★	NU	NU	NU	
RED	*	128			101			134		107									
YELLOW		129			102		*	135		108									
GREEN		130			103			136		109									
RED ARROW														A121			A114		
YELLOW ARROW	126													A122			A115		
FLASHING YELLOW ARROW														A123			A116		
GREEN ARROW	127	127						133											
Hand icon																			
Person icon																			

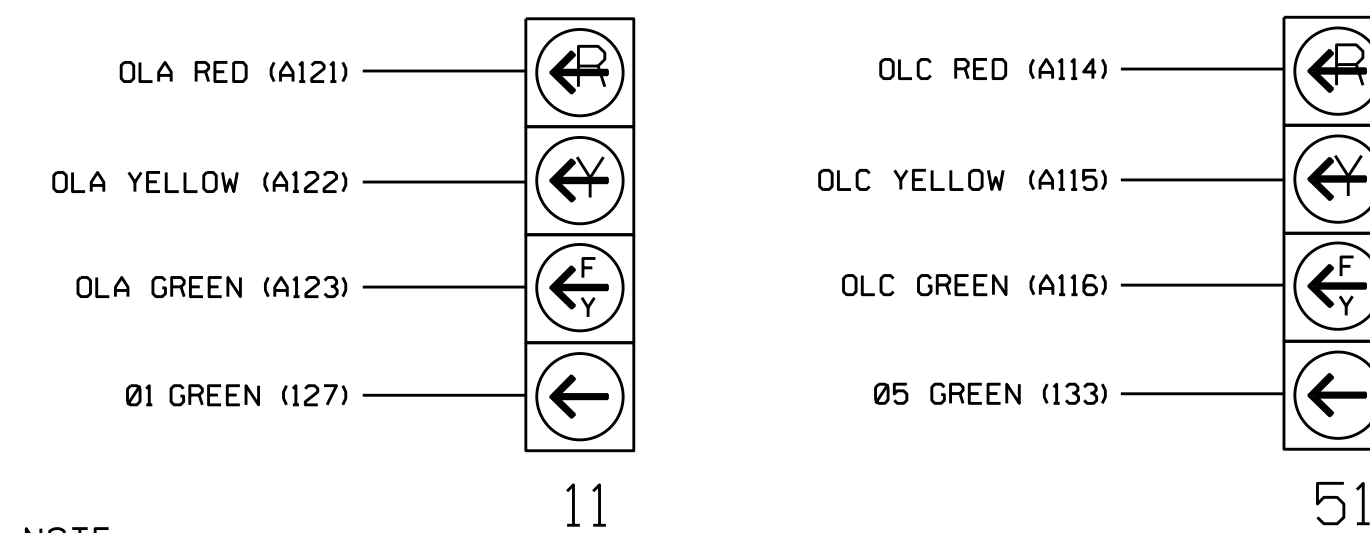
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 1	Ø 1	Ø 2	Ø 2	Ø 3	Ø 4	Ø 4	Ø 5	Ø 5	Ø 6	Ø 6	Ø 7	Ø 7	Ø 8
L	1A	1B	2B	2C	3A	4A	4B	5A	5B	6A	6B	7A	7B	8A
U	NOT USED	Ø 2	Ø 2	Ø 3	Ø 4	Ø 4	Ø 5	Ø 5	Ø 6	Ø 6	Ø 7	Ø 7	Ø 8	Ø 8
L	2A	2C	2C	3A	4A	4B	5A	5B	6A	6B	7A	7B	8A	8B
U	Ø 5	Ø 6	Ø 6	Ø 7	Ø 8	Ø 8	Ø 9	Ø 9	Ø 10	Ø 10	Ø 11	Ø 11	Ø 12	Ø 12
L	5A	6A	6C	7A	8A	8A	9A	9B	10A	10B	11A	11B	12A	12B
U	NOT USED	Ø 6	NOT USED	Ø 7	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED	NOT USED
L	6B	6B	7A	8A	9A	9B	10A	10B	11A	11B	12A	12B	13A	13B

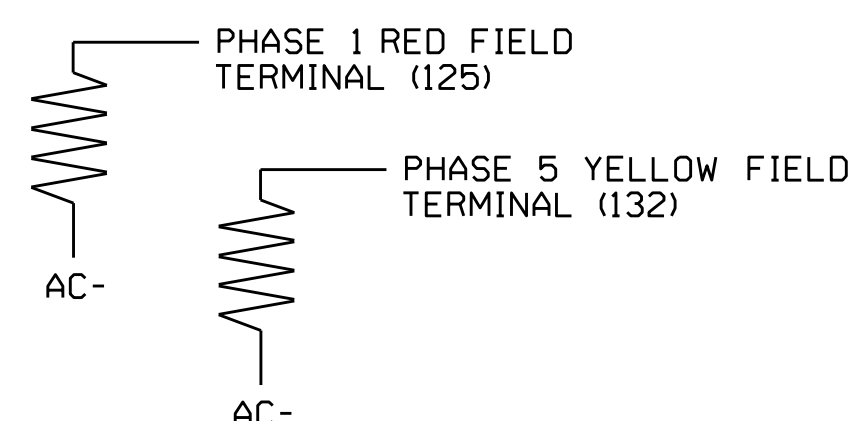
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			10
	-	J4U	48	18★	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
	2A	TB2-7,8	I2L	43	5	12	2	Y	Y		
2B	TB2-9,10	I3U	63	25	32	2	Y	Y			
2C	TB2-11,12	I3L	76	38	42	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y			10
	-	I4U	47	9★	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
	6B	TB3-7,8	J2L	44	6	16	6	Y	Y		
6C	TB3-9,10	J3U	64	26	36	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
* S3	TB7-9,10	J9U	59	21	15	SYS					
PED PUSH BUTTONS											
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					

NOTE:
 INSTALL DC ISOLATOR IN INPUT FILE SLOT I12.

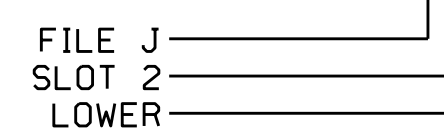
- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

* See Input Page Assignment programming details on sheets 3-4.

* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

Remove the following jumpers, if present:
 From TB2-5 to TB2-7, and from TB2-6 to TB2-8
 From TB3-5 to TB3-7, and from TB3-6 to TB3-8

INPUT FILE POSITION LEGEND: J2L



COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0212
 DESIGNED: September 2015
 SEALED: 3/30/2016
 REVISED: N/A

Electrical Detail - Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	US 117-NC 132 (S College Rd) at Big K/Dick's Sporting Goods	SEAL
	Division 3 New Hanover County Wilmington PLAN DATE: March 2016 REVIEWED BY: BAS PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS INIT. DATE DocuSigned by: Keith M. Mims 4/11/2016 SIG. INVENTORY NO. 03-0212	