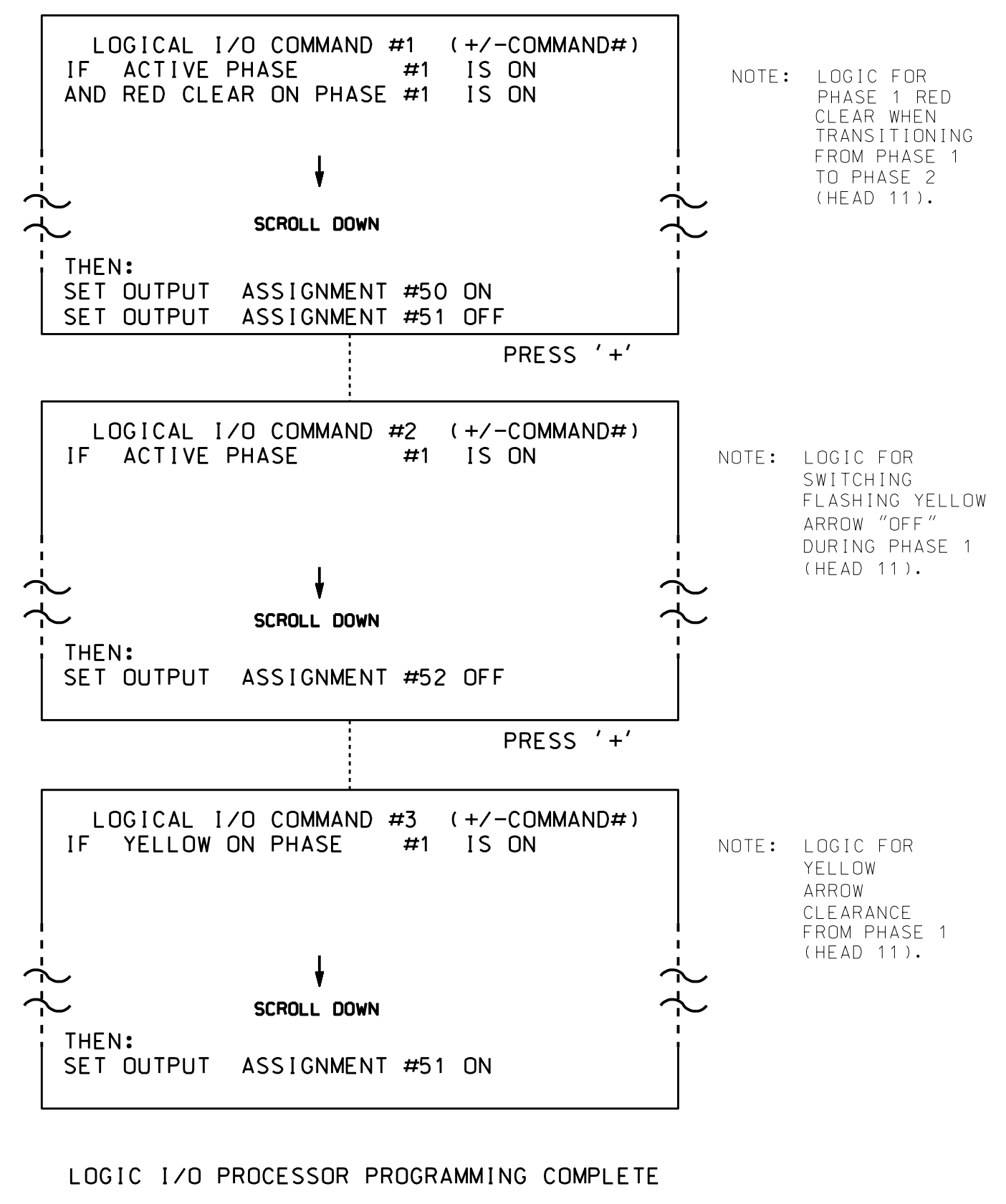


## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



|                                  |
|----------------------------------|
| <b>OUTPUT REFERENCE SCHEDULE</b> |
| OUTPUT 50 = Overlap A Red        |
| OUTPUT 51 = Overlap A Yellow     |
| OUTPUT 52 = Overlap A Green      |

## OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | _ RED _ YELLOW _ GREEN
FLASH COLORS:   | _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

## OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2 →

```

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      |12345678910111213141516
VEH OVL PARENTS: |X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | _ RED _ YELLOW _ GREEN
FLASH COLORS:   | _ RED _ YELLOW _ GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 03-0929  
DESIGNED: January 2016  
SEALED: 2-16-16  
REVISED: N/A

Electrical Detail - Sheet 2 of 4

DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED

REVISION SEAL

Keith M. Mims  
3-03-16  
DATE

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

|  |                       |
|--|-----------------------|
| NC 133 (Castle Hayne Road)<br>at<br>I-140-US 17 EB Ramps |                       |
| Division 03  | New Hanover County    |
| PLAN DATE: September 2009                                | REVIEWED BY: T. Joyce |
| PREPARED BY: S. Armstrong                                | REVIEWED BY:          |
| REVISIONS  |                       |
| Added alternate phasing and updated overlap programming. | KMA                   |
| DATE: 3-03-16  |                       |

SEAL

Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by George C. Brown, PE #022013, on 09/23/09. This document is only certified as to the revisions.

SIGNATURE DATE

SIG. INVENTORY NO. 03-0929

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T. Peterson