

INPUT FILE POSITION LAYOUT

(front view)

1 2 3 4 5 6 7 8 9 10 11 12 13 14

3. Ensure that Red Enable is active at all times during normal operation.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070

controller. Ensure conflict monitor communicates with 2070.

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. Verify that signal heads flash in accordance with the signal plans.
- 2. Program controller to Start Up in phases 2 and 6 green.
- 3. Set power-up flash time to 0 seconds within the controller programming. The conflict monitor will govern startup flash. Ensure STARTUP "RED START" is set to 0 seconds.
- 4. Enable Simultaneous Gap-Out feature for all phases.
- 5. Program all timing information into phase banks 1, 2, and 3 unless otherwise noted.
- 6. Set phase bank 3 maximum limit to 250 seconds for phases
- 7. Ensure start up flash phases are coordinated with flash program block assignments.
- 8. Program Startup Ped Calls for phase 6.
- 9. Set the Red Revert interval on the controller to 1 second.
- 10. This cabinet and controller are part of the Durham Signal System.

EQUIPMENT INFORMATION

SPECIAL DETECTOR NOTE

installation according to manufacturer's directions and NCDOT

Install a video detection system for vehicle detection. Perform

engineer-approved mounting locations to accomplish the detection

SIGNAL HEAD HOOK-UP CHART CMU CHANNEL NO. 8 | 16 15 8 OL1 OL2 SPARE OL3 OL4 SPARE PHASE SIGNAL HEAD NO. NU 22,23 NU NU 42,43 NU NU 62,63 P61, P62 RED 102 YELLOW 103 GREEN RED A114 ARROW YELLOW ARROW FLASHING YELLOW ARROW GREEN ARROW

PROJECT REFERENCE NO.

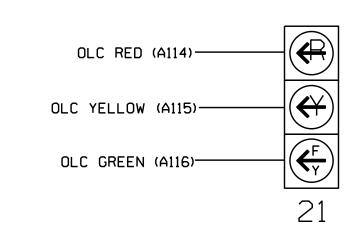
U-3308

Sig. 19.1

NU = Not Used

3 SECTION FYA WIRING DETAIL

(wire signal heads as shown)



OVERLAPS [3] PROGRAMMING DETAIL

Program overlaps as follows: Main Menu - 4) OVERLAP

schemes shown on the Signal Design Plans.

PRESS '+' TWO TIMES

OVERLAP [3]:

LOADSWITCH = 11VEH SET 1 = 6

YELLOW CLEARANCE = 3.9

RED CLEARANCE = 2.4

NOTE: FOR SIGNAL HEAD 21

OVERLAP GREEN FLASH PROGRAMMING FOR 3 SECTION FYA

The following will cause the overlap green outputs to flash, which are wired to the flashing yellow arrow. Program as follows:

Main Menu - 1) PHASE - 2) PHASE FUNCTIONS PAGE TWO OLAP G FL = 3

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1030T3 DESIGNED: September 2014 SEALED: 4-02-15 REVISED: N/A

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

for instructions on selecting this feature.

Electrcial Detail

ELECTRICAL AND PROGRAMMING DETAILS FOR Prepared in the Offices of:

NC 55 (South/North Alston Avenue)

E. Main St.

ivision 5 Durham PLAN DATE: November 2014 REVIEWED BY: PREPARED BY: James Peterson Reviewed BY: ISTATE DATE REVISIONS

John T. Rowe, Jr. 4/2/2015 SIG. INVENTORY NO. 05-1030T3

SEAL

INPUT FILE POSITION LEGEND: J2L FILE J SLOT 2-

LOWER-

P61,P62 | TB8-7,9 |

EX.: 1A, 2A, ETC. = LOOP NO.'S

I13U

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO. TERMINAL FILE POS. NO. NO. ATTRIBUTES NEMA PHASE

26 | 68 | 2

DETECTOR ATTRIBUTES LEGEND:

6 PED

1-FULL TIME DELAY 2-PED CALL 3-RESERVED 4-COUNTING 5-EXTENSION 6-TYPE 3 7-CALLING

FILE

FILE

PED PUSH BUTTONS

8-ALTERNATE

ø6 PED |

NOT

USED

FS = FLASH SENSE ST = STOP TIME

DC DC ISOLATOR

ST

[★] See pictorial of head wiring in detail below.