

# SE-PAC2070 CONTROLLER RING CONFIGURATION DETAIL

(program controller as shown below)

**NOTE:**  
BEFORE PROGRAMMING CONTROLLER, BE SURE TO LOAD DEFAULT PARAMETERS.

SELECT ④ FROM MAIN MENU

SE-PAC UNIT DATA      PRESS # DESIRED

1- STARTUP & MISC	6- ALT SEQUENCES
2- REMOTE FLASH	7- PORT 1 DATA
3- OVERLAP STANDARD	8- I/O MISC
4- OVERLAP SPECIAL	9- SIG DRV OUT
5- RING STRUCTURE	

F- PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 1    RING: 1    NXT PHS: 2    .....

CONCUR PHS: 100011000 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 100000000 0000000000 00000

PED CHN(S): 000000000 0000000100 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 2    RING: 1    NXT PHS: 3    .....

CONCUR PHS: 010011000 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 010000000 0000000000 00000

PED CHN(S): 000000001 0000000000 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 3    RING: 1    NXT PHS: 4    .....

CONCUR PHS: 001000110 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 001000000 0000000000 00000

PED CHN(S): 000000000 0000000010 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 4    RING: 1    NXT PHS: 1    .....

CONCUR PHS: 000100110 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 000100000 0000000000 00000

PED CHN(S): 000000000 1000000000 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 5    RING: 2    NXT PHS: 7    .....

CONCUR PHS: 110010000 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 000010000 0000000000 00000

PED CHN(S): 000000000 0000000001 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 6    RING: 2    NXT PHS: 5    .....

CONCUR PHS: 110001000 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 000001000 0000000000 00000

PED CHN(S): 000000000 0100000000 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 7    RING: 2    NXT PHS: 8    .....

CONCUR PHS: 001100100 0000000

PHS/CHN: 123456789 0123456789 01234

VEH CHN(S): 000000100 0000000000 00000

PED CHN(S): 000000000 0000000000 10000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

SE-PAC RING STRUCTURE      (0-NO / 1-YES)

PHASE: 8    RING: 2    NXT PHS: 6    .....

CONCUR PHS: 001100010 0000000

PHS/CHN: 123456789 0123456789 01234

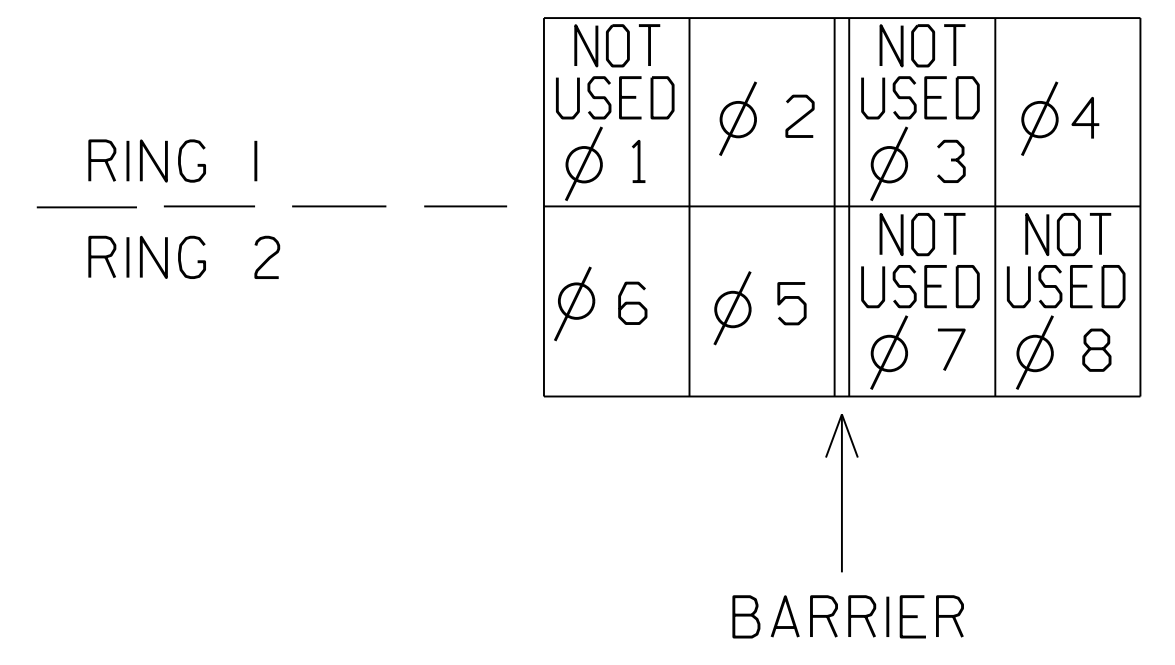
VEH CHN(S): 000000010 0000000000 00000

PED CHN(S): 000000000 0010000000 00000

A-UP B-DN D-DspChn E-EDIT F-PRIOR MENU

*end of programming*

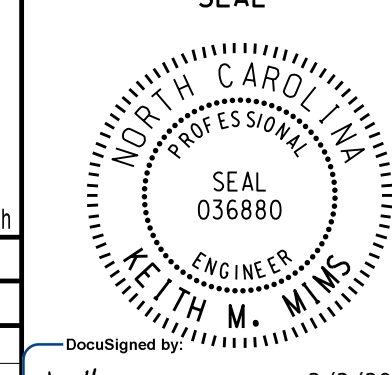
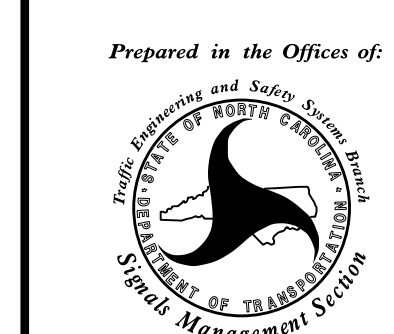
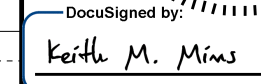
DUAL-QUAD WITH  
PHASES 5 & 6 ROTATED



THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 05-1642T2  
DESIGNED: December 2015  
SEALED: 2/1/2016  
REVISED: N/A

DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED

Electrical Detail - Temp Design 2 (TMP Area II Phase I, Stage II) - Sheet 2 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:	<b>W. Peace Street</b> at <b>US 70 WB-401/NC 50 NB</b> (Capital Blvd.) Ramps	SEAL 
Prepared in the Offices of:  750 Greenfield Parkway, Garner, NC 27529	Division 5      Wake County      Raleigh PLAN DATE: January 2016      REVIEWED BY: T. Joyce PREPARED BY: S. Armstrong      REVIEWED BY:	DocuSigned by:  2/2/2016 DATE
REVISIONS      INIT.      DATE		SIG. INVENTORY NO. 05-1642T2

05-1642-2016\_09-16  
S:\ITS\ASST\TSS\Sig\Signal\work\hgr\051642\_sm.ele.xxx.dgn  
sarmstrong