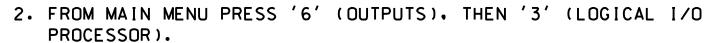
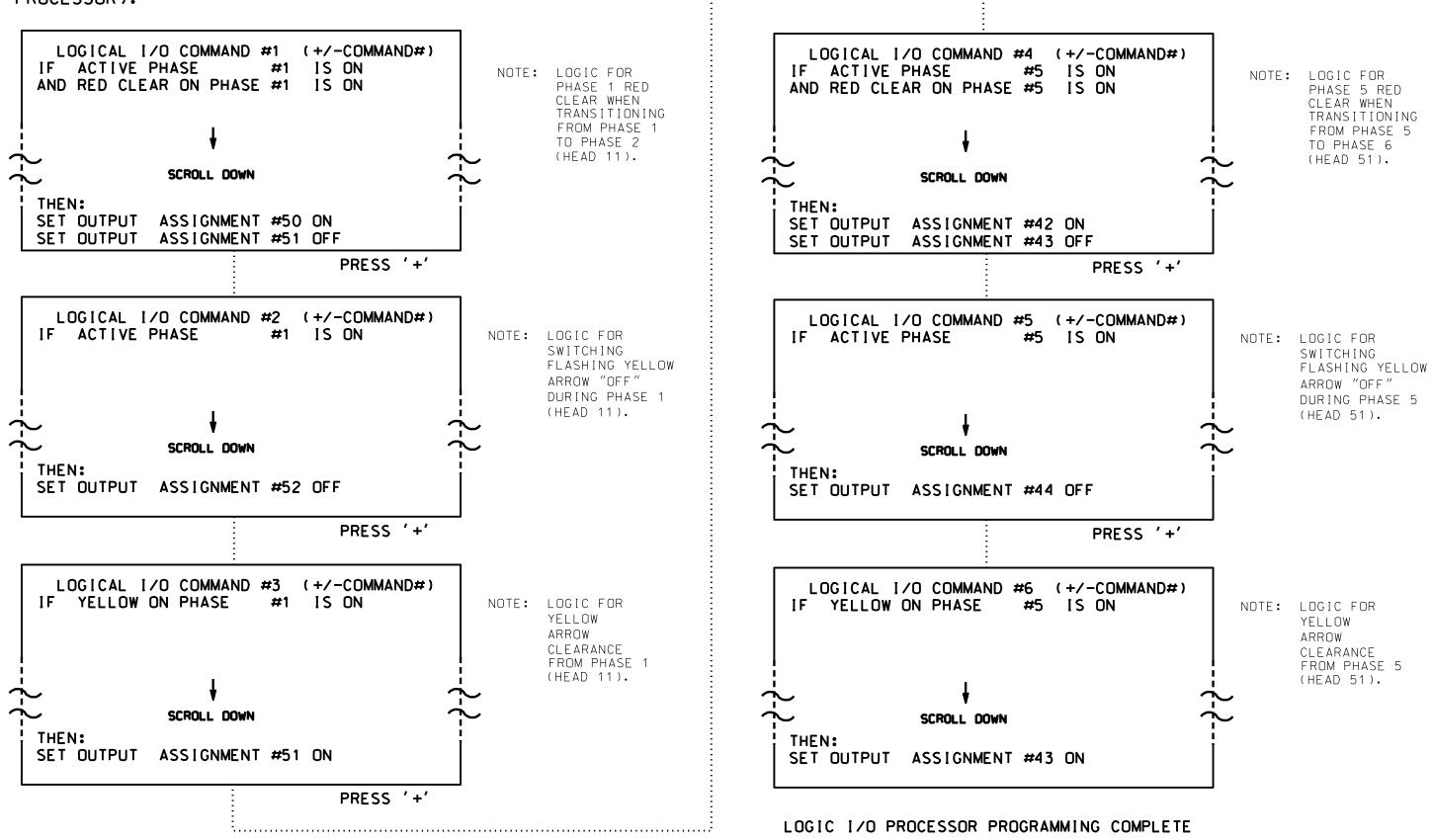
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.





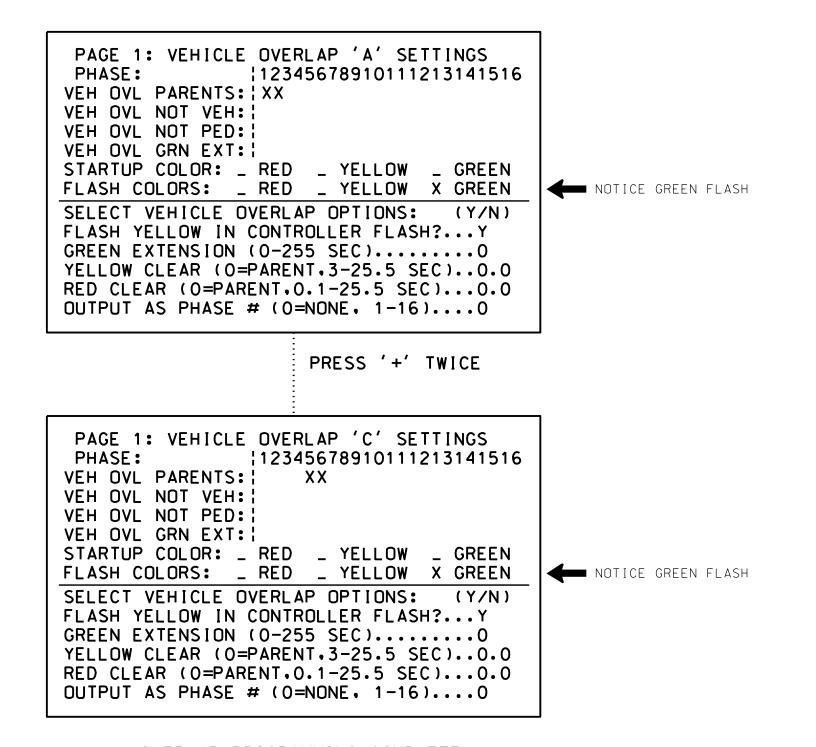
OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



PROJECT REFERENCE NO.

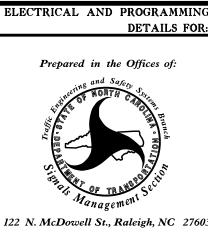
W-5206AM

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0221
DESIGNED: September 2015
SEALED: September 28, 2015
REVISED:

Signal Upgrade - Sheet 2 of 2





NC 24-87 (Bragg Boulevard)

Pearl Street

Division 06 Cumberland County Fayatevill
PLAN DATE: September 2015 REVIEWED BY: D. Clodgo
PREPARED BY: B. Wan REVIEWED BY:

REVISIONS INIT. DATE



SIGNATURE DATE
SIG. INVENTORY NO. 06-0221

...*06-0221_sm_ele_20150928. IISFR: WANR