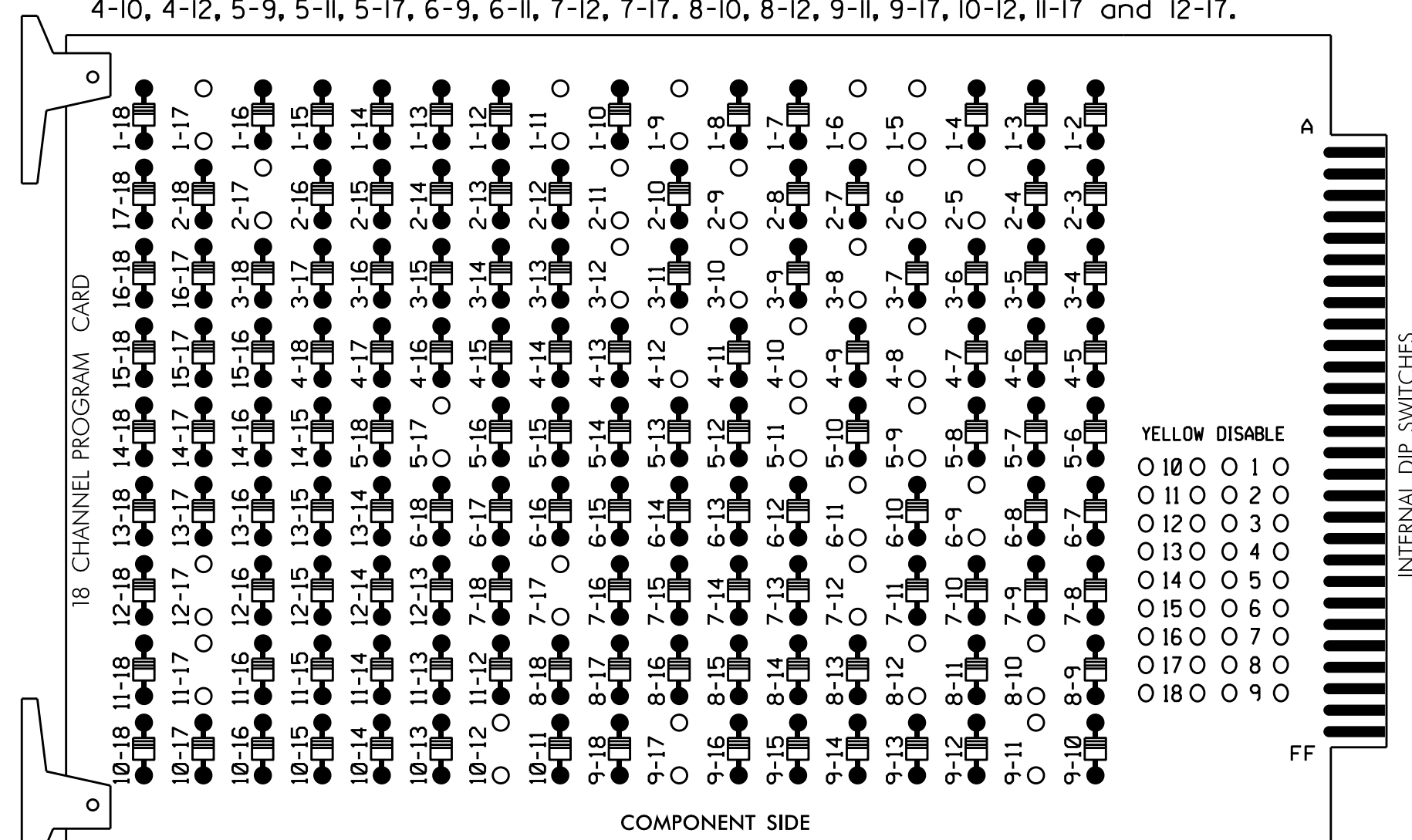


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

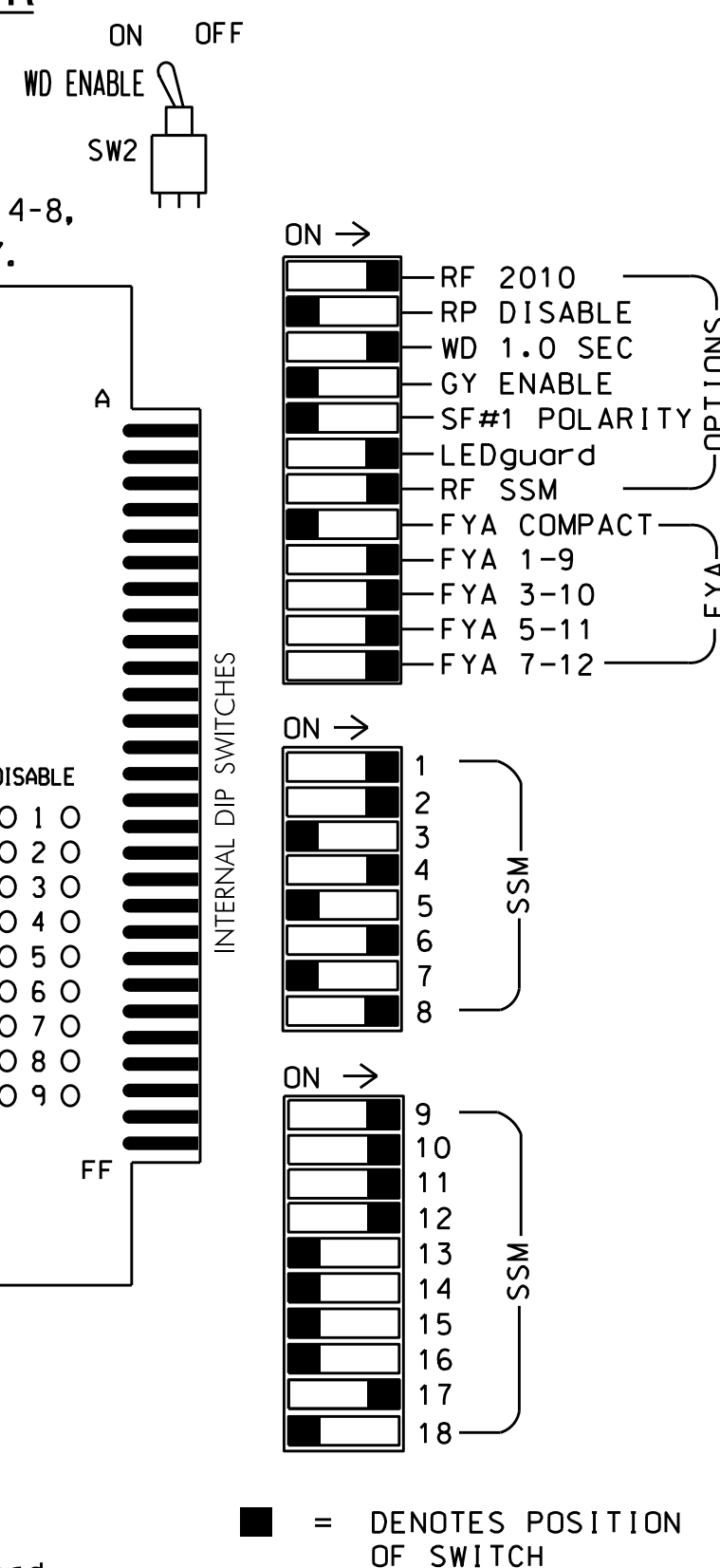
REMOVE DIODE JUMPERS: 1-5, 1-6, 1-9, 1-11, 1-17, 2-5, 2-6, 2-9, 2-11, 2-17, 3-8, 3-10, 3-12, 4-8, 4-10, 4-12, 5-9, 5-11, 5-17, 6-9, 6-11, 7-12, 7-17, 8-10, 8-12, 9-11, 9-17, 10-12, 11-17 and 12-17.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 4 and 8 for Start Up In Green.
- Program phases 4 and 8 for Yellow Flash and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,
 AUX S1,AUX S2,AUX S3,AUX S4,AUX S5.
 PHASES USED.....1,2,*3,4,5,6,*7,8.
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8
 OVERLAP "E".....5+7
 *USED ONLY DURING PREEMPT

SIGNAL HEAD HOOK-UP CHART

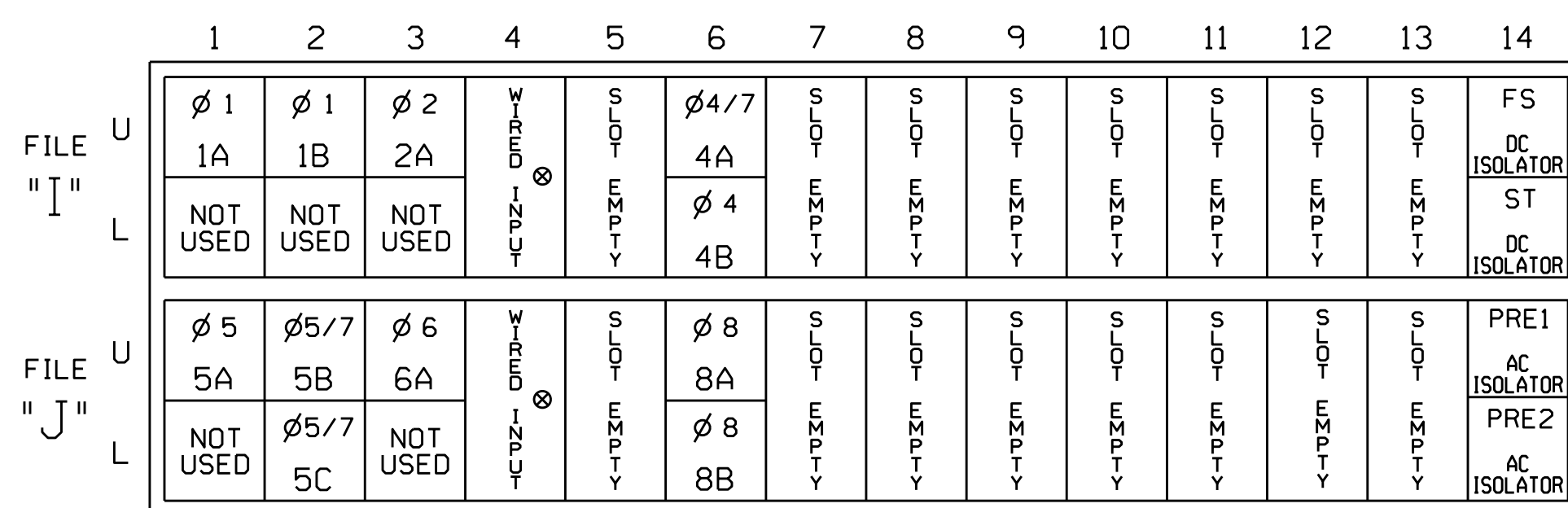
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	DLA	DLB	OLE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	82	21,22	NU	31	41,42	NU	51	61,62	NU	71	81,82	NU	11	31	42	51	71	NU
RED	*		128			101			134			107			*				
YELLOW			129	*		102		*	135		*	108							
GREEN			130			103			136			109							
RED ARROW														A121	A124		A114	A101	
YELLOW ARROW		126												A122	A125	A112	A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW	127	127				118			133			124					A113		

NU = Not Used

- * Denotes install load resistor. See load resistor installation detail this sheet 3.
 * See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME
 PRE = PREEMPT

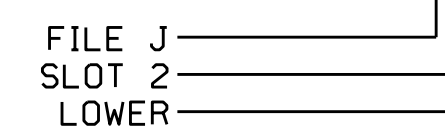
⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4/7	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
5A ³	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
5B	TB3-5,6	J2U	40	2	6	5/7	Y	Y			15
5C	TB3-7,8	J2L	44	6	16	5/7	Y	Y			20
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



PHASE SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU
 SELECT: 4 PHASE SEQUENCE

PHASE SEQUENCE:	PAGE	1	NEXT:	PAGES)
RNG:LEAD	BARRIER 1	X-LAG:LEAD	BARRIER 2	X-LAG
1	1	2	0	0
2	5	6	0	0
3	0	0	0	0
4	0	0	0	0

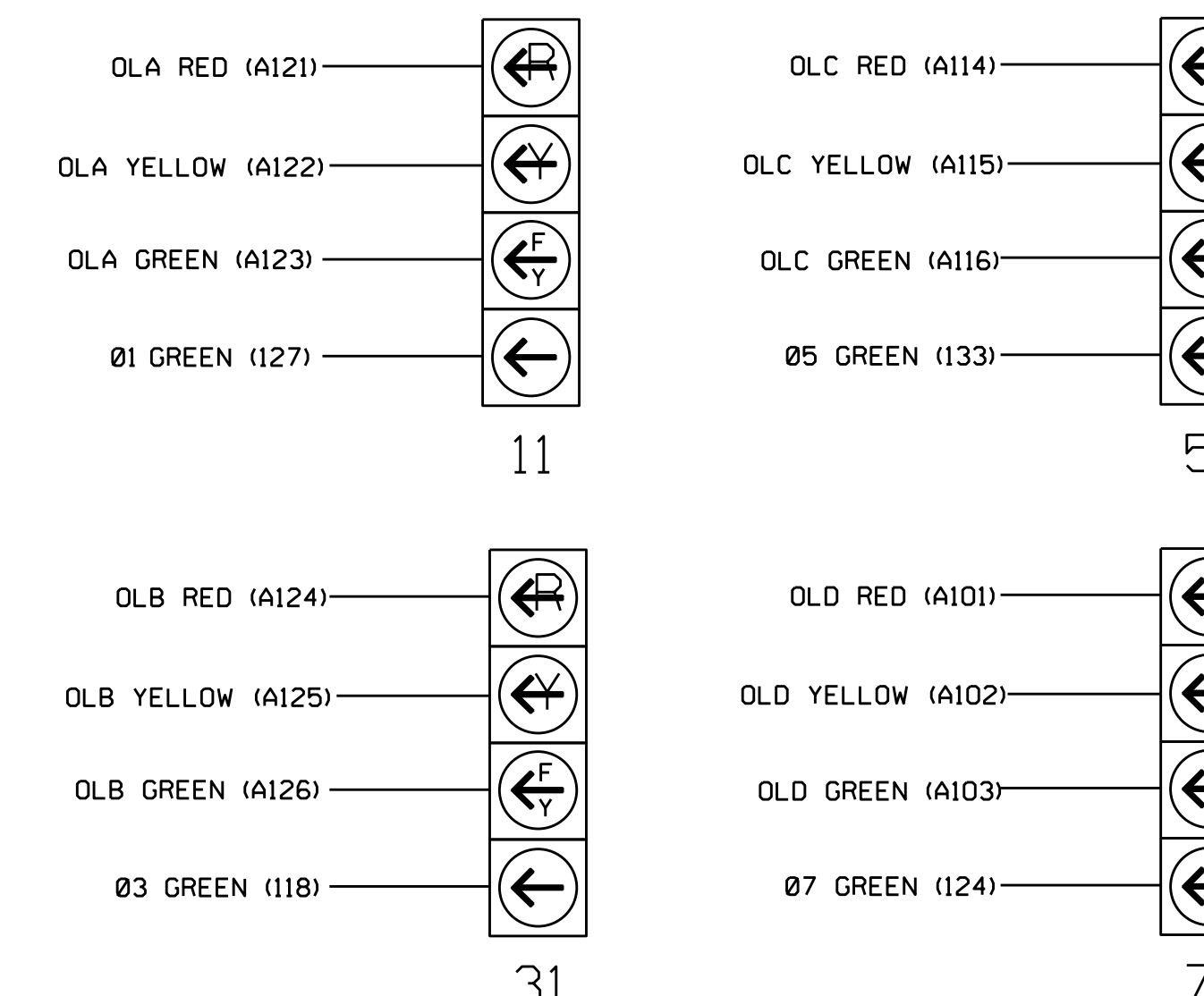
PREEMPT ONLY PHASE OMIT NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control), Then '1' (Phase Control Functions). Program Phases 3 & 7 for 'Omit Phase' and Phases 1, 2, 4, 5, 6 and 8 for 'Startup Calls'. This is to prevent Phases 3 and 7 from being served when not in Preempt.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

- The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 08-0707
 DESIGNED: July 2014
 SEALED: 5-06-15
 REVISED: N/A

ELECTRICAL DETAIL SHEET 1 OF 5

<p>Electrical and Programming Details For:</p> <p>Prepared In the Offices of:</p> <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	SR 1595 (Surrett Drive) at SR 1592 (Eden Terrace) and Corporation Drive	SEAL 	
	Division 8 PLAN DATE: July 2014 PREPARED BY: James Peterson	as of Archdale REVIEWED BY: JTR REVIEWED BY:	SIG. INVENTORY NO. 08-0707
	DocuSigned by: John T. Rowe, Jr. 5/6/2015		