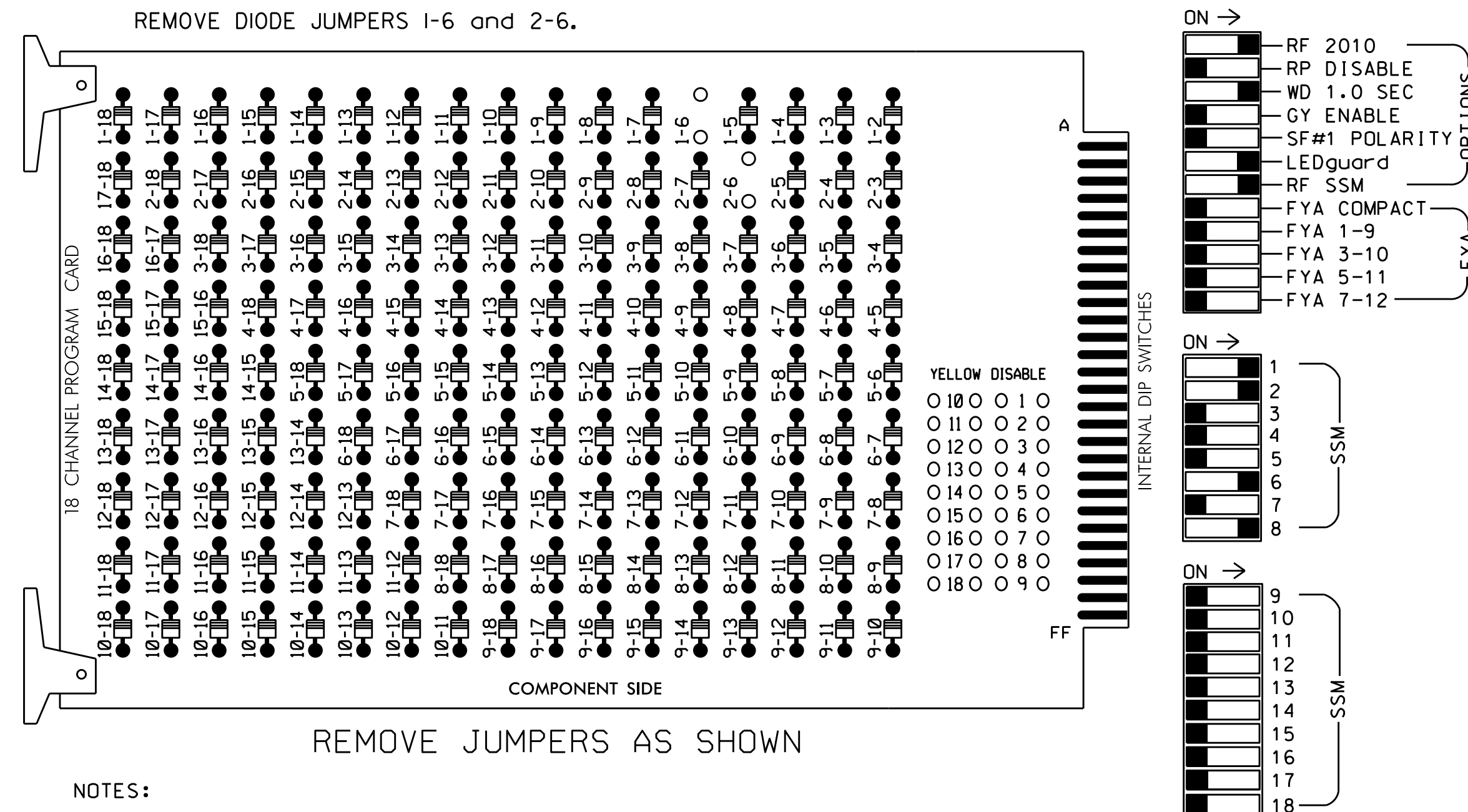


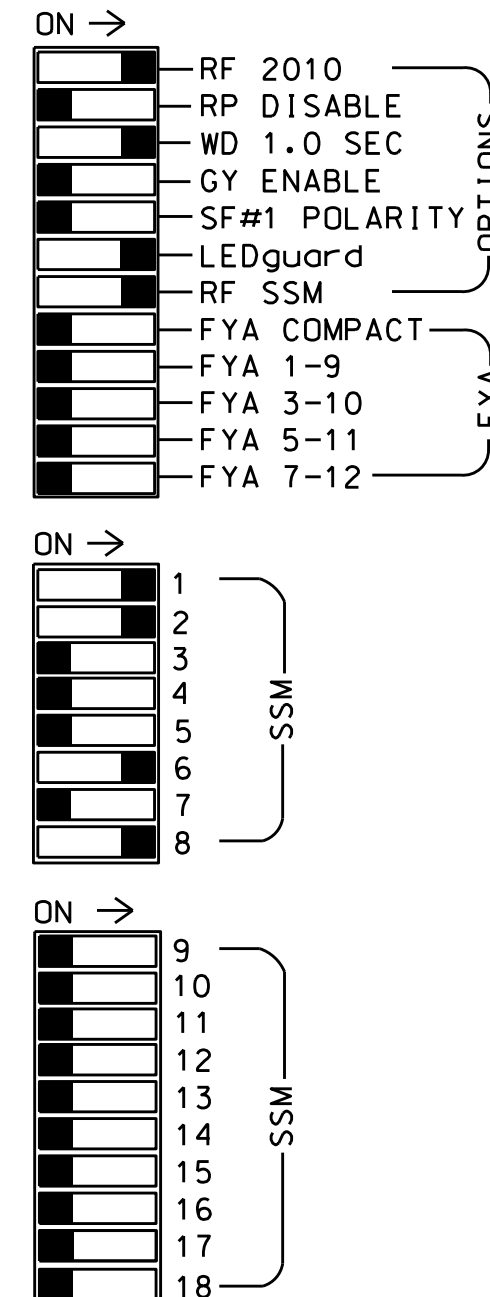
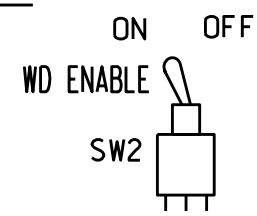
**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash.
6. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S1,S2,S8,S11
 PHASES USED.....1,2,6,8
 OVERLAPS.....NONE

SIGNAL HEAD HOOK-UP CHART

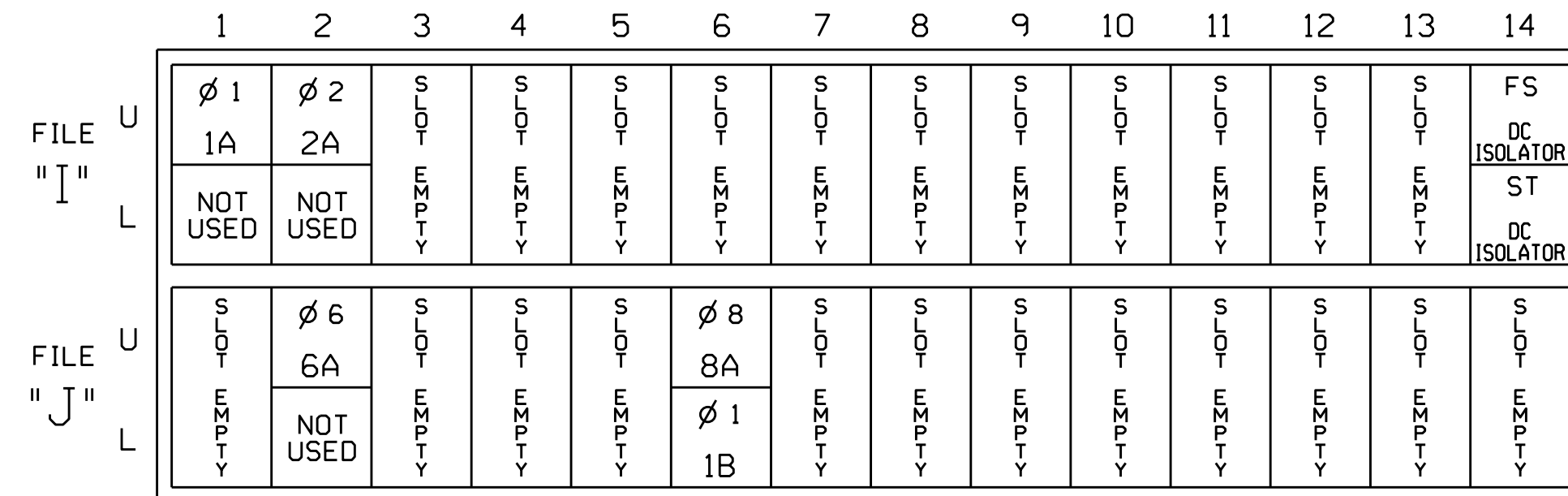
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	61,82	21,22	NU	NU	NU	NU	NU	61,62	NU	NU	81,82	NU
RED	*	128						134			107	
YELLOW		129						135			108	
GREEN		130						136			109	
RED ARROW												
YELLOW ARROW	126											
GREEN ARROW	127											

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



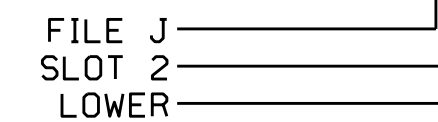
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

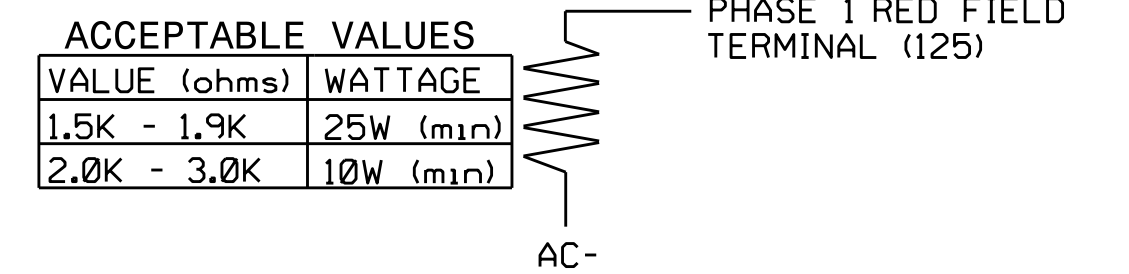
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			10
1B	TB5-11,12	J6L	46	8	18	1	Y	Y			15
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3

INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)



NOTE: The purpose of this resistor is to load the channel red monitor input in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 08-0505
 DESIGNED: April 2014
 SEALED: 3/31/2015
 REVISED: N/A

Electrical Detail

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

 750 N. Greenfield Pkwy, Garner, NC 27529

SR 1577 (Archdale Road) at SR 1681 (Balfour Drive)

Division 8 Randolph County Archdale

PLAN DATE: July 2014 REVIEWED BY: T. Joyce
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS: _____ INIT. DATE: _____

DocuSigned by: George C. Brown 4/9/2015
 SEAL: PROFESSIONAL ENGINEER SEAL 022013 GEORGE C. BROWN
 SIG. INVENTORY NO. 08-0505