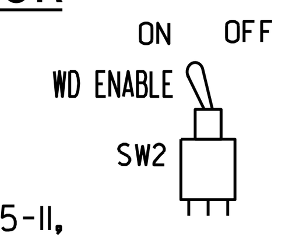
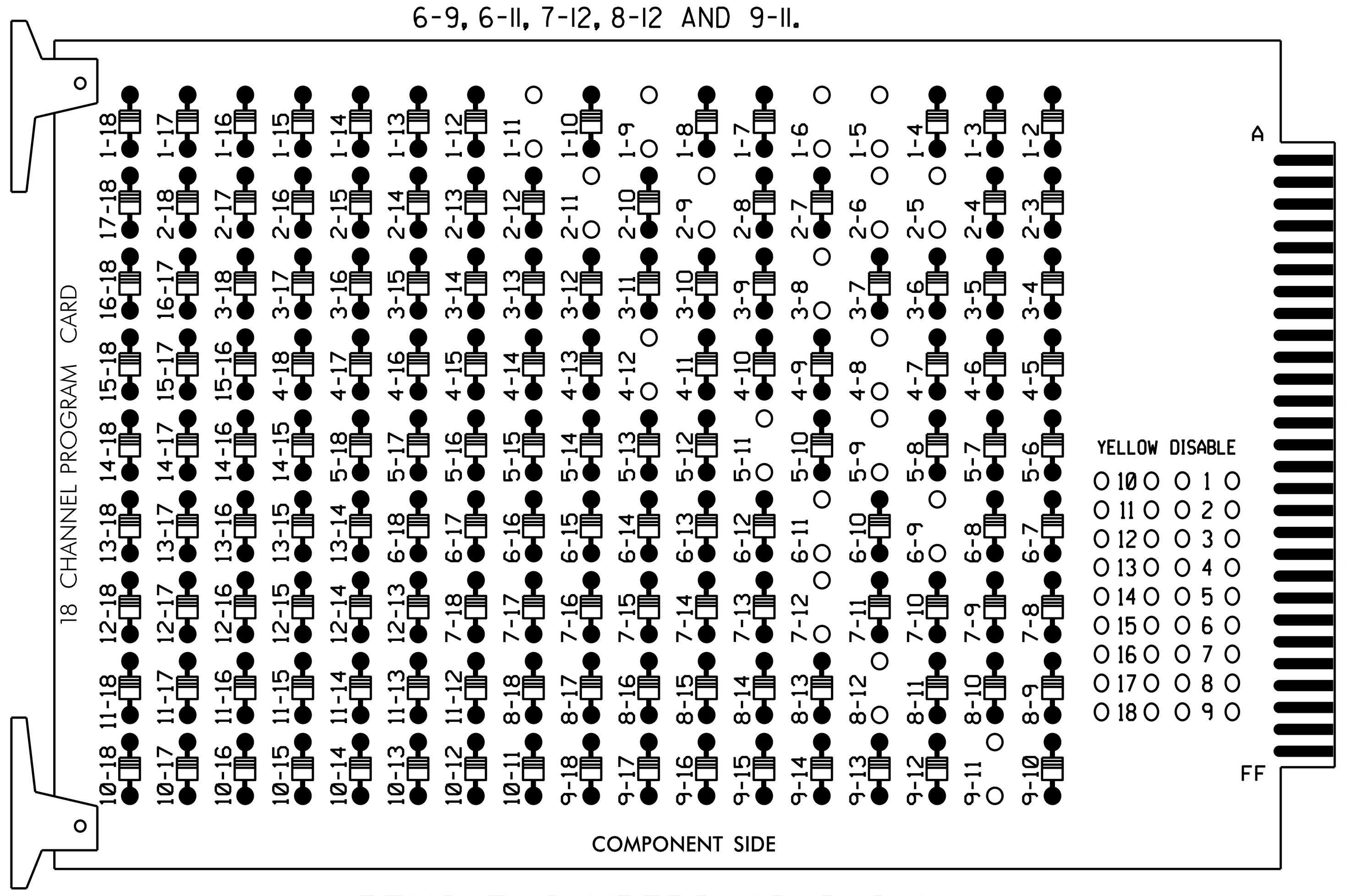
PROJECT REFERENCE NO.	SHEET NO.
C-5558	Sig. 172.1

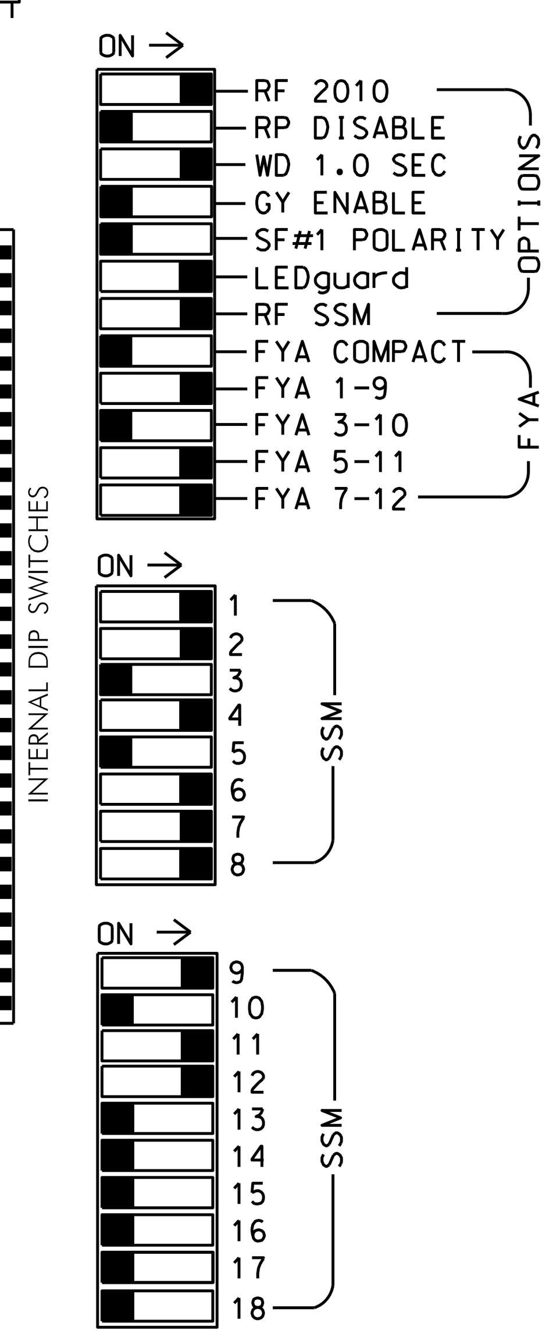
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-8, 4-8, 4-12, 5-9, 5-11, 6-9, 6-11, 7-12, 8-12 AND 9-11.







REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER..............2070L
CABINET.................332 /W/ AUX
SOFTWARE................ECONOLITE OASIS
CABINET MOUNT...........BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED......S1,S2,S4,S5,S7,S8,S10,S11, AUX S1,AUX S4,AUX S5.
PHASES USED.............1,2,*3,4,5,6,*7,8.
OVERLAP "A".............1+2
OVERLAP "B".............NOT USED
OVERLAP "C".............5+6
OVERLAP "D".............7+8
OVERLAP "P".............1+2+4+5+6+8

*USED ONLY DURING PREEMPT

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4	S5	S6	S7	S8	S9	S10		S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3	4	14	5	6	15	7		8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3	4	4 PED	5	6	6 PED	7		8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	82	21,22	NU	81	41,42	NU	51★	61,62 63	NU	62	71★	81 82,83	NU	11★	NU	NU	51★	71★	NU
RED	*		128			101			134			*	107							
YELLOW			129		*	102		*	135				108							
GREEN			130			103			136				109							
RED ARROW															A121			A114	A101	
YELLOW ARROW		126									123				A122			A115	A102	
FLASHING YELLOW ARROW															A123			A116	A103	
GREEN ARROW	127	127			118			133			124	124								

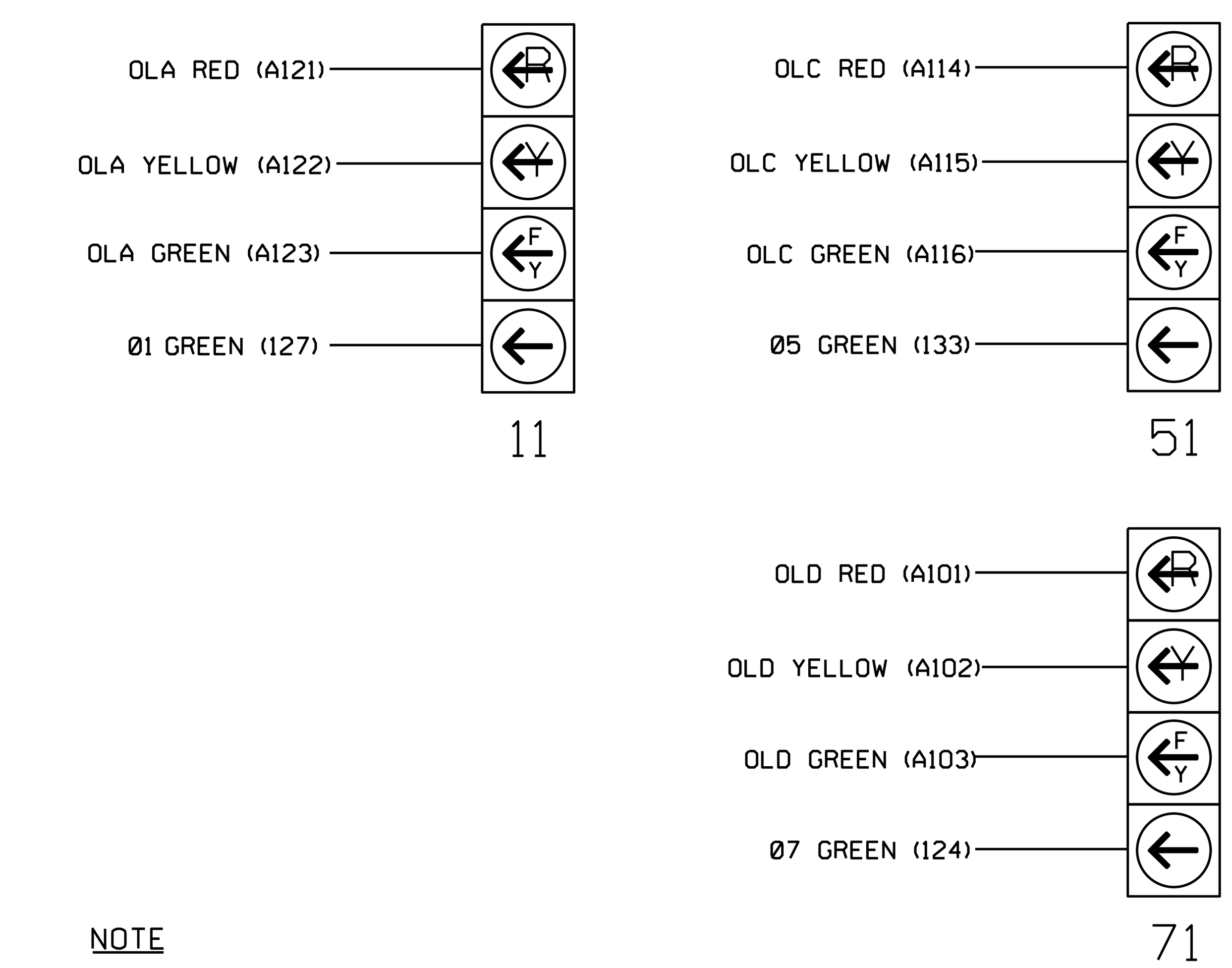
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE		1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	U	ø1 1A	ø1 1B	ø2 2A	WIRED INPUT ⊗	SLOT EMPTY	ø4 4B	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
"I"	L	NOT USED	NOT USED	NOT USED			ø4 4C								ST DC ISOLATOR
"J"	U	ø5 5A	ø6 6A	SLOT EMPTY	WIRED INPUT ⊗	ø7 4A	ø8 8A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	PRE1 AC ISOLATOR
"J"	L	NOT USED	NOT USED			NOT USED	ø8 8B								NOT USED

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME
PRE = PREEMPT

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
4B	TB4-9,10	I6U	41	3	4	4	Y	Y			
4C	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A[2]	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
4A[3]	TB5-5,6	J5U	57	19	7	7	Y	Y			3
	-	I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			

[1] Add jumper from I1-W to J4-W, on rear of input file.

[2] Add jumper from J1-W to I4-W, on rear of input file.

[3] Add jumper from J5-W to I8-W, on rear of input file.

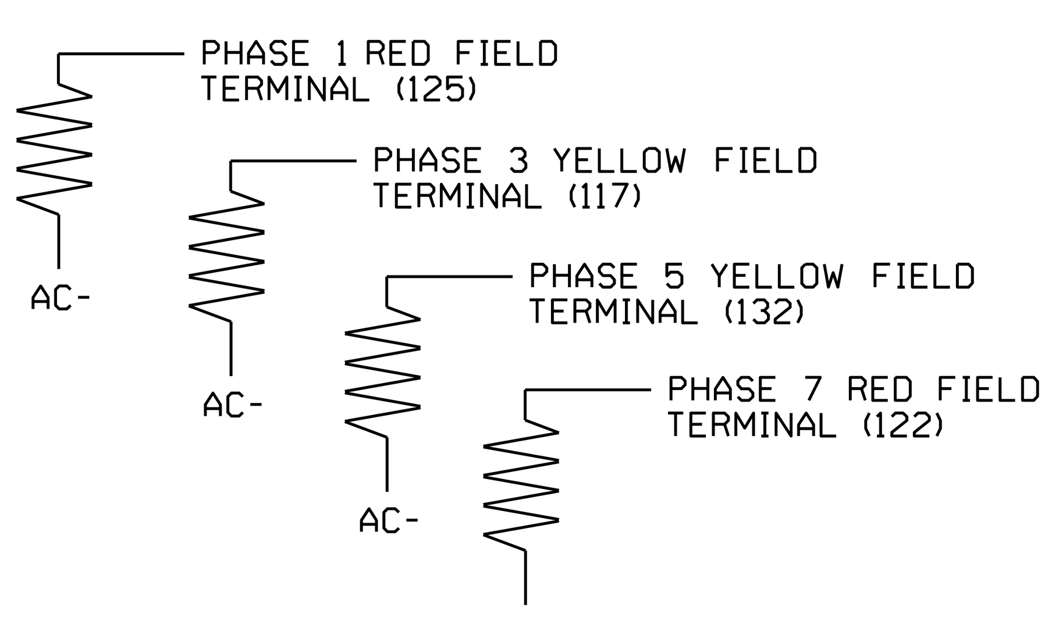
INPUT FILE POSITION LEGEND: J2L

FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



PREEMPT ONLY PHASE OMIT NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control). Then '1' (Phase Control Functions). Program Phase 3 and 7 for 'Omit Phase' and Phases 1, 2, 4, 5, 6 and 8 for 'Startup Calls'. This is to prevent Phases 3 and 7 from being served when not in Preempt.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-2171
DESIGNED: August 2014
SEALED: 4-24-15
REVISED: N/A

ELECTRICAL DETAIL SHEET 1 OF 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of: Transportation Mobility and Safety Division, Signals Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

SR 4053 (Surrett Drive)
at
Fraley Road/Finch Avenue

Division 7 Guilford County High Point

PLAN DATE: September 2014 REVIEWED BY: JTR

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 008453 ENGINEER JOHN T. ROWE, JR.

DocuSigned by: John T. Rowe, Jr. 5/28/2015 DATE

SIG. INVENTORY NO. 07-2171

28-MAY-2015 07:14
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jtpeterson