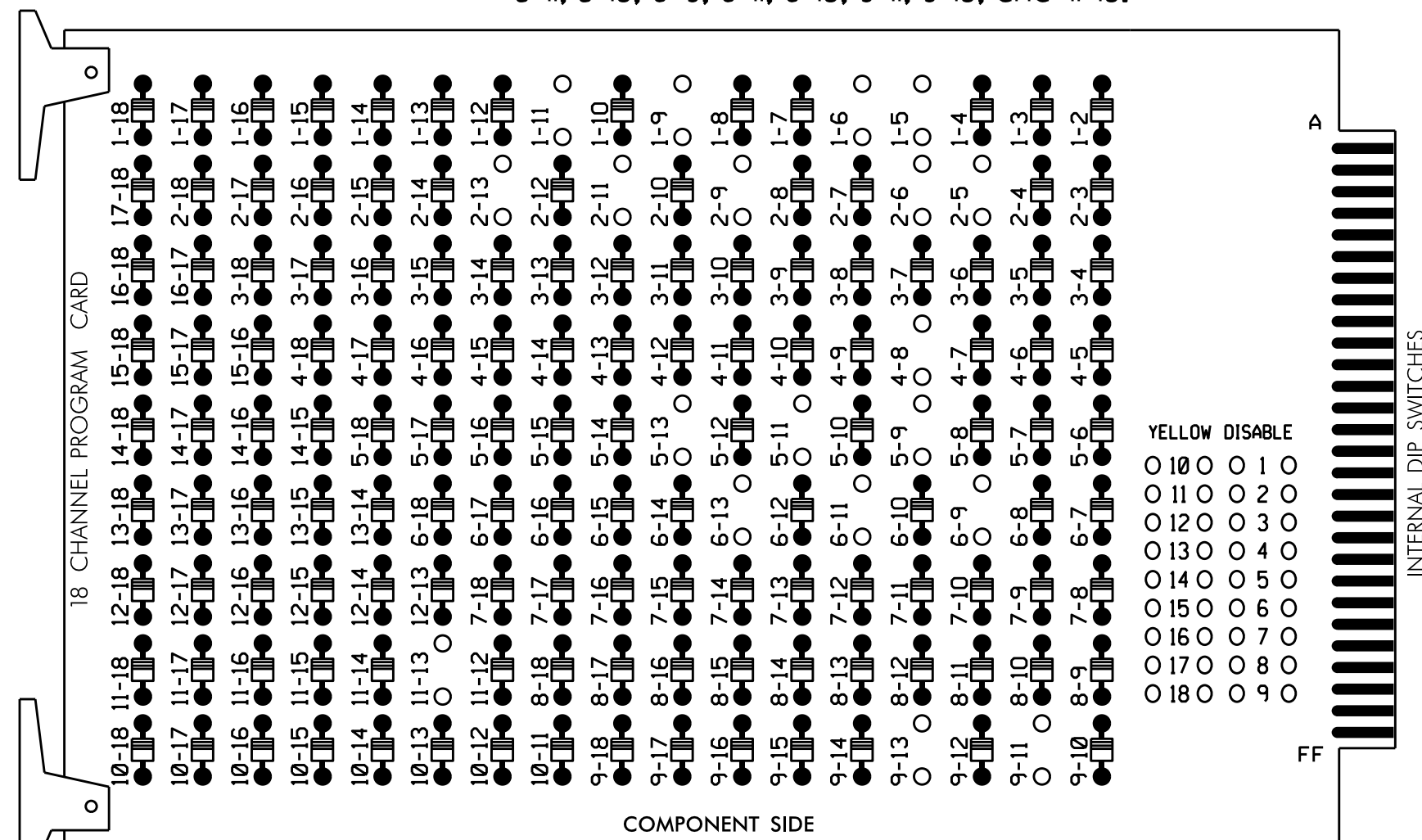


**EDI MODEL 2018EClip-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

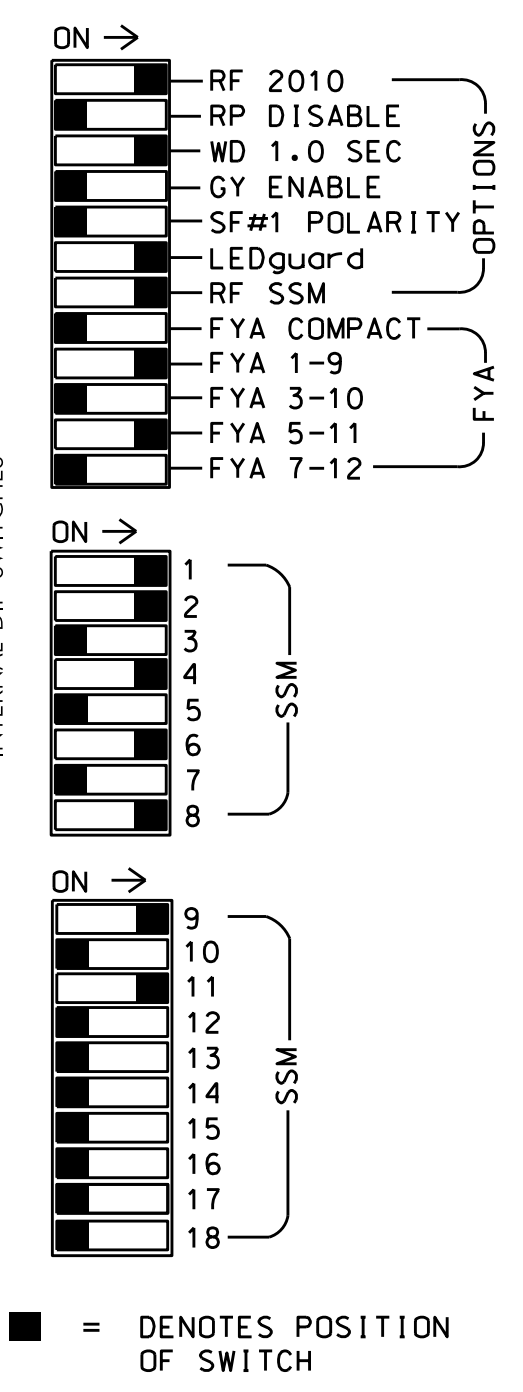
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 2-13, 4-8, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 9-11, 9-13, and 11-13.



REMOVE JUMPERS AS SHOWN

**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phase 2 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
7. The cabinet and controller are part of the High Point Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3\*,S5,S7,S8,S11,  
 AUX S1,AUX S4  
 PHASES USED.....1,2,2PED,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED  
 \* S3 USED FOR PEDS AND FOR FLASHER

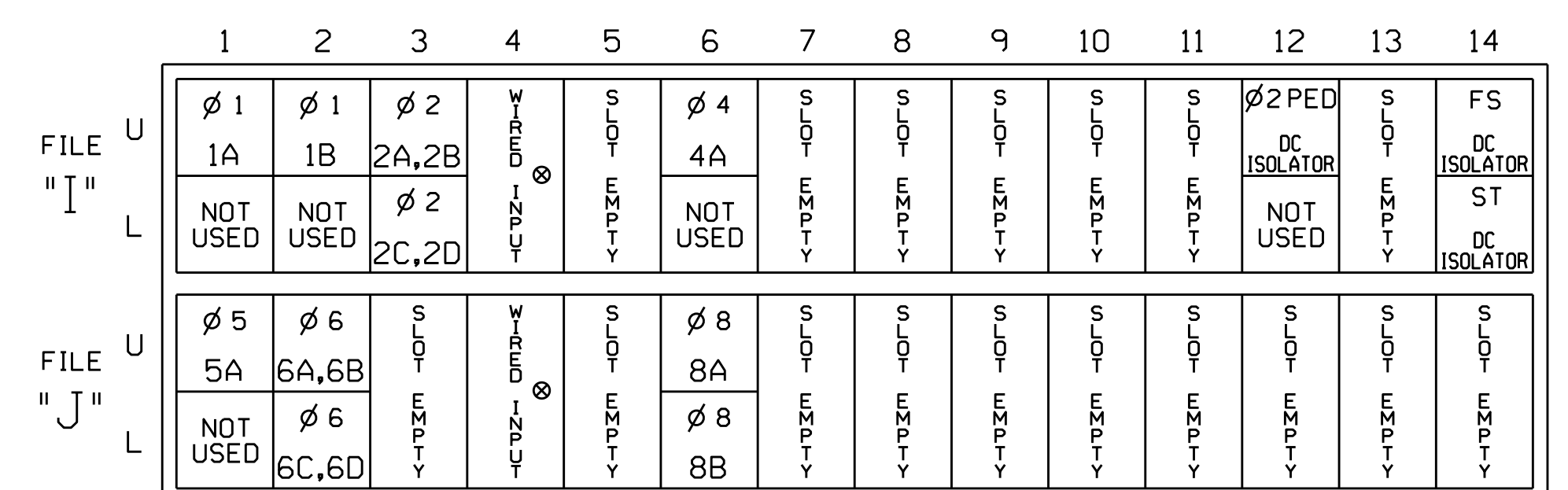
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED SPECIAL EVENT FLASHER	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22 P21, P22	101	NU	41,42	NU	51	61,62	NU	NU	81,82	NU	11	NU	NU	51	NU
RED	*	128				101			134			107						
YELLOW		129				102		*	135			108						
GREEN		130				103			136			109						
RED ARROW																A121		A114
YELLOW ARROW		126														A122		A115
FLASHING YELLOW ARROW																A123		A116
GREEN ARROW	127	127							133									
PED YELLOW																		** 114
																		115

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.  
 \*\* S3-Y used for Special Event Flasher. See sheet 3 for wiring and programming details.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

**COUNTDOWN PEDESTRIAN SIGNAL OPERATION**

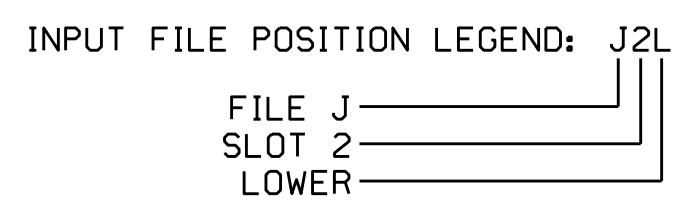
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A,2B	TB2-9,10	I3U	63	25	32	2	Y	Y		1.6	
2C,2D	TB2-11,12	I3L	76	38	42	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y		1.6	
6C,6D	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					

NOTE:  
 INSTALL DC ISOLATOR IN INPUT FILE SLOT 112.

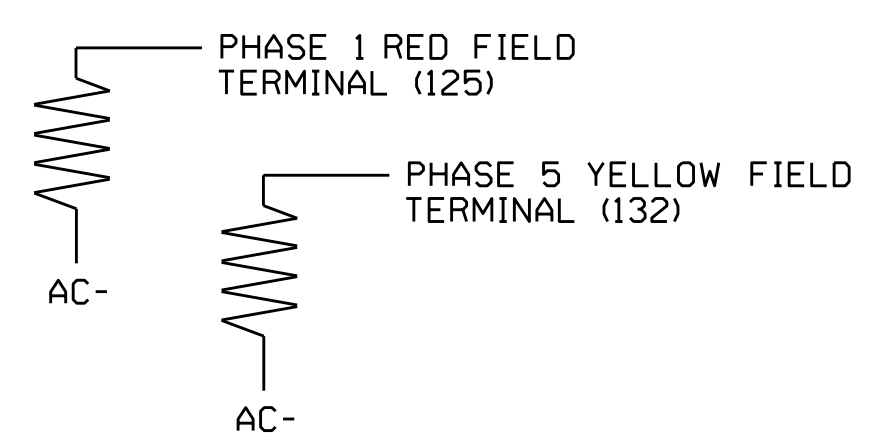
- <sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.
- <sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.



**LOAD RESISTOR INSTALLATION DETAIL**

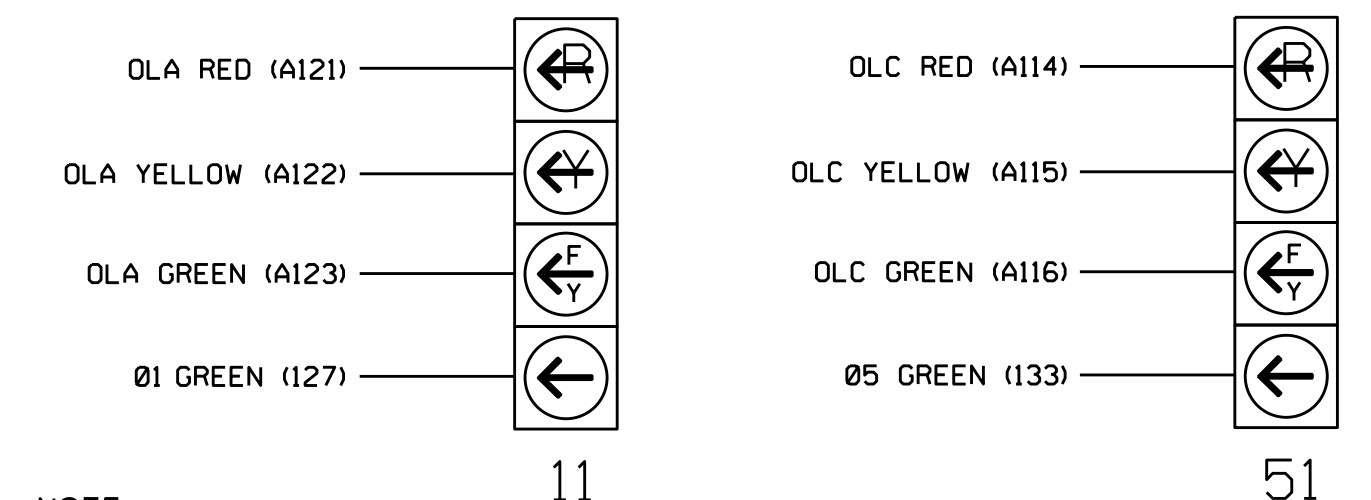
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2101  
 DESIGNED: May 2014  
 SEALED: 4/2/2015  
 REVISED: N/A

Electrical Detail - Sheet 1 of 3

Prepared In the Offices of:  
**TRANSPORTATION MOBILITY AND SAFETY SOLUTIONS**  
 750 N. Greenfield Pkwy, Garner, NC 27529

NC 68 (Eastchester Drive) at SR 1538 (Deep River Road) / Lake Forest Drive

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: [Signature]  
 PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

SEAL  
 JOHN T. ROWE, JR.  
 ENGINEER  
 SEAL 008453

DocuSigned by:  
 John T. Rowe, Jr. 4/8/2015

SIG. INVENTORY NO. 07-2101