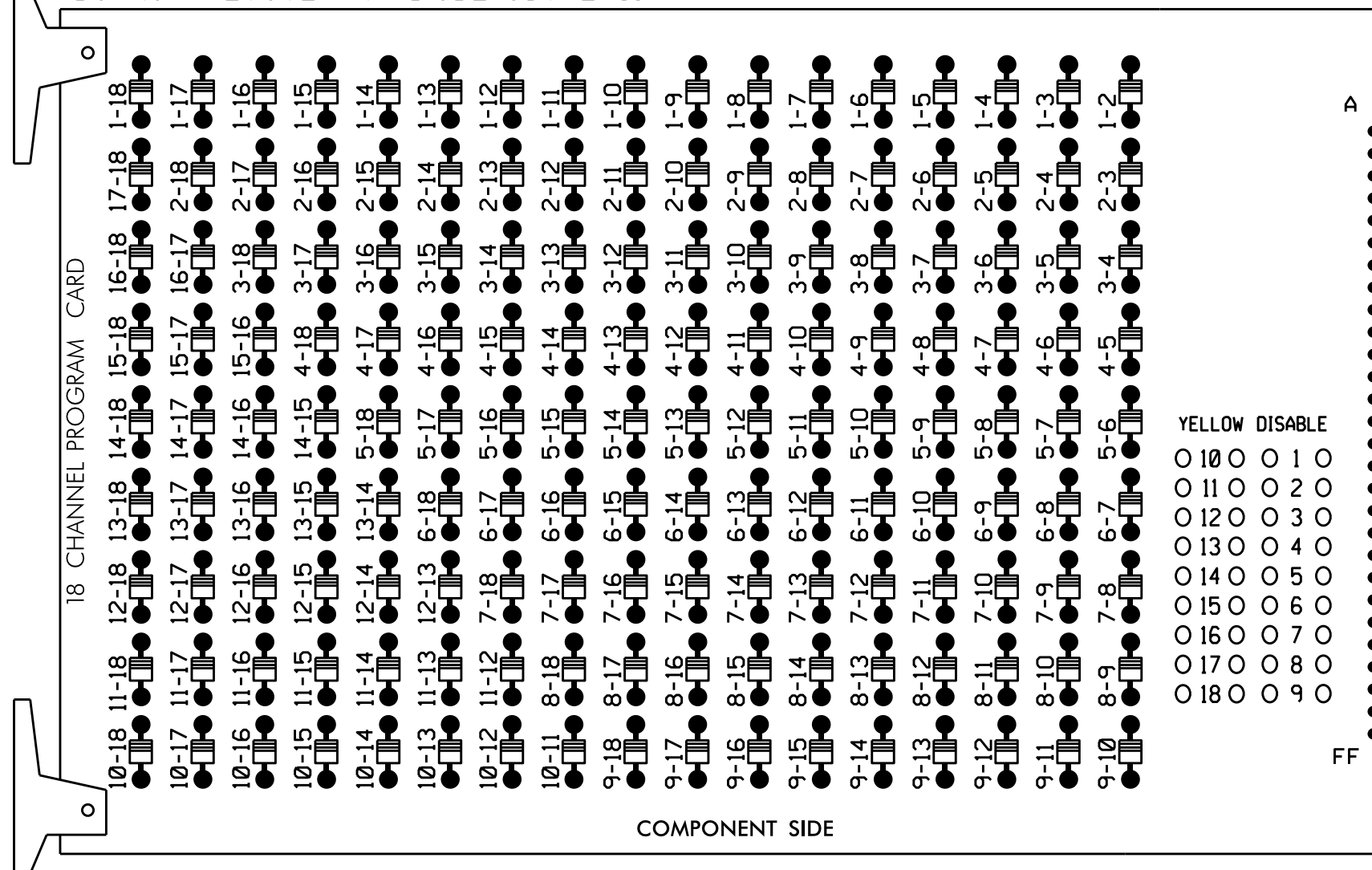


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

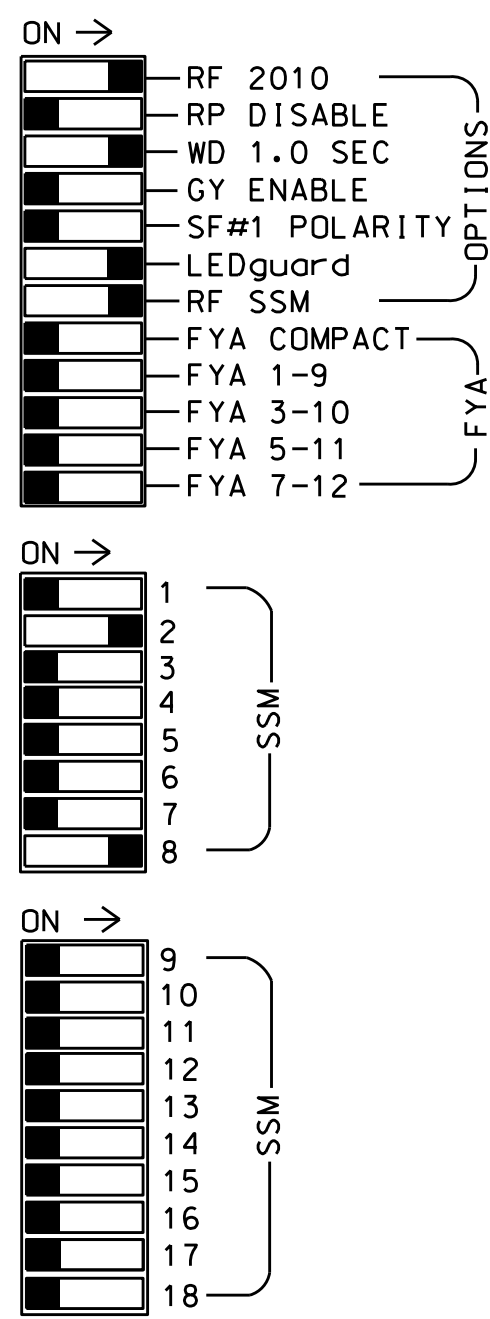
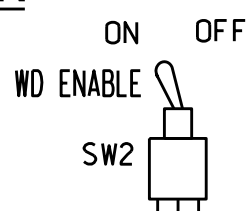
DO NOT REMOVE ANY DIODE JUMPERS.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 8 for Gap Reduction.
4. Program phase 2 for Start Up In Green.
5. Program phase 2 for Yellow Flash.
6. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
 CABINET.....336
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....POLE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S2,S11
 PHASES USED.....2,8
 OVERLAPS.....NONE

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	21,22 23	NU	NU	NU	NU	NU	NU	NU	NU	81,82 83	NU
RED		128									107	
YELLOW		129									108	
GREEN		130									109	
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

NU = Not Used

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 2	∅ 2	∅ 2	S	S	∅ 8	∅ 8	∅ 8	S	S	S	S	S	FS
I	2A	2C	2E	TOP	TOP	8A	8C	8E	TOP	TOP	TOP	TOP	TOP	DC ISOLATOR
L	∅ 2	∅ 2	∅ 2	TRIP	TRIP	∅ 8	∅ 8	∅ 8	TRIP	TRIP	TRIP	TRIP	TRIP	ST
	2B	2D	2F	TRIP	TRIP	8B	8D	8F	TRIP	TRIP	TRIP	TRIP	TRIP	DC ISOLATOR

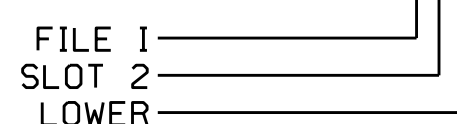
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB21-1,2	11U	56	18	1	2	Y	Y			
2B	TB23-1,2	11L	47	9	22	2	Y	Y			
2C	TB21-3,4	12U	39	1	2	2	Y	Y			
2D	TB23-3,4	12L	43	5	12	2	Y	Y	Y	2.0	5
2E	TB21-5,6	13U	58	20	3	2	Y	Y	Y	2.0	5
2F	TB23-5,6	13L	49	11	24	2	Y	Y	Y	2.0	5
8A	TB21-11,12	16U	40	2	6	8		Y			
8B	TB23-11,12	16L	44	6	16	8		Y			
8C	TB21-13,14	17U	57	19	7	8		Y			
8D	TB23-13,14	17L	50	12	28	8	Y	Y	Y	2.0	5
8E	TB22-1,2	18U	42	4	8	8	Y	Y	Y	2.0	5
8F	TB24-1,2	18L	46	8	18	8	Y	Y	Y	2.0	5

INPUT FILE POSITION LEGEND: I2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2089
 DESIGNED: July 2014
 SEALED: 4-10-15
 REVISED: N/A

Electrical Detail

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

I-85 Bus./US 29 NB-US 70 EB Ramp at I-74 WB-US 311 NB Ramp

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: JTR

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: John T. Rowe, Jr. 4/23/2015
 SEAL 008453
 JOHN T. ROWE, JR. ENGINEER
 SIG. INVENTORY NO. 07-2089

23-APR-2015 07:57
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 J. Peterson