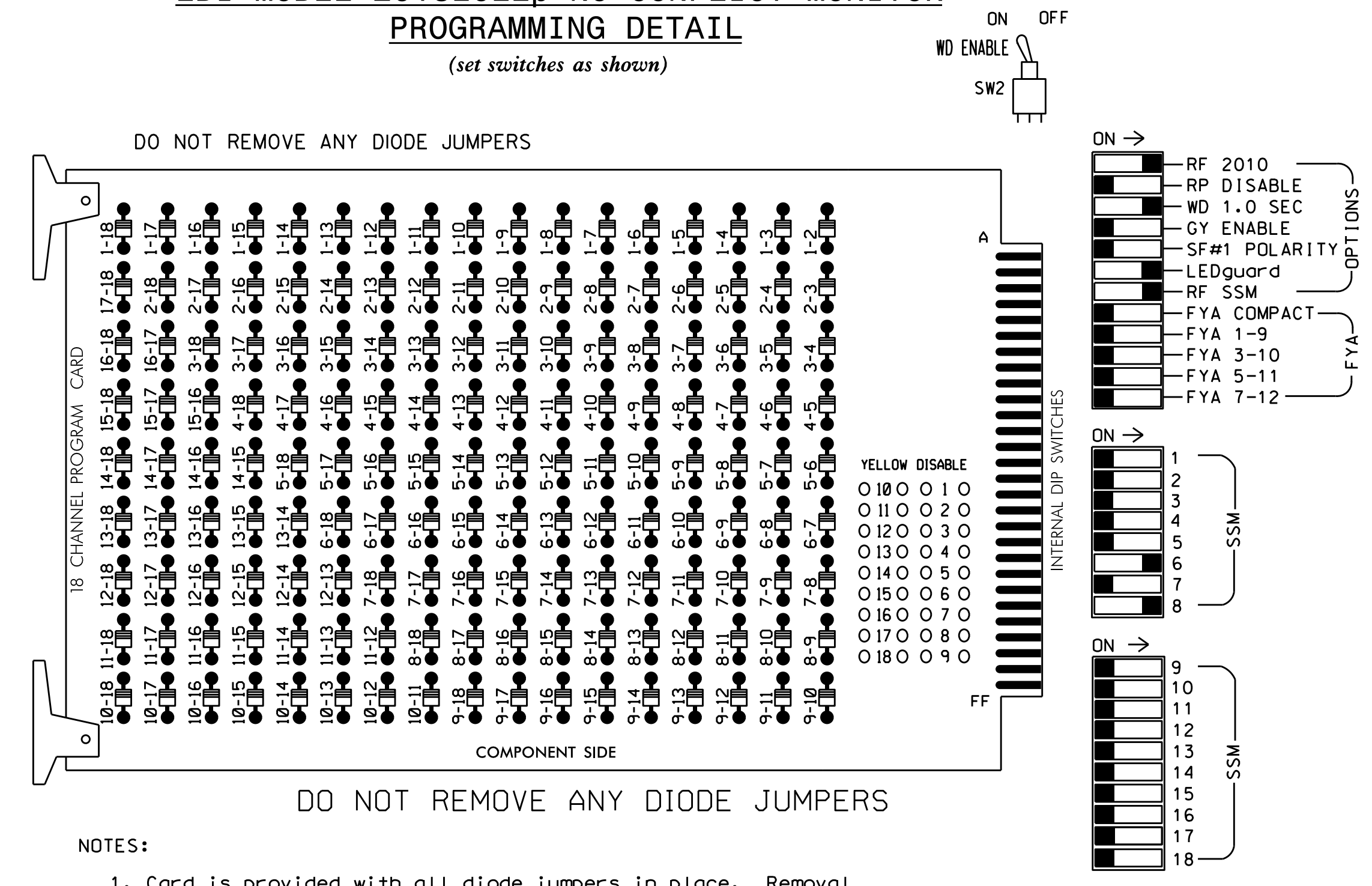


EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL
(set switches as shown)



- NOTES:**
1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 3. Ensure that Red Enable is active at all times during normal operation.
 4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phase 6 for Gap Reduction.
4. Program phase 6 for Start Up In Green.
5. Program phase 6 for Yellow Flash.
6. The cabinet and controller are part of the High Point Signal System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	NU	NU	NU	NU	NU	NU	61,62 63	NU	NU	81,82	NU
RED								134			107	
YELLOW								135			108	
GREEN								136			109	
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

NU = Not Used

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S8,S11
 PHASES USED.....6,8
 OVERLAPS.....NONE

INPUT FILE POSITION LAYOUT

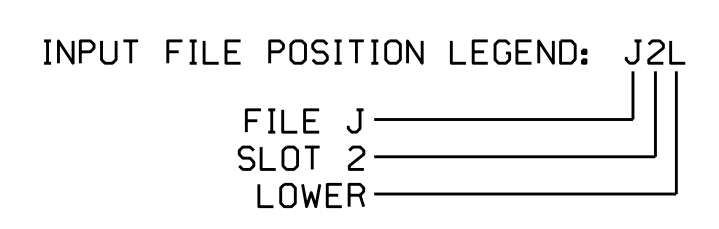
(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	S	∅ 6	∅ 6	S	S	S	S	S	S	S	S	S	S	FS
I	∅ 6	6A	6C	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	DC ISOLATOR
L	∅ 6	6B	6D	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	∅ 6	DC ISOLATOR
U	S	∅ 6	∅ 6	S	S	∅ 8	∅ 8	S	S	S	S	S	S	S
I	∅ 6	6E	6G	∅ 6	∅ 6	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8
L	∅ 6	6F	NOT USED	∅ 6	∅ 6	∅ 8	NOT USED	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8	∅ 8

EX. : 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
6A	TB2-5,6	I2U	39	1	2	6		Y			
6B	TB2-7,8	I2L	43	5	12	6		Y			
6C	TB2-9,10	I3U	63	25	32	6		Y			
6D	TB2-11,12	I3L	76	38	42	6	Y	Y	Y	2.0	5
6E	TB3-5,6	J2U	40	2	6	6	Y	Y	Y	2.0	5
6F	TB3-7,8	J2L	44	6	16	6	Y	Y	Y	2.0	10
6G	TB3-9,10	J3U	64	26	36	6	Y	Y	Y		15
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
8C	TB7-1,2	J7U	66	28	38	8	Y	Y			



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2009
 DESIGNED: July 2014
 SEALED: 4/10/15
 REVISED: N/A

Electrical Detail

ELECTRICAL AND PROGRAMMING DETAILS FOR: I-85 Bus./US 29 SB-US 70 WB Ramp at I-74 WB/US 311 NB Ramp

Prepared In the Offices of:

Division 7 Guilford County High Point

PLAN DATE: July 2014 REVIEWED BY: *STR*

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS: _____ INIT. DATE

DocuSigned by: *John T. Rowe, Jr.* 4/13/2015

750 N. Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 07-2009

13-APR-2015 07:55
 S:\IT\SSM\15_Signal\work\hgr\cds\g_Mon\Arms\stron\072009_sm_elec_xxx.dgn
 S:\IT\SSM\15_Signal\work\hgr\cds\g_Mon\Arms\stron