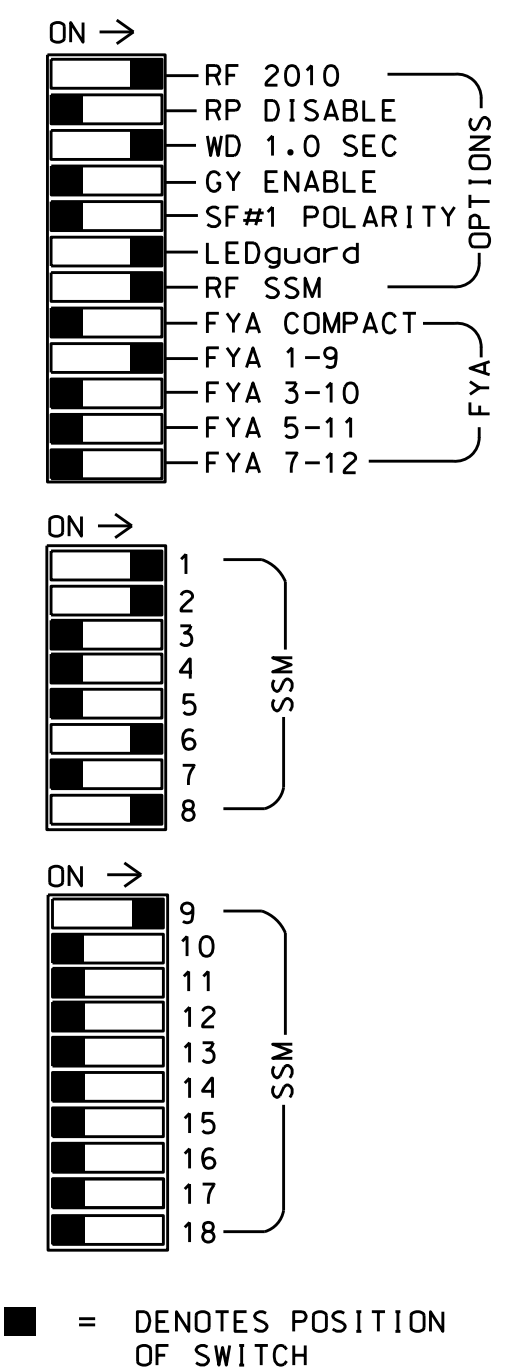
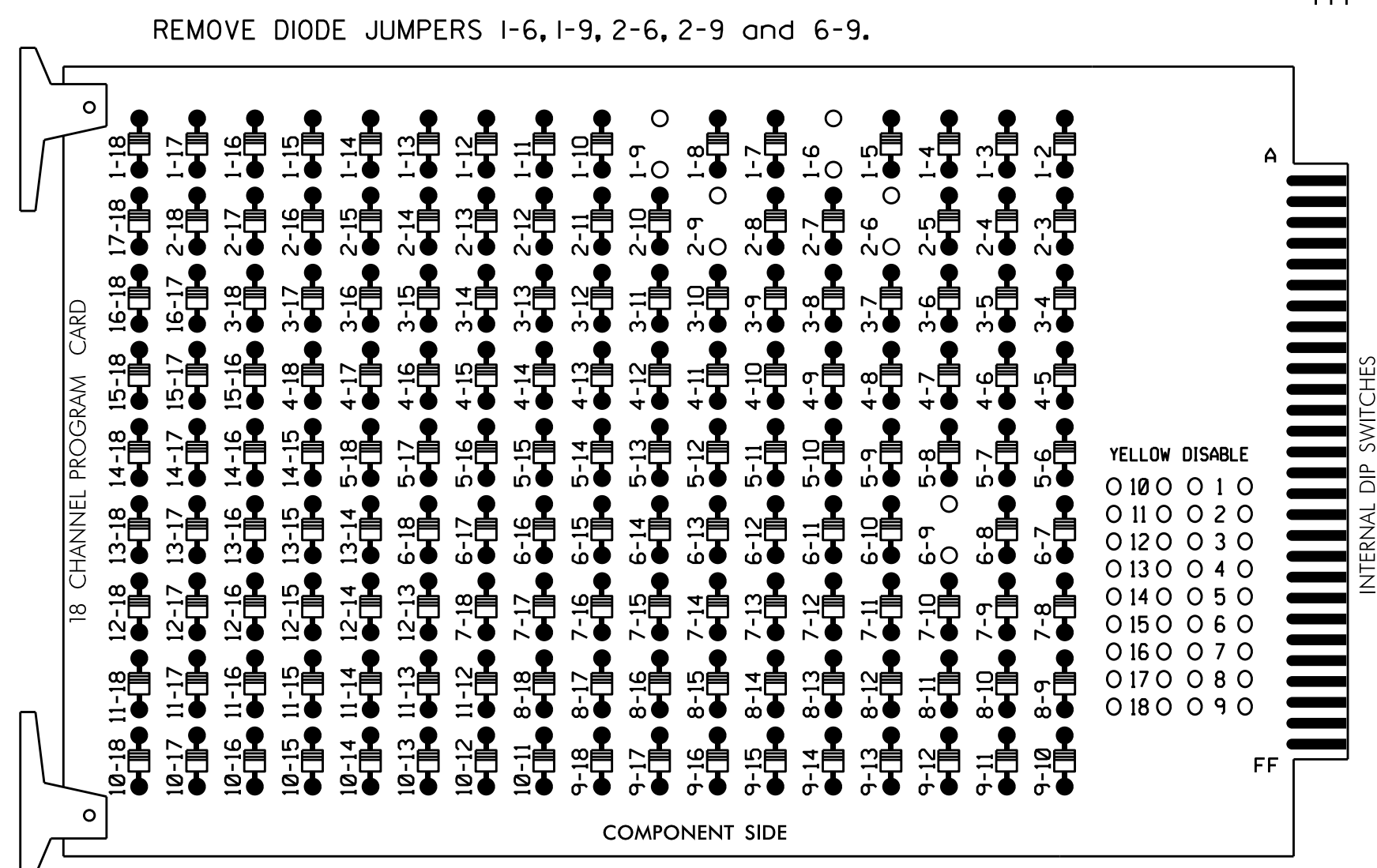


PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S1  
 PHASES USED.....1,2,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22	NU	NU	NU	NU	61,62	NU	NU	81,82	NU	11	NU	NU	NU	NU	NU
RED	*	128						134		107								
YELLOW		129						135		108								
GREEN		130						136		109								
RED ARROW													A121					
YELLOW ARROW		126												A122				
FLASHING YELLOW ARROW														A123				
GREEN ARROW	127	127																

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)

FILE	U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
"I"	U	∅ 1	∅ 2	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	FS
	L	NOT USED	NOT USED	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	DC ISOLATOR
"J"	U	-ORS	∅ 6	-ORS	-ORS	-ORS	∅ 8	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS	-ORS
	L	<-ORS	NOT USED	<-ORS	<-ORS	<-ORS	∅ 1	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	<-ORS	DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
1B	TB5-11,12	J6L	46	8	18	1	Y	Y			15
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3

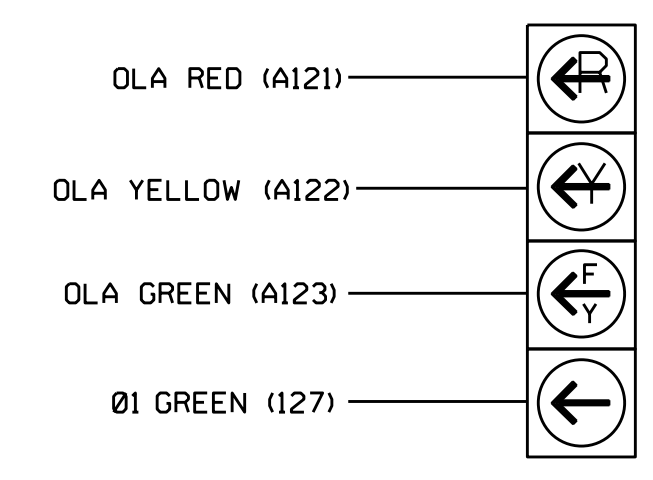
<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal head as shown)



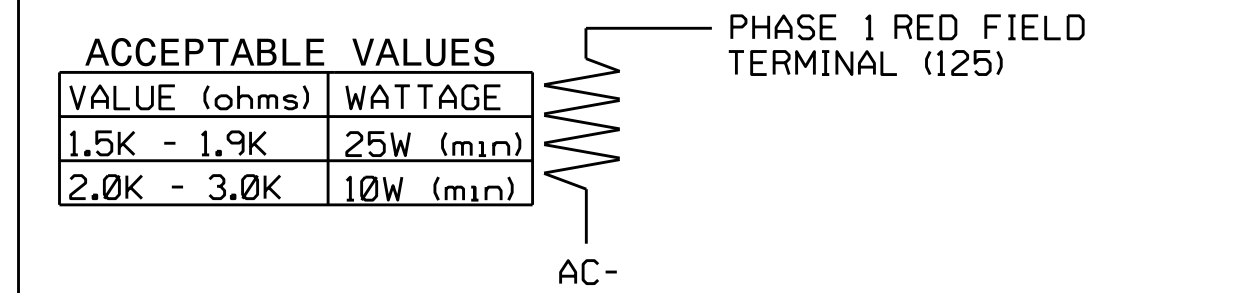
11

NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)



NOTE: The purpose of this resistor is to load the channel red monitor input in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1536 (Penny Road) at SR 1545 (East Fork Road)		SEAL GEORGE C. BROWN ENGINEER
	Division 7 PLAN DATE: July 2014 PREPARED BY: C. Strickland	Guilford County High Point REVIEWED BY: T. Joyce REVIEWED BY:	

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1853  
 DESIGNED: June 2014  
 SEALED: 3/13/2015  
 REVISED: N/A

26-1485-2015\_1E-53  
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