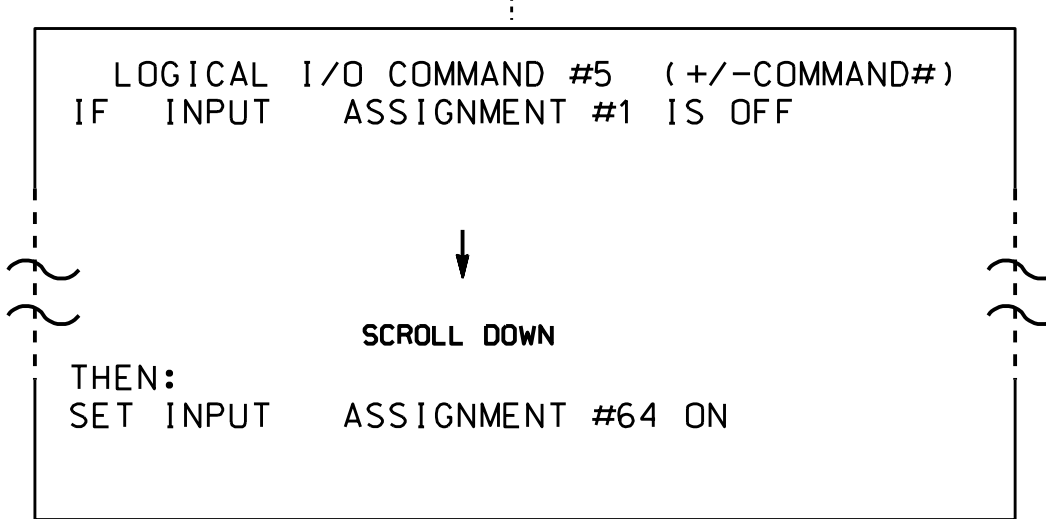
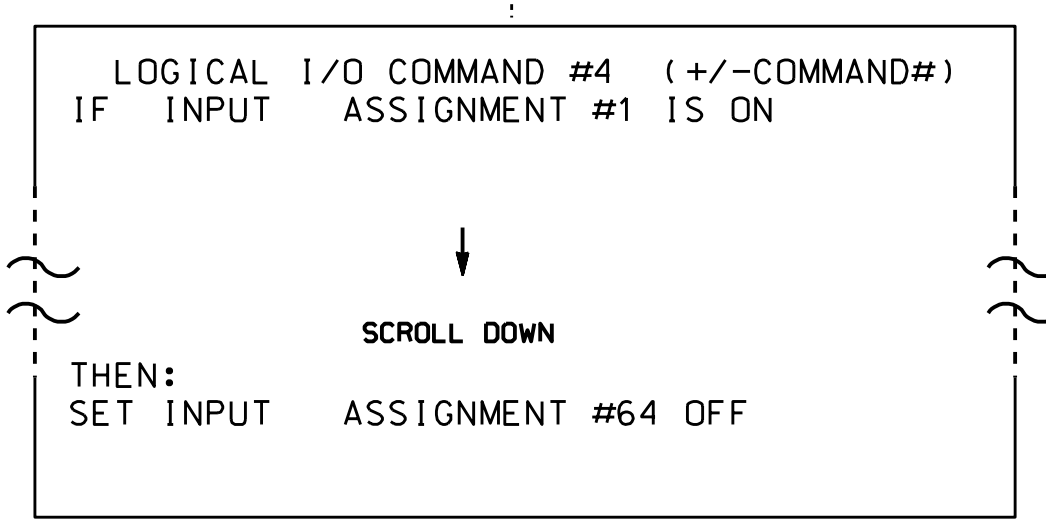
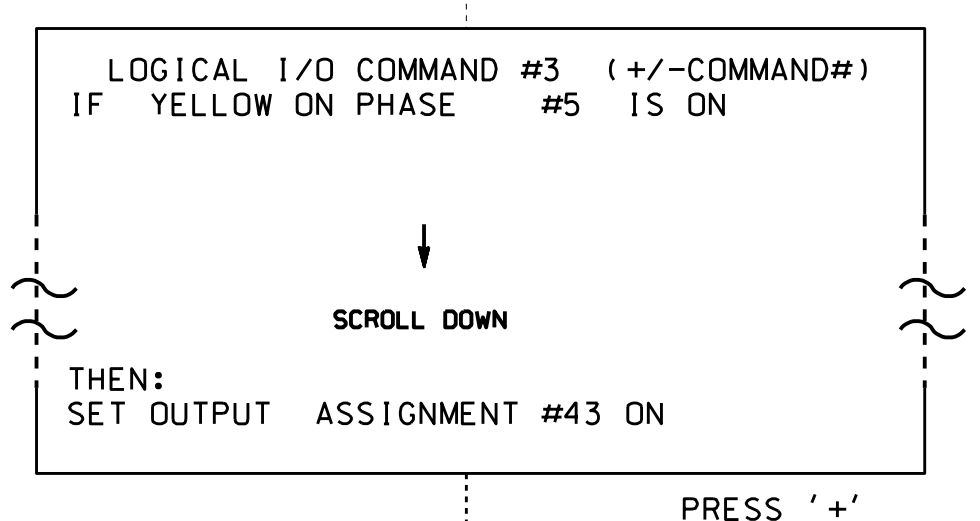
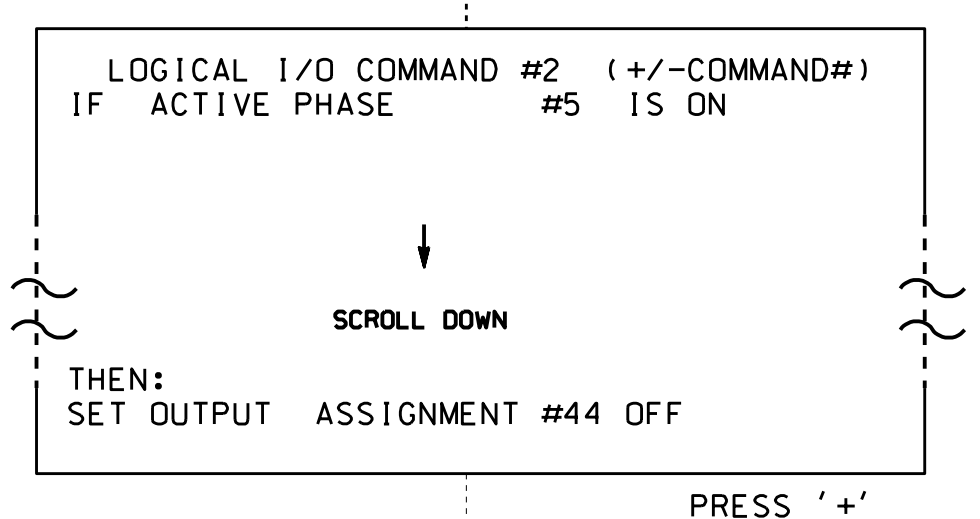
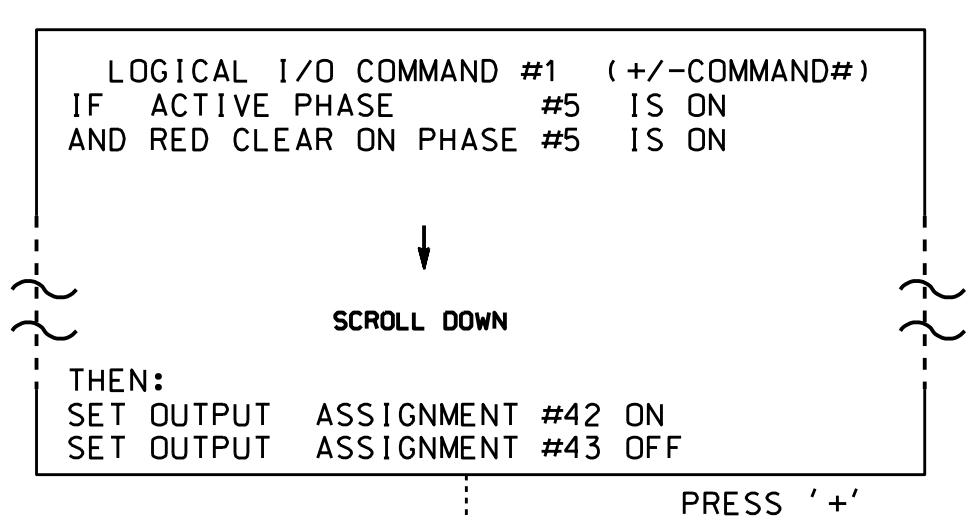


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE AND TO INVERT INPUT FROM MICROWAVE DETECTOR

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, and 5.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

REFERENCE SCHEDULE

- * INPUT 1 = Microwave Detector Physical Input (Not Enabled)
- * INPUT 64 = Dummy Microwave Detector Input (Detector 6)
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green

* Input Remapped (See Sheet 3 of 3).

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).


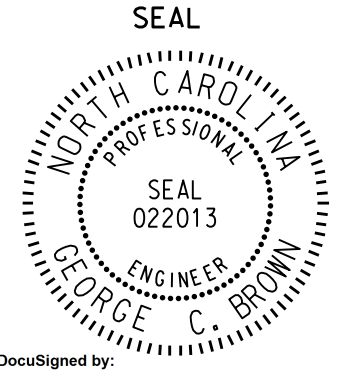
PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: XX
 VEH OVL NOT VEH:
 VEH OVL NOT PED:
 VEH OVL GRN EXT:
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN ← NOTICE GREEN FLASH
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0=255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3=25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1=25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1623
 DESIGNED: April 2014
 SEALED: 3/26/2015
 REVISED: N/A

Electrical Detail - Sheet 2 of 3

	ELECTRICAL AND PROGRAMMING DETAILS FOR: NC 68 (Eastchester Drive) at I-74 WB/US 311 NB Ramps		
	Division 7 PLAN DATE: June 2014 PREPARED BY: C. Strickland	G Guilford County High Point REVIEWED BY: T. Joyce REVIEWED BY:	

13-Jul-2014 09:38
 S:\ITS\ASIS\IS\Sig\Signal\workgroups\Sig_Mgmt\Strickland\071623_sme_elec_xxx.dgn
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